

# UNCONTROLLED DOCUMENT

INTERFACE PIN CONNECTION

LCT-H480272M43W1T PART NUMBER

REV.

58 A2 ANODE OF B. 59 K1 CATHODE OF 60 K2 CATHODE OF OF DEPTH CATHODE OF DE	B. CLK_TRG=L; DATA SAMPLING AT THE CLK RISING EDGE.  SHIFT DIRECTION SELECTION SIGNAL.  A. LR=H: S1-S2S720  B. LR=H: S720-S719S1  SCAN DIRECTION SELECTION SIGNAL INTERNALLY PULLED HIGH.  A. UD=H: G1-G2G544  B. UD=L: G544-G543G1  14 DE INPUT DATA ENABLE CONTROL. INTERNALLY PULLED HIGH.  15 VS VERTICAL SYNC INPUT WITH NEGATIVE POLARITY. INTERNALLY PULLED HIGH.  16 HS HORIZONITAL SYNC INPUT WITH NEGATIVE POLARITY. INTERNALLY PULLED HIGH.  17 DISP A. DISP=H, NORMAL DISPLAY MODE.  B. DISP=H, NORMAL DISPLAY MODE.  CONTROL SIGNAL FOR DATA LATCHING AND INTERNALLY PULLED HIGH.  18~25 CLK CONTROLLER.  CONTROLLER.  CONTROLLER.  CONTROLLER.  SCAN DIRECTION SELECTION SIGNAL INTERNALLY PULLED HIGH.  A DISP=L STANDBY MODE.  B. DISP=H, NORMAL DISPLAY MODE.  CONTROLLER.  CONTROLLER.  CONTROLLER.  ACTIVE LOW GLOBAL RESET SIGNAL INPUT. INTERNALLY PULLED HIGH.  POLARITY SIGNAL TO MONITOR VCOM SIGNAL.  INPUT PIN TO ENABLE INTERNAL CHARGE PUMP VCL,VGH,  CONNECT TO VDDIO TO ENABLE INTERNAL CHARGE PUMP VCL,VGH,  CONNECT TO DVSS TO DISABLE INTERNAL CHARGE PUMP VCL,VGH,  CONNECT TO DVSS TO DISABLE INTERNAL CHARGE PUMP VCL,VGH,  CONNECT TO DVSS TO DISABLE INTERNAL CHARGE PUMP VCL,VGH,
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH.	
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF HIGH. LED HIGH. 1. PULLED HIGH.	
58 A2 ANODE OF BR 59 K1 CATHODE OF 60 K2 CATHODE OF 0 HIGH. LED HIGH. THE TIMING THE TIMING 1.	
S8 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH. SH.  SH.  SH.  SH.  SH.  SH.  SH.  S	
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH. ER OF THE TIMING ER OF THE TIMING	
S8 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH. SH.  ER OF THE TIMING	
S8 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH. SH.  ER OF THE TIMING	· · · · · · · · · · · · · · · · · · ·
S8 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH. BR OF THE TIMING	·
S8 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH. SH.  ER OF THE TIMING	
58 A2 ANODE OF BE 59 K1 CATHODE OF GO K2 CATHODE OF DELECT HIGH.  EP OF THE TIMING	
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH.	
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH. LY PULLED HIGH.	
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH.	B. CLK_IRG=L; DATA SAMPLING AT THE CLK SHIFT DIRECTION SELECTION SIGNAL.  A. LR=H: S1-S2S720  B. LR=H: S720-S719S1  SCAN DIRECTION SELECTION SIGNAL. INTERNALL:  SCAN DIRECTION SELECTION SIGNAL. INTERNALL:  A. UD=H: G1-G2G544  B. UD=L: G544-G543G1  INPUT DATA ENABLE CONTROL. INTERNALLY PUL  VERTICAL SYNC INPUT WITH NEGATIVE POLARITY  HORIZONITAL SYNC INPUT WITH NEGATIVE POLARITY
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF PULLED HIGH.	B. CLK_IRG=L; DATA SAMPLING AT THE CLK RESIDENT DIRECTION SELECTION SIGNAL.  A. LR=H: S1-S2S720  B. LR=H: S720-S719S1  SCAN DIRECTION SELECTION SIGNAL. INTERNALL SCAN DIRECTION SELECTION SIGNAL. INTERNALL A. UD=H: G1-G2G544  B. UD=L: G544-G543G1 INPUT DATA ENABLE CONTROL. INTERNALLY PULL VERTICAL SYNC INPUT WITH NEGATIVE POLARITY
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF 3H.	B. CLK_IRG=L; DATA SAMPLING AT THE CLK SHIFT DIRECTION SELECTION SIGNAL.  A. LR=H: S1-S2S720  B. LR=H: S720-S719S1  SCAN DIRECTION SELECTION SIGNAL. INTERNALL: A. UD=H: G1-G2G544  B. UD=L: G544-G543G1  INPUT DATA ENABLE CONTROL. INTERNALLY PUL
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF 3H.	B. CLK_TRG=L; DATA SAMPLING AT THE CLK R SHIFT DIRECTION SELECTION SIGNAL. A. LR=H: S1-S2S720 B. LR=H: S720-S719S1 SCAN DIRECTION SELECTION SIGNAL. INTERNALL: A. UD=H: G1-G2G544 A. UD=L: G544-G543G1
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF 5H.	B. CLK_TRG=L; DATA SAMPLING AT THE CLK R SHIFT DIRECTION SELECTION SIGNAL. A. LR=H: S1-S2S720 B. LR=H: S720-S719S1 SCAN DIRECTION SELECTION SIGNAL. INTERNALL: A. UD=H: G1-G2G544
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF 3H.	B. CLK_TRG=L; DATA SAMPLING AT THE CLK R SHIFT DIRECTION SELECTION SIGNAL. A. LR=H: S1-S2S720 B. LR=H: S720-S719S1 SCAN DIRECTION SELECTION SIGNAL. INTERNALL
58 A2 ANODE OF B 59 K1 CATHODE OF 60 K2 CATHODE OF	B. CLK_TRG=L; DATA SAMPLING AT THE CLK R SHIFT DIRECTION SELECTION SIGNAL. A. LR=H: S1-S2S720 B. LR=H: S720-S719S1
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58 A2 ANODE OF BA 59 K1 CATHODE OF	B. CLK_TRG=L; DATA SAMPLING AT THE CLK R SHIFT DIRECTION SELECTION SIGNAL.
58 A2 ANODE OF B	B. CLK_TRG=L; DATA SAMPLING AT THE CLK R
	A OUT TO THE OWNER OF THE OUT OF
57 A1	
56 3-NC	CLK TRC INTERNALLY PULLED HIGH.
53~55  GND  F	CLOCK EDGE SELECTION SIGNAL FOR THE DATA
52 VCOM THIS IS OUTF	
V   V   U   C   C   C   C   C   C   C   C   C	A. PS=H: PARALLEL RGB
ת מי	INDIT DATA FORMAT SELECT SICNAL INTERNALL
-	CHIP SELECT PIN OF SERIAL INTERFACE. INTER
50 DRV1 POWER TRANS	
VFB1 DEFAULT THRESHOLD IS 1.0V.	DATA INPUT PIN IN SERIAL MODE.
49 VFB1 DRIVER TO GI	SDO DATA OUTPUT PIN IN SERIAL MODE.
. MAIN BOOST	DUTY CYCLE CONTROL SIGNAL OF CABC
PANEL. 48   URVI   1 PWM CAN BE USED FOR LED BACKLIGHT	VCOM THIS OUTPUT PIN FOR COMMON SIGNAL OF A TFT
POWER TRANS	GND POWER GROUND.
VC1 BOOSTER INPUT VOLTAGE	NO CONNECT.
VDDIO VOLTAGE INPUT PIN FOR I/O	NO CONNECT.
PIN SYMBOL FUNCTION	SYMBOL FUNCTION

RELUBLITY NOTE
OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT
SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FITURE FAILURE.
PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS. DRAWN B ::

2:00 VIEW, LED BACKLIGHT,

4.3"

ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL

-20°C TO +70°C OPERATING TEMP

LCT-H480272M43W1T

PART NUMBER

REV.

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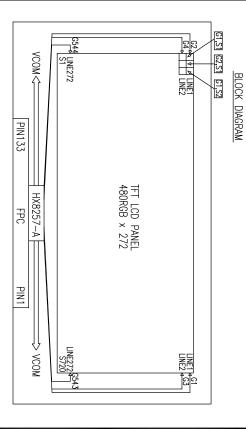
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ELECTRICAL CHARACTERISTICS

																				7.6		_	S
III./I IH	102	힏	로	<u>≅</u> 1	¥H1	VOL1	SOV	YDD	VOH1	VLCD	COML	COMH	\C	2	Š	VDC	VCIX2	\CL	무	ISLEEP	VCI	VDDIO	SYMBOL
LOGIC INTPUT CURRENT	LOGIC OUTPUT TRI-STATE CURRENT DRAIN SOURCE	LOGIC HIGH OUTPUT CURRENT DRAIN	LOGIC HIGH OUTPUT CURRENT SOURCE	LOGIC LOW INPUT VOLTAGE	LOGIC HIGH INPUT VOLTAGE	LOGIC LOW OUTPUT VOLTAGE	SOURCE OUTPUT VOLTAGE DEVIATION	SOURCE OUTPUT VOLTAGE DEVIATION	LOGIC HIGH OUTPUT VOLTAGE	VLCD OUTPUT VOLTAGE	VCOM LOW OUTPUT VOLTAGE $(3)$	VCOM HIGH OUTPUT VOLTAGE (3)	GATE DRIVER LOW OUTPUT VOLTAGE	EFFICIENCY(2)	GATE DRIVER HIGH OUTPUT	VDC OUTPUT VOLTAGE	VCIX2 PRIMARY BOOSTER EFFICIENCY(2)	NEGATIVE VCI OUTPUT VOLTAGE	OPERATING MODE CURRENT	SLEEP MODE CURRENT	BOOSTER REFERENCE SUPPLY VOLTAGE RANGE	POWER SUPPLY PIN OF IO PINS	CONDITION
ı	ı	V 0UT= 0.4V	V OUT= VDD -0.4V	ı	ı	1 OUT= 100uA	ı	ı	OUT= −100uA	VRH[5:0]=100100	1	ı	$VGL = -2 \times VDC$	NO PANEL LOADING; 3X BOOSTER	NO PANEL LOADING; 3X BOOSTER	VDC[3:0]=1011	NO PANEL LOADING,ITO FOR VCIX2, VCI	NO PANEL LOADING	VCI=3.3V	1	RECOMENNDED OPERATING VOLTAGE POSSIBLE OPERATING VOLTAGE	RECOMENNDED OPERATING VOLTAGE POSSIBLE OPERATING VOLTAGE	CONDITION
	<u> </u>	ı	50	0	0.9*VDDIO	0	ı	ı	0.9*VDDIO	4.41	-3%	-3%	-10	80	84	4.9	_	-VCI	ı	1	>VDD10 &>3	1.8	M Z
ı	ı	ı	1	1	1	ı	ı	<b>₽</b>	-	4.51	COMC —COMPP	+COMPP	-10	88.5	89.5	5	Ι	ı	13	50	ı	ı	ΤΥP.
_	_	-50	ı	0.2*VDDIO	VDDIO	0.1*VDDIO	±30	±30	VDD	4.61	3%	3%	-9	1	ı	5.1	-	-VCI+0.7	15		3.6	3.6	MAX
uA	uA	uA	uA	<	<	<	m۷	m۷	<	٧	<b>V</b>	<	<	%	%	<	-	<	mA	uA	<	<	UNIT

> LCT-H480272M43W1T PART NUMBER

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ABSOLUTE MAXIMUM RATINGS			
ITEM	TOBMAS	UNIT	ANTAA
POWER SUPPLY VOLTAGE (1)	VDD	٧	-0.3 TO +0.8
POWER SUPPLY VOLTAGE (2)	VDC	٧	-0.3 TO +0.8
POWER SUPPLY VOLTAGE (3)	VGH-VGL	٧	-0.3 TO +45
POWER SUPPLY VOLTAGE (4)	VDD-VCL	٧	-0.3 TO +10
OPERATING TEMPERATURE	TOP	°C	-20~70
STORAGE TEMPERATURE	TST	Ċ	-30~80



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2:00 VIEW, LED 4.3" ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL BACKLIGHT, -20°C 7 +70°C OPERATING TEMP

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# LCT-H480272M43W1T

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\_INEARITY TOUCH SCREEN PANEL SPECIFICATIONS -1.5 ≤ Z ₹ . ELECTRICAL CHARASTERISTICS MA X <u>.</u>5 S mΑ ANALOG X AND Y DIRECTIONS

TERMINAL

100

Y (FILM SIDE)

RESISTANCE 100 \( \Omega\) X (FILM SIDE)  INSULATION RESISTANCE 25 \( \omega\) \( \Omega\) DC25V  VOLTAGE 7 V DC  CHARTERING 7 V DC  TRANSPARENCY - 80 - \( \omega\) NON-GLARE  CAUCTION: DO NOT OPERATE IT WITH ANY THING EXCEPT A POLYACETAL PEN (TIP RO.8MM OR A ESS) OR A FINGER, ESPECIALLY THOSE WITH HARD OR SHARP TIPS AS A BALL PEN OR A			
INSULATION RESISTANCE  VOLTAGE  CHARTERING  TRANSPARENCY  CAUCTION: DO NOT OPERATE IT WITH ANY THING E LESS) OR A FINGER, ESPECIALLY THOSE WITH HARI	_	Ω	X (FILM SIDE)
VOLTAGE  CHARTERING  TRANSPARENCY  CAUCTION: DO NOT OPERATE IT WITH ANY THING E LESS) OR A FINGER, ESPECIALLY THOSE WITH HARI	_	MΩ	DC25V
CHARTERING 80  TRANSPARENCY - 80  CAUCTION: DO NOT OPERATE IT WITH ANY THING E LESS) OR A FINGER, ESPECIALLY THOSE WITH HARI	7	٧	DC
TRANSPARENCY   -   80  CAUCTION: DO NOT OPERATE IT WITH ANY THING E LESS) OR A FINGER, ESPECIALLY THOSE WITH HARI	10	ms	100K <b>Ω</b> PULL-UP
CAUCTION: DO NOT OPERATE IT WITH ANY THING E LESS) OR A FINGER, ESPECIALLY THOSE WITH HARI	1	%	NON-GLARE
LESS) OR A FINGER, ESPECIALLY THOSE WITH HARI	CEPT A F	OLYACE	TAL PEN (TIP RO.8MM OR
	OR SHAF	RP TIPS	AS A BALL PEN OR A

MECHANICAL PENCIL.

ITEM	M N	TYP.	MAX	UNIT	NOTE
ACTIVATION FORCE	80	1	100	9	(A)
DURABILITY-SURFACE SCRATCHING 100000	100000	1	1	CHARACTERS	(B)
DURABILITY-SURFACE PITTING	1000000	ı	-	TOUCHS	(C)
SURFACE HARDNESS	3	I	ı	Τ	

- (A) STYLUS PEN INPUT: R 0.8mm POLYACETAL OR FINGER
- (B) MEASURMENT FOR SURFACE AREA
- 20000 TIMES - SCRATCH 100000 TIMES STRAIGHT LINE ON THE FILM WITHA A STYLUS CHANGE EVERY

HALF LUMMINANCE)

LUMINANCE (TILL UNIFORMITY

> $\triangleright$ 7

85% 2400

2600

3000

cd/m²

MIN/MAX\*100%

Vr=10V

0.276 0.282

0.330 0.320

റ് റ് 30

MA

T=25°C

CONDITIONS

20000

50000

HOURS  $| \text{If}=10\text{mA}, \text{Lv}=2200\text{cd/m}^2$ HOURS If=15mA, Lv=2600cd/m² \_UMINANCE

CHROMATICITY COORDINATES

FORWARD CURRENT

- FORCE: 250gfSPEED: 60mm/SECSTYLUS: R 0.8 POLYACETAL TRIP
- (C) PIT 1000000 TIMES ON THE FILM WITH A R8.0 SILICON RUBBER
- FORCE: 250gf
- SPEED: 2 TIMES/SEC



FORWARD VOLTAGE STORAGE TEMPERATURE RANGE ABSOLUTE MEM OPERATING TEMPERATURE RANGE REVERSE VOLTAGE BACKLIGHT SPECIFICATIONS BACKLIGHT SPECIFICATIONS MAX FORWARD CURRENT Ifm (PER STRING LED BACKLIGHT ABSOLUTE MAXIMUM RATINGS (Ta=25°C) LED BACKLIGHT ELECTRO OPTICAL CHARACTERISTICS SYMBOL ≤ SYMB0L TOPR STST \$ ≤ Z -30-20≦  $\sigma$ STANDARD VALUE 13.0 ₹ ≓ . ı ı 1 MA X ı MAX 80 70 20 ı S < S MΑ റ് റ് <

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PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

2:00 VIEW, LED

BACKLIGHT,

-20°C

TO +70°C OPERATING TEMP

4.3"

ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL

LCT—H480272M43W1T

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OPTICAL CHARASTERISTICS

OPTIMUM VIEWING DIRECTION			REFERENCE ONLY	"SIMULATION DATA			(CIE1931)	COLOR CROMACITY	TRANSMISIVE RATE	LUMINANCE OF WHITE (CENTER POINT OF LCM)		(CR <b>2</b> IU)	VIEWING ANGLE		CONTRAST RATIO	KESFUNSE IIME		II LIVI	ITEM
RECTION	WIIIL	WLITE	חרטר		GIVEEIN	CDEEN	וירט	0		TE LCM)					)		'		
	Wy	W×	Ву	Bx	Gy	Gx	Ry	R <sub>x</sub>	1%	_	LOWER	UPPER	LEFT	RIGHT	CR	Tf	Tr	טוואוטטב	CAMPUI
				4						ı	<b>♠</b> 270°C	<b>0</b> °06 <b>=</b>	<b>¢</b> ±180°C	<b>⊕</b> 0°C	<b>25°</b> C	4	<b>P</b>	CONDITION	CONDITION
12 0'CLOCK	0.319	0.281	0.119	0.103	0.533	0.276	0.314	0.590	1	200	1	_	_	1		_	1	MIN	AIS
웃	0.349	0.311	0.149	0.133	0.563	0.306	0.344	0.620	6.1	250	35	15	45	45	250	15	5	TYP.	STANDARD VALUE
	0.379	0.341	0.179	0.163	0.593	0.336	0.374	0.650	ı	1	ı	1	ı	ı	1	1	1	MAX	TUE
	_	_	_	_	ı	_	_	ı	%	Cd/m²	DEG	DEG	DEG	DEG	<	٧	<	ONII	TIINIT
I							NISC=50%	4		5				3	2			NOIL	NOTE

NOTE(1): DEFINITION OF RESPONSE TIME

OPTICAL RESPONSE DISPLAY DATA 100% 206 10% 0% WHITE  $\widehat{\exists}$ OFF)  $\frac{\frac{1}{2}}{2}$ BLACK (TFT ON) <u>=</u> WHITE  $\widehat{\Xi}$ . 0FF) ĬME

 $\underline{\text{NOIE}(2)}$ : DEFINITION OF CONTRAST RATIO CR=BRIGHTNESS AT ALL PIXELS "BLACK"

NOTE(4): MEASURED AT CENTER POINT VERTICALLY WITH BACKLIGHT ON.

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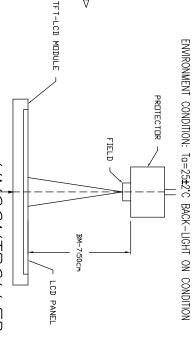
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**=**180°C NOTE(3): DEFINITION OF VIEWING ANGLE 0 6' D'CLDCK ₱270°C 12' D'CLOCK **90°**C

NOTE(5): AFTER STABILIZING AND LEAVING THE PANEL ALONE AT GIVEN TEMPERATURE FOR 30MIN, THE MEASUREMENT SHOULD BE EXECUTED. MEASURMENT SHOULD BE EXECUTED IN STABLE, WINDLESS, AND DARK ROOM 30 MINS AFTER LIGHTING THE BACK-LIGHT. THIS SHOULD BE MEASURED IN THE CENTER OF SCREEN.



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ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL TO +70°C OPERATING TEMP

2:00 VIEW, LED BACKLIGHT,

-20°C

4.3"

REV.

\_CT-H480272M43W1T PART NUMBER

'n

DRAWN

## STANDARD SPECIFICATION FOR REABILITY

## STANDARD SPECIFICATION OF REABILITY TEST

]	7		6	5	4	3	2	_	8
	DROP TEST		TEMPERATURE CYCLE	HIGH TEMPERATURE/ HUMIDITY OPERATION	LOW TEMPERATURE OPERATION	HIGH TEMPERATURE OPERATION	LOW TEMPERATURE STORAGE	HIGH TEMPERATURE STORAGE	TEST ITEM
	ENDURANCE TEST APPLYING THE DROP DURING TRANSPORTATION.	MECHANICAL TEST	ENDURANCE TEST APPLYING THE LOW AND HIGH TEMPERATURE CYCLE.  -20°C  -25°C  -20°C  5 MIN  1 CYCLE  AND AND HIGH THE LOW AND HI	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND TEMPERATURE / HUMIDITY STRESS TO THE ELEMENT FOR A LONG TIME.	ENDURANCE TEST APPLYING THE ELECTRIC STRESS UNDER LOW TEMPERATURE FOR A LONG TIME.	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND THE THERMAL STRESS 70+/-3°C 240HRS TO THE ELEMENT FOR A LONG TIME.	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	CONTENT OF TEST
•	PACKED,100cm FREE FALL(6 SLIDES, 1 CORNER, 3 EDGES)		-20°C/ 70°C 10 CYCLES	40°C, 90%RH 120HRS	-20+/-3°C 240HRS	70+/-3°C 240HRS	-30+/-3°C 240HRS	80+/-3°C 240HRS	TEST CONDITION
			 	MIL-202E-103B JIS-C5023	1 1 1			 	APPLICABLE STANDARD

- 1. FOR OPERATION TEST, ABOVE SPECIFICATION IS APPLICABLE WHEN TEST PATTERN IS CHANGING DURING ENTIRE OPERATION TEST.
  2. INSPECTIONS AFTER RELIABILITY TESTS ARE PERFORMED WHEN THE DISPLAY TEMPERATURE RESUMES BACK TO ROOM TEMPERATURE.
  3. IT IS A NORMAL CHARACTERISTIC THAT SOME DISPLAY ABNORMALITY CAN BE SEEN DURING REABILITY TEST. IF THE DISPLAY ABNORMALITY CAN RESUME BACK TO NORMAL CONDITION AT ROOM TEMPERATURE WITHIN 24 HOURS, THERE IS NO PERMANENT DESTRUCTION OVER THE DISPLAY. THE DISPLAY STILL POSSESSES ITS FUNCTIONALITY AFTER REABILITY TESTS.



\*UNLESS OTHERWISE SPECIFIED TOLEFANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN=+DECIMAL PRECISION

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RELUBLITY NOTE
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SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE.
PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

2:00 VIEW, LED BACKLIGHT,

4.3"

ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL

-20°C TO +70°C OPERATING TEMP

LCT-H480272M43W1T

PART NUMBER

REV.

DRAWN BY: CHECKED

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QUALITY ASSURANCE

ACCEPTABLE QUALITY LEVEL (AQL)

EACH LOT SHOULD SATISFY THE QUALITY LEVEL DEFINED AS FOLLOWS:

A. INSPECTION METHOD: MIL-SDT-105E LEVEL II NORMAL ONE TIME SAMPLING. B. AQL LEVEL.

MINOR		MAJOR	CATEGORY	
1.00%		0.25%	AQL	
1.00% STANDARD.	SATIFY ALL FLINCTIONS AS DROBLET BLIT NOT SATISFY COSMETIC	0.25% FUNCTIONAL DEFECTIVE AS PRODUCT.	DEFINITION	

### COSMETIC SCREENING CRITERIA

Dis	6 97	NC	33		4 F	DE	3 AL		<u></u>	2 BL	SC	DC		(7	-	1 /	SP	8
DISPLAY	OR MISSING	NO DISPLAY	CONDITION	DISPLAY	RAINBOW	DENSITY	ALLOWABLE		(LINE TYPE)	BLACK STREAK	SCRATCHES/	DUST/		(RUUND TYPE)	מטשטרר, / פחשטרר		SPOTS/DUST	DEFECT
NOT ACCEPTABLE.	REQUIRED. NO	THE PATTERNS	SHORT CIRCUIT	DIM DISPLAY ON	NOTICEABLE.	5mm EACH OTHER.	ABOVE DEFECTS	W>0.05	W <b>≤</b> 0.05	W <b>≤</b> 0.03	<b>W≤</b> 0.02	WIDTH, W(mm)		D>0.20	0.15 <b>&lt;</b> D <b>≤</b> 0.20	D <b>≤</b> 0.15	3	
į	REQUIRED. NO DISPLAY OR MISSING DISPLAY ARE	THE PATTERNS OF DISPLAY SHALL LIGHT UP AS	SHORT CIRCUIT ARE NOT ACCEPTABLE	DIM DISPLAY ON THE PATTERNS, EXTRA PATTERN AND	NOTICEABLE:	京.	ABOVE DEFECTS SHOULD BE SEPARATED MORE THAN	DISREGARD	L ≤ 2.0	L <b>s</b> 1.0	DISREGARD	WIDTH, W(mm) LENGTH, L(mm) IN ACTIVE AREA	ACC	0	3	DISREGARD	ACCEPTABLE QUANTITY IN	JUDGMENT CRITERIA
	DISPLAY ARE	_IGHT_UP_AS	įμ	TRA PATTERN AND	SHALL NOT BE		ATED MORE THAN	0	3	DISREGARD	DISREGARD	CTIVE AREA	ACCEPTABLE QUANTITY			ARD	Y IN ACTIVE AREA	RIA
	MAJOR		MAJOR	5	MINOR	MINON	MINOR DR			MINOR					MINOR			CATEGORY

NOTE: D= (LONG LENGTH + SORTH LENGTH)/2

### FAILURE JUDGMENT CRITERIA

AFTER REABILITY TEST ABOVE, TEST SAMPLE SHALL BE LET RUN TO ROOM TEMPERATURE AND HUMIDITY AT LEAST 4 HOURS BEFORE FINAL TESTS ARE CARRIED OUT.

OUT OF APPERANCE STANDARD.	OPTICAL CHARACTERISTIC
MECHANICAL CHARACTERISTIC OUT OF MECHANICAL SPECIFICATION.	MECHANICAL CHARACTERISTIC
ELECTRICAL CHARACTERISTIC  ELECTRICAL SHORT AND OPEN.	ELECTRICAL CHARACTERISTIC
FAILURE JUDGMENT CRITERIA	CRITERION ITEM

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4.3"

ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL

-20°C TO +70°C OPERATING TEMP

LCT-H480272M43W1T

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PRECISION

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### HANDLING PRECAUTIONS

- 1, THE DISPLAY PANEL IS MADE OF GLASS AND POLARIZER. DO NOT SUBJECT IT TO MECHANICAL SHOCK BY DROPPING OR IMPACT WHICH MAY CAUSE CHIPPING ESPECIALLY ON THE EDGES.
- 2. DO NOT TOUCH, PUSH OR RUB THE EXPOSED POLARIZERS WITH ANYTHING HARDER THAN AN HB PENCIL (GLASS,TWEEZERS, ETC.). THE POLARIZER COVERING THE DISPLAY SURFACE OF THE LCD MODULE IS
- POLARIZER SURFACE. ALCOHOL. AVOID USING SOLVENTS LIKE ACETONE (KETENE), WATER, TOLUENE, ETHANOL TO CLEAN THE A SOFT DRY CLOTH. IF IT IS HEAVILY CONTAMINATED, MOISTEN CLOTH WITH ISOPROPYL ALCOHOL OR ETHYL SOFT AND EASILY SCRATCHED. HANDLE THIS POLARIZER CAERFULLY. 3. IF THE DISPLAY SURFACE BECOMES CONTAMINATED, BREATHE ON THE SURFACE AND GENTLY WIPE IT WITH
- 4. PLEASE KEEP THE TEMPERATURE WITHIN SPECIFIED RANGE FOR USE AND STORAGE. POLARIZATION DEGRADATION, BUBBLE GENERATION OR POLARIZER PEEL-OFF MAY OCCUR WITH HIGH TEMPERATURE AND HIGH HUMIDITY.
- 5. DO NOT APPLY EXCESSIVE FORCE TO THE DISPLAY SURFACE OR THE ADJOINING AREAS SINCE THIS MAY CAUSE THE COLOR TONE TO VARY.
- 6. INSTALL THE LCD MODULE BY USING THE MOUNTING HOLES. WHEN MOUNTING THE LCD MODULE MAKE
- ENVIRONMENT. ACCELERATED BY WATER DROPLETS, MOISTURE CONDENSATION OR A CURRENT FLOW IN A HIGH-HUMIDITY SURE IT IS FREE OF TWISTING, WARPING AND DISTORTION.
  7. EXERCISE CARE TO MINIMIZE CORROSION OF THE ELECTRODES IS
- 8. NC TERMINAL SHOULD BE OPEN. DO NOT CONNECT ANYTHING.
  9. IF THE LOGIC CIRCUIT POWER IS OFF, DO NOT APPLY THE INPUT SIGNALS.
- 10. AVOID CONTACTING OIL AND FATS.
- A CONTAINER BEFORE COMING IN CONTACT WITH ROOM TEMPERATURE AIR. 11. CONDENSATION ON THE SURFACE AND CONTACT WITH TERMINALS DUE TO COLD WILL DAMAGE, STAIN OR DIRTY THE POLARIZERS. AFTER PRODUCTS ARE TESTED AT LOW TEMPERATURE THEY MUST BE WARMED UP IN
- MAY CAUSE DEFORMATION OR COLOR FADING. 12. WIPE OFF SALIVA OR WATER DROPS IMMIDEATLY, CONTACT WITH WATER OVER A LONG PERIOD OF TIME

## RECAUTION OF SOLDERING TO THE LCM

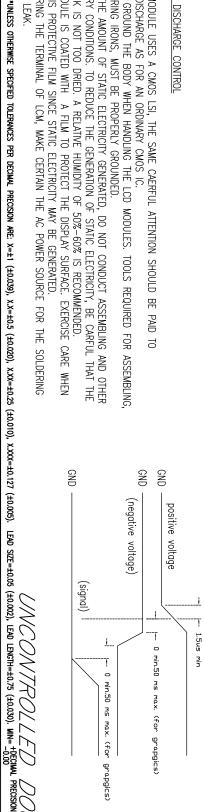
- 1, OBSERVE THE FOLLOWING WHEN SOLDERING LEAD WIRE, CONNECTOR CABLE AND ETC. TO THE LCD
- SOLDERING IRON TEMPERATURE: 300~350°C
- SOLDER: EUTECTIC SOLDER SOLDERING TIME: \$3 SEC.
- ABOVE IS A RECOMMENDED APPROACH. DUE TO DIFFERENT SOLDER COMPOSITION AND PROCESSING METHOD, IT IS RECOMMENDED THAT CUSTOMER TO STUDY AND FINE TUNING THEIR SOLDERING PROCESS PARAMETERS ACCORDINGLY.
- FLUX SPATTERS THAT YOU PROTECT THE LCD SURFACE WITH A COVER DURING SOLDERING TO PREVENT ANY DAMAGE DUE TO 2. IF SOLDERING FLUX IS USED, BE SURE TO REMOVE ANY REMANING FLUX AFTER FINISHING TO SOLDERING OPERATION. (THIS DOSE NOT APPLY IN THE CASE OF A NON-HALOGEN TYPE OF FLUX.) IT IS RECOMMENDED

### PRECAUTION FOR OPERATION

- 1. VIEWING ANGLE VARIES WITH THE CHANGE OF LIQUID CRYSTAL DRIVING VOLTAGE (Vo.). ADJUST Vo. SHOW THE BEST CONTRAST.
- 2. DRIVING THE LCD IN THE VOLTAGE ABOVE THE LIMIT SHORTERNS ITS LIFETIME
- 3. RESPONSE TIME IS GREATLY DELAYED AT TEMPERATURE BELOW THE OPERATURE TEMPERATURE RANGE HOWEVER, IT WILL RECOVER WHEN IT RETURNS TO THE SPECIFIED TEMPERATURE RANGE.
- 4. IF THE DISPLAY AREA IS PUSHED HARD DURING OPERATION, THE DISPLAY WILL BECOME ABNORMAL HOWEVER, IT WILL RETURN TO NORMAL IF IT IS TURNED OFF AND THEN BACK ON.
- 5. WHEN TURNING THE POWER ON, INPUT EACH SIGNAL AFTER THE POSITIVE/NEGATIVE VOLTAGE BECOMES DESIGN). STABLE (BELOW FIGURE IS A GENERAL ILLUSRATION WHERE TYPICAL VALUE DEPENDS ON INDIVIDUAL PRODUCT

# ELECTRO-STATIC DISCHARGE CONTROL

- ELECTROSTATIC DISCHARGE AS FOR AN ORDINARY CMOS IC. SINCE THIS MODULE USES A CMOS LSI, THE SAME CAERFUL ATTENTION SHOULD BE PAID 70
- 2. BE SURE TO GROUND THE BODY WHEN HANDLING THE LCD MODULES. TOOLS REQUIRED FOR ASSEMBLING, SUCH AS SOLDERING IRONS, MUST BE PROPERLY GROUNDED.
- AIR IN THE WORK IS NOT TOO DRIED. A RELATIVE HUMIDITY OF 50%-60% IS RECOMMENDED. 3. TO REDUCE THE AMOUNT OF STATIC ELECTRICITY GENERATED, DO NOT CONDUCT ASSEMBLING AND OTHER WORK UNDER DRY CONDITIONS. TO REDUCE THE GENERATION OF STATIC ELECTRICITY, BE CARFUL THAT THE
- 4. THE LCD MODULE IS COATED WITH A FILM TO PROTECT THE DISPLAY SURFACE. EXERCISE CARE WHEN PEELING OFF THIS PROTECTIVE FILM SINCE STATIC ELECTRICITY MAY BE GENERATED.
- 5. When soldering the terminal of LCM, make certain the ac power source for the soldering iron does not leak.



LCT-H480272M43W1T

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4.3, ACTIVE MATRIX FULL COLOR TFT W/ TOUCH PANEL

2:00 VIEW, LED

BACKLIGHT,

-20°C

7

+70°C OPERATING TEMP

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CHECKED

### RoHS COMPLIANT PRODUCT

THAN

100PPM

- CADMIUM AND CADMIUM COMPOUNDS
   HEXAVALENT CHROMIUM COMPOUNDS
   LEAD AND LEAD COMPOUNDS
   MERCURY AND MERCURY COPMPOUNDS
- POLYBROMINATED BIPHENYLS (PBBs)
- 6 POLYBROMINATED DIPHENYL ETHERS (PBDEs)

THAN

1000PPN

LESS LESS LESS LESS THA! THAN THAN THAN 1000PPM 1000PPM 1000PPN 1000PPM

### PACKAGING STANDARD

P.G	WEIGHT	ı	QUANTITY/ OUTER BOX
No.	WEIGHT		QUANTITY/ INER BOX
NEW	טטא וודב	463mm × 403mm × 303mm	BOX SIZE
NIDW	BOY TYPE	165	OUTER CARTON
PAPER CARTON	BOX MATERIAL PAPER CARTON		QUANTITY/ EACH BOX 96 PCS.
	PREPARE BY:	TFT MODULE	PRODUCT NAME.
RELEASE DATE   04/APR. 2007	RELEASE DATE	LCT-H480272M43W1T	PRODUCT NO.

THERE ARE 6 PCS LCD PER EACH ANTI-STATIC PLASTIC PLATE.

ARE 8 LAYER PLASTIC PLATES PER EACH INNER CARTON BOX.

ARE 2 INNER CARTON BOX PER EACH OUTER CARTON BOX.

- 1. WHEN STORING LCDS AS SPARES FOR SOME YEARS, THE FOLLOWING PRECAUCTIONS ARE NECESSARY
- STORE THEM IN A SEALED POLYETHYLENE BAG. IF PROPERLY SEALED, THERE IS NO NEED FOR DESICCANI.
   STORE THEM IN A DARK PLACE. DO NOT EXPOSE TO SUNLIGHT OR FLUORESCENT LIGHT, KEEP THE
- TEMPERATURE BETWEEN O'C AND 35°C.
- 4. ENVIRONMENTAL CONDITIONS:
- DO NOT LEAVE THEM FOR MORE THAN 168HRS. AT  $60^{\circ}\mathrm{C}$ . SHOULD NOT BE LEFT FOR MORE THAN 48HRS. AT  $-20^{\circ}\mathrm{C}$

### SAFETY

- 1. ITS RECOMMENDED TO CRUSH DAMAGED OR UNNECESSARY LCD INTO PIECES AND WASH THEM OFF WITH SOLVENTS SUCH AS ACETONE AND ETHANOL, WHICH SHOULD LATER BE BURNED.
  2. IF ANY LIQUID LEAKS OUT OF DAMAGED GLASS CELL AND COMES IN CONTACT WITH THE HANDS, WASH OFF THOROUGHLY WITH SOAP AND WATER.

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