

# 1A, 50V - 1000V High Efficient Rectifier

#### **FEATURES**

- AEC-Q101 qualified available
- · Glass passivated chip junction
- High current capability, Low V<sub>F</sub>
- High reliability
- High surge current capability
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

#### **MECHANICAL DATA**

- Case: DO-204AL (DO-41)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- · Polarity: Indicated by cathode band
- Weight: 0.330g (approximately)

KEY PARAMETERS					
PARAMETER VALUE UN					
I <sub>F</sub>	1	Α			
$V_{RRM}$	50 - 1000	V			
I <sub>FSM</sub>	30	Α			
$T_{JMAX}$	150	°C			
Package	DO-204AL (DO-41)				
Configuration	Single die				







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	HER 101G	HER 102G	HER 103G	HER 104G	HER 105G	HER 106G	HER 107G	HER 108G	UNIT
Marking code on the device		HER 101G	HER 102G	HER 103G	HER 104G	HER 105G	HER 106G	HER 107G	HER 108G	
Repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	200	300	400	600	800	1000	٧
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	140	210	280	420	560	700	V
Forward current	I <sub>F</sub>	1				Α				
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I <sub>FSM</sub>	30				А				
Junction temperature	T <sub>J</sub> -55 to +150				°C					
Storage temperature	T <sub>STG</sub>	T <sub>STG</sub> -55 to +150					°C			



THERMAL PERFORMANCE						
PARAMETER	SYMBOL	TYP	UNIT			
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	60	°C/W			
Junction-to-case thermal resistance	$R_{\Theta JC}$	15	°C/W			

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
(1)	HER101G HER102G HER103G HER104G			-	1.0	V
Forward voltage <sup>(1)</sup>	HER105G	$I_F = 1A, T_J = 25^{\circ}C$	$V_{F}$	-	1.3	V
	HER106G HER107G HER108G			-	1.7	V
		T <sub>J</sub> = 25°C		-	5	μΑ
Reverse current @ rated V <sub>R</sub>	. ,	T <sub>J</sub> = 125°C		-	150	μΑ
Junction capacitance	HER101G HER102G HER103G HER104G HER105G	1MHz, V <sub>R</sub> = 4.0V	C <sub>J</sub>	15	-	pF
	HER106G HER107G HER108G			10	-	pF
Reverse recovery time	HER101G HER102G HER103G HER104G HER105G	$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t <sub>rr</sub>	-	50	ns
	HER106G HER107G HER108G			-	75	ns

## Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION					
ORDERING CODE <sup>(1)(2)</sup>	PACKAGE	PACKING			
HER1xG	DO-204AL (DO-41)	5,000 / Tape & Reel			
HER1xG A0G	DO-204AL (DO-41)	3,000 / Ammo box			
HER1xGH	DO-204AL (DO-41)	5,000 / Tape & Reel			
HER1xGHA0G	DO-204AL (DO-41)	3,000 / Ammo box			

### Notes:

- 1. "x" defines voltage from 50V (HER101G) to 1000V (HER108G)
- 2. "H" means AEC-Q101 qualified



### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve

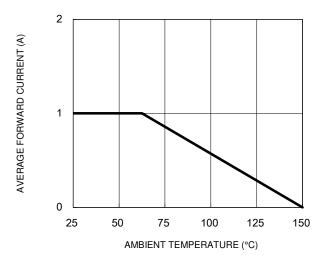


Fig.3 Typical Reverse Characteristics

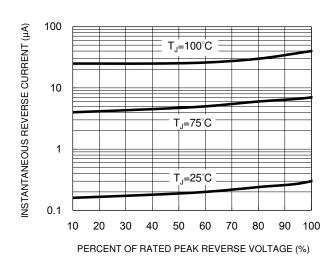


Fig.2 Typical Junction Capacitance

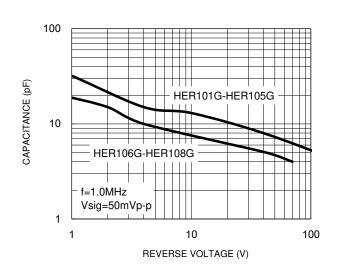


Fig.4 Typical Forward Characteristics

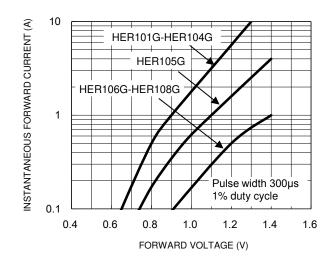
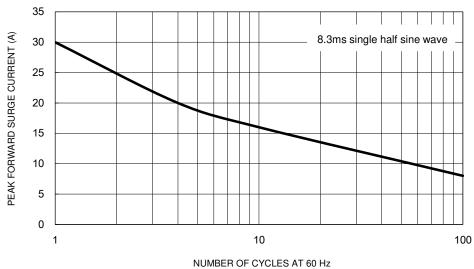


Fig.5 Maximum Non-Repetitive Forward Surge Current



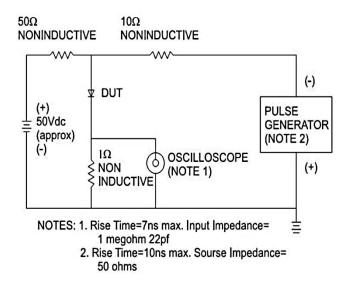
3

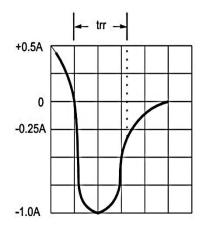


## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram

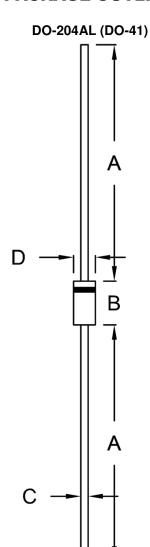








## **PACKAGE OUTLINE DIMENSIONS**



DIM.	Unit (mm)		Unit (inch)		
Dilvi.	Min.	Max.	Min.	Max.	
А	25.40	-	1.000	-	
В	4.20	5.20	0.165	0.205	
С	0.71	0.86	0.028	0.034	
D	2.00	2.70	0.079	0.106	

## **MARKING DIAGRAM**



P/N = Marking Code G = Green Compound

YWW = Date Code F = Factory Code



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