

# **PC3H7 Series**

\*4-channel package type is also available. (model No. **PC3Q67**)

# Mini-flat Half Pitch Package, General Purpose Photocoupler



#### ■ Description

**PC3H7 Series** contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin Mini-flat package, Half ptich type.

Input-output isolation voltage(rms) is 2.5kV.

Collector-emitter voltage is 80V(\*) and CTR is 20% to 400% at input current of 1mA.

#### **■** Features

- 1. 4-pin Mini-flat Half pitch package (Lead pitch : 1.27mm)
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High collector-emitter voltage ( $V_{\text{CEO}}$ : 80 $V^{(*)}$ )
- 4. Current transfer ratio (CTR : MIN. 20% at  $I_F=1mA$ ,  $V_{CE}=5V$ )
- 5. Several CTR ranks available
- 6. Isolation voltage between input and output ( $V_{iso(rms)}$ : 2.5kV)
  - (\*) Up to Date code "P9" (September 2002)  $V_{\text{CEO}}$ : 70V.

#### ■ Agency approvals/Compliance

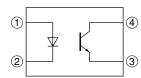
- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC3H7)
- 2. Approved by VDE, VDE0884 (as an option), file No. 5922UG (as model No. **PC3H7**)
- 3. Package resin: UL flammability grade (94V-0)

#### ■ Applications

1. Programmable controllers



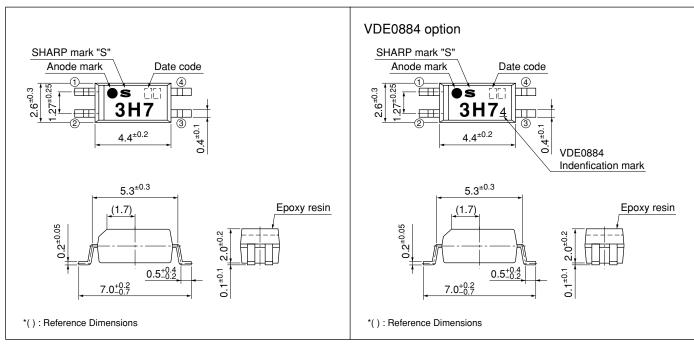
## ■ Internal Connection Diagram



- 1 Anode
- ② Cathode
- 3 Emitter
- 4 Collector

#### **■** Outline Dimensions

(Unit:mm)



Product mass: approx. 0.05g



# Date code (2 digit)

	1st o	digit		2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan

Rank mark Refer to the Model Line-up table



■ Absolute Maximum Ratings

■ Absolute Maximum Ratings $(T_a=25^{\circ}C)$							
	Parameter	Symbol	Rating	Unit			
	Forward current	$I_{\mathrm{F}}$	50	mA			
Input	*1 Peak forward current	$I_{FM}$	1	A			
Inj	Reverse voltage	$V_R$	6	V			
	Power dissipation	P	70	mW			
	Collector-emitter voltage	$V_{CEO}$	*4 80	V			
Output	Emitter-collector voltage	$V_{ECO}$	6	V			
Out	Collector current	$I_C$	50	mA			
	Collector power dissipation	$P_{C}$	150	mW			
Total power dissipation		$P_{tot}$	170	mW			
	Operating temperature	Topr	-30 to +100	°C			
5	Storage temperature	$T_{stg}$	-40 to +125	°C			
*2 I	solation voltage	V <sub>iso (rms)</sub>	2.5	kV			
*3 €	Soldering temperature	$T_{sol}$	260	°C			

# **■** Electro-optical Characteristics

 $(T_a=25^{\circ}C)$ 

	•						$(1_{a}-25\mathbf{C})$	
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
	Forward voltage		$V_F$	$I_F=20mA$	_	1.2	1.4	V
Input	Reverse Current		$I_R$	$V_R=4V$	_	_	10	μΑ
	Terminal capacitance		$C_t$	V=0, f=1kHz	_	30	250	pF
	Collector dark current		$I_{CEO}$	$V_{CE}$ =50V, $I_F$ =0	_	_	100	nA
Output	Collector-emitter breakdown voltage		$\mathrm{BV}_{\mathrm{CEO}}$	$I_{C}=0.1 \text{ mA}, I_{F}=0$	*5 80	_	_	V
	Emitter-collector breakdown voltage		$BV_{ECO}$	$I_{E}=10\mu A, I_{F}=0$	6	_	_	V
	Collector current		$I_{C}$	$I_F=1mA$ , $V_{CE}=5V$	0.2	-	4.0	mA
	Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_F=20\text{mA}, I_C=1\text{mA}$	-	0.1	0.2	V
Transfer	Isolation resistance		$R_{\rm ISO}$	DC500V, 40 to 60%RH	5×10 <sup>10</sup>	1×10 <sup>11</sup>	_	Ω
charac- teristics	Floating capacitance		$C_{\rm f}$	V=0, $f=1MHz$	-	0.6	1.0	pF
wiistics	D .:	Rise time	t <sub>r</sub>	V 2V I 2m A B 1000	_	4	18	μs
	Response time	Fall time	$t_{\rm f}$	$V_{CE}=2V$ , $I_{C}=2mA$ , $R_{L}=100\Omega$	_	3	18	μs

<sup>\*5</sup> Up to Date code "P9" (September 2002) BV<sub>CEO</sub>≥70V.

<sup>\*1</sup> Pulse width≤100µs, Duty ratio : 0.001 \*2 40 to 60%RH, AC for 1 minute, f=60Hz \*3 For 10s \*4 Up to Date code "P9" (September 2002) V<sub>CEO</sub> : 70V.



# **■** Model Line-up

Package	Tap 3 000p		Rank mark	I <sub>C</sub> [mA]	
VDE0884	— Approved		Kank mark	$(I_F=1 \text{mA}, V_{CE}=5 \text{V}, T_a=25 ^{\circ}\text{C})$	
	PC3H7	PC3H7Y	with or without	0.2 to 4.0	
	PC3H7A	PC3H7Y1	A	0.35 to 0.7	
	РС3Н7В	PC3H7Y2	В	0.5 to 1.0	
	РС3Н7С	PC3H7Y3	С	0.8 to 1.6	
	PC3H7D	PC3H7Y4	D	1.2 to 2.4	
Model No.	PC3H7AB	PC3H7Y5	A or B	0.35 to 1.0	
	PC3H7BC	PC3H7Y6	B or C	0.5 to 1.6	
	PC3H7CD	PC3H7Y7	C or D	0.8 to 2.4	
	PC3H7AC PC3H7Y8		A, B or C	0.35 to 1.6	
	PC3H7BD	PC3H7Y9	B, C or D	0.5 to 2.4	
	PC3H7AD	PC3H7Y0	A, B, C or D	0.35 to 2.4	

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.



Fig.1 Forward Current vs. Ambient Temperature

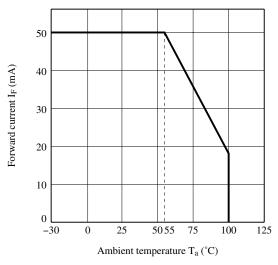


Fig.3 Collector Power Dissipation vs. Ambient Temperature

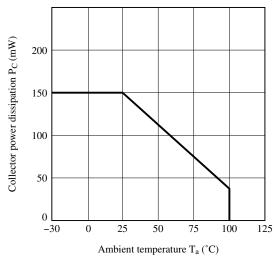


Fig.5 Peak Forward Current vs. Duty Ratio

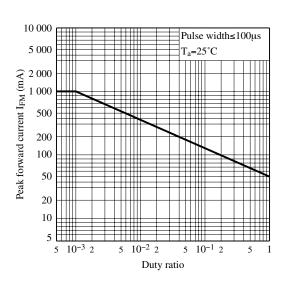


Fig.2 Diode Power Dissipation vs. Ambient Temperature

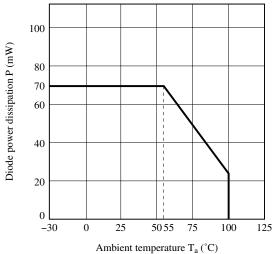


Fig.4 Total Power Dissipation vs. Ambient Temperature

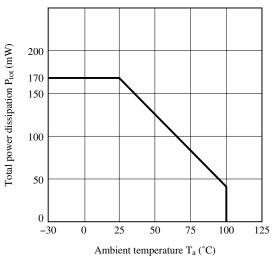


Fig.6 Forward Current vs. Forward Voltage

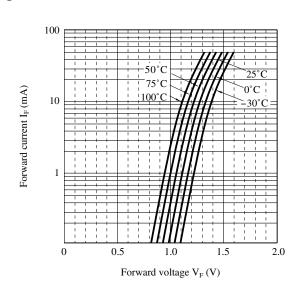




Fig.7 Current Transfer Ratio vs. Forward Current

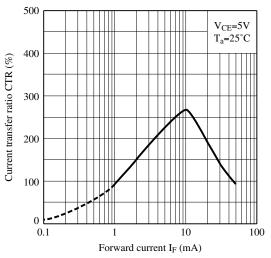


Fig.9 Relative Current Transfer Ratio vs. Ambient Temperature

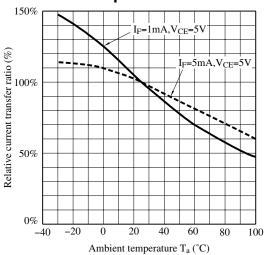


Fig.11 Collector Dark Current vs. Ambient Temperature

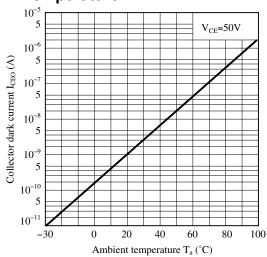


Fig.8 Collector Current vs. Collector-emitter Voltage

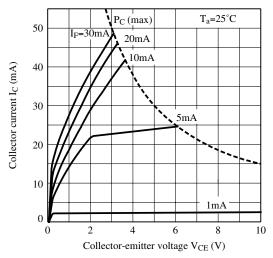


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

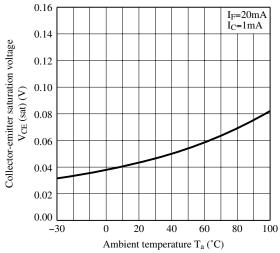


Fig.12 Response Time vs. Load Resistance

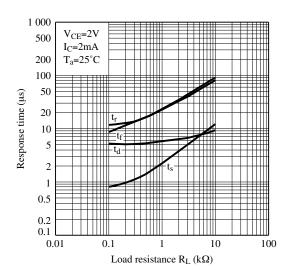
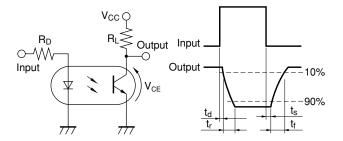


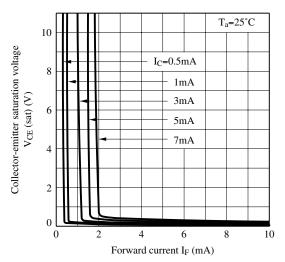


Fig.13 Test Circuit for Response Time



Please refer to the conditions in Fig.12

Fig.14 Collector-emitter Saturation Voltage vs. Forward Current



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



#### ■ Design Considerations

# Design guide

While operating at I<sub>F</sub><1.0mA, CTR variation may increase.

Please make design considering this fact.

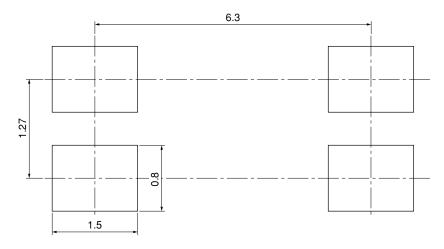
This product is not designed against irradiation and incorporates non-coherent IRED.

### Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

## Recommended Foot Print (reference)



(Unit: mm)

<sup>☆</sup> For additional design assistance, please review our corresponding Optoelectronic Application Notes.



#### ■ Manufacturing Guidelines

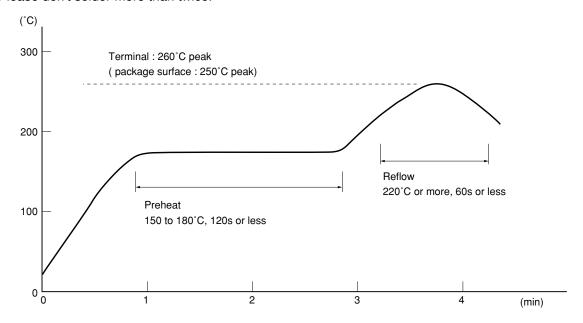
#### Soldering Method

#### Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



#### Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

#### Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

#### Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



#### Cleaning instructions

#### Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

#### Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

#### Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

#### Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

Sheet No.: D2-A01901EN



## ■ Package specification

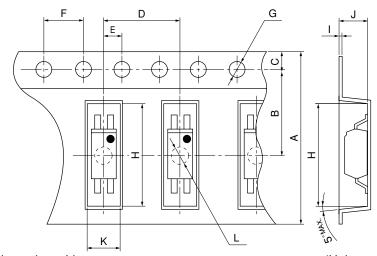
# ● Tape and Reel package

Package materials Carrier tape : PS

Cover tape: PET (three layer system)

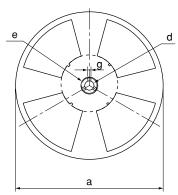
Reel: PS

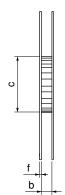
# Carrier tape structure and Dimensions



Dimension	ns List				(L	Jnit: mm)
A	В	C	D	Е	F	G
12.0 <sup>±0.3</sup>	5.5 <sup>±0.1</sup>	1.75 <sup>±0.1</sup>	8.0 <sup>±0.1</sup>	2.0 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>	φ1.5 <sup>+0.1</sup>
Н	I	J	K	L		
$7.5^{\pm0.1}$	0.3 <sup>±0.05</sup>	2.3 <sup>±0.1</sup>	3.1 <sup>±0.1</sup>	φ1.6 <sup>+0.1</sup>		

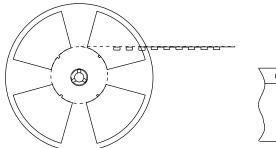
# Reel structure and Dimensions

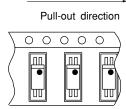




Dimension	ns List	(Unit: mm)			
a	b	С	d		
330 13.5±1.5		100±1.0	13 <sup>±0.5</sup>		
e	f	g			
23±1.0	23 <sup>±1.0</sup> 2.0 <sup>±0.5</sup>				

# Direction of product insertion





[Packing: 3 000pcs/reel]



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