Λ Λ Χ Λ Ο.4Ω, Low-Voltage, Single-Supply SPST Analog Switches in SC70

General Description

The MAX4715/MAX4716 are low on-resistance, low-

voltage, single-pole/single-throw (SPST) analog switches that operate from a +1.6V to +3.6V single supply. The MAX4715 is normally open (NO), and the MAX4716 is normally closed (NC). These devices also have fast switching speeds (t_{ON} = 18ns max, t_{OFF} = 12ns max).

When powered from a +3V supply, the MAX4715/ MAX4716 offer 0.4Ω max on-resistance (R_{ON}) with 0.1Ω max R_{ON} flatness. Their digital logic inputs are +1.8V CMOS compatible when using a single +3V supply.

The MAX4715 is pin compatible with the MAX4594, and the MAX4716 is pin compatible with the MAX4595. The MAX4715/MAX4716 are available in SC70-5 packages.

Applications

Power Routing Battery-Operated Equipment Audio and Video Signal Routing

Low-Voltage Data-Acquisition Systems

Communications Circuits

PCMCIA Cards

Cellular Phones

Modems

Hard Drives

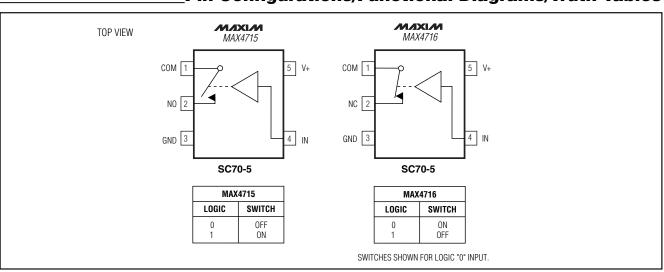
Low R_{ON} 0.4Ω max (+3V Supply) 1.2Ω max (+1.8V Supply)

- ♦ 0.1Ω max Ron Flatness (+3V Supply)
- +1.6V to +3.6V Single-Supply Operation
- Available in 5-Pin SC70 Packages
- Fast Switching: ton = 18ns max, toFF = 12ns max
- +1.8V CMOS Logic Compatible (+3V Supply)
- Pin Compatible with MAX4594 (MAX4715)
 Pin Compatible with MAX4595 (MAX4716)

Features

Ordering Information

PART	TEMP. RANGE	IP. RANGE PIN- PACKAGE	
MAX4715EXK-T	-40°C to +85°C	5 SC70-5	ACJ
MAX4716EXK-T	-40°C to +85°C	5 SC70-5	ACK



M/X/W

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For price, delivery, and to place orders, please contact Maxim Distribution at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Pin Configurations/Functional Diagrams/Truth Tables

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ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to GND

V+, IN	0.3V to +4V
COM, NO, NC (Note 1)	0.3V to (V+ + 0.3V)
Continuous Current NO, NC to COM	±300mÁ
Peak Switch Current NO, NC to COM	
(pulsed at 1ms, 10% duty cycle max)	±600mA
Continuous Power Dissipation ($T_A = +70^{\circ}C$	C)
5-Pin SC70 (derate 3.1mW/°C above +7	0°C)247mW

Operating Temperature Range

MAX471_EXK	40°C to +85°C
Junction Temperature	
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)+300°C

Note 1: Signals on NO, NC, or COM exceeding V+ or GND are clamped by internal diodes.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Single +3V Supply

(V+ = +2.7V to +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V+ = +3.0V and T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	ТА	MIN	ТҮР	МАХ	UNITS	
ANALOG SWITCH								
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V	
On Registeres (Note 6)	Devi	V+ = 2.7V, I _{COM} = 100mA,	+25°C		0.3	0.4	Ω	
On-Resistance (Note 6)	R _{ON}	$V_{NO} \text{ or } V_{NC} = 1.5 V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			0.45	52	
On-Resistance Flatness (Note 4)	DEL ATION	V+ = 2.7V, I _{COM} = 100mA,	+25°C		0.05	0.09		
On-nesistance riatness (Note 4)	RFLAT(ON)	$V_{NO} \text{ or } V_{NC} = 0.6, 1.5V, 2.1V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			0.1	Ω	
NO, NC Off-Leakage Current	I _{NO(OFF)} or	V+ = 3.3V, V _{COM} = 0.3V, 3V	+25°C	-1	0.01	1	nA	
NO, NO OII-Leakage Current	I _{NC(OFF)} or	$V_{NO} \text{ or } V_{NC} = 3V, 0.3V$	T _{MIN} to T _{MAX}	-10		10	ПА	
COM Off-Leakage Current	ICOM(OFF)	V+ = 3.3V, V _{COM} = 0.3V, 3V	+25°C	-1	0.01	1	nA	
	ICOIVI(OFF)	$V_{NO} \text{ or } V_{NC} = 3V, 0.3V$	T _{MIN} to T _{MAX}	-10		10	ΠA	
COM On-Leakage Current	ICOM(ON)	$V + = 3.3V$, $V_{COM} = 0.3V$, $3V$, V_{NO} or	+25°C	-2		2	nA	
<u> </u>	ICOM(ON)	$V_{NC} = 0.3V$, 3V or floating	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	-10		10	ПА	
DYNAMIC	1		1				1	
Turn-On Time	ton	$V_{NO} \text{ or } V_{NC}$ = 1.5V, R_L = 50 Ω ,	+25°C		12	18	ns	
	-011	C _L = 35pF, Figure 1	T _{MIN} to T _{MAX}			20		
Turn-Off Time	torr	toff	$V_{NO} \text{ or } V_{NC}$ = 1.5V, R_L = 50 $\Omega,$	+25°C		6	12	ns
	-011	C _L = 35pF, Figure 1	T_{MIN} to T_{MAX}			15		
Charge Injection	Q	$V_{GEN} = 0$, $R_{GEN} = 0$, $C_L = 1.0$ nF, Figure 2	+25°C		20		рС	
Off-Isolation (Note 5)	VISO	f = 1MHz, V _{COM} = 1V _{RMS} , R _L = 50 Ω , C _L = 5pF, Figure 3	+25°C		-54		dB	
Total Harmonic Distortion	THD	f = 20Hz to 20kHz, V _{COM} = 2V _{P-P} , R _L = 32 Ω	+25°C		0.01		%	
NC or NO Off-Capacitance	C _{NO(OFF)} C _{NC(OFF)}	f = 1MHz, Figure 4	+25°C		55		pF	
COM Off-Capacitance	CCOM(OFF)	f = 1MHz, Figure 4	+25°C		55		рF	
COM On-Capacitance	C _{COM} (ON)	f = 1MHz, Figure 4	+25°C		80		рF	

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ELECTRICAL CHARACTERISTICS—Single +3V Supply (continued)

(V+ = +2.7V to +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V+ = +3.0V and T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	ТА	MIN	ТҮР	MAX	UNITS
LOGIC INPUT	•	·					
Input Voltage Low	VIL					0.5	V
Input Voltage High	VIH			1.4			V
Input Leakage Current	l _{IN}	$V_{IN} = 0 \text{ or } V_{+}$		-1		1	μΑ
SUPPLY			·				
Power-Supply Range	V+			1.6		3.6	V
	1.		+25°C		0.04	0.2	0
Positive Supply Current	I+ V+ = $+3.6V$, V _{IN} = 0 or V+ T _{MIN} to		T _{MIN} to T _{MAX}			2	μA

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply

 $(V + = +1.8V, V_{IH} = +1V, V_{IL} = +0.4V, T_A = T_{MIN}$ to T_MAX, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	ТА	MIN	ТҮР	MAX	UNITS
ANALOG SWITCH			· · · · · · · · · · · · · · · · · · ·				
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V
On Bagistanga	Davi	$I_{COM} = 10 \text{mA},$	+25°C		0.6	1.2	0
On-Resistance	R _{ON}	$V_{NO} \text{ or } V_{NC} = 0.9V$	T_{MIN} to T_{MAX}			2.5	Ω
	I _{NO(OFF)} or	V _{COM} = 0.3V, 1.5V, V _{NO} or	+25°C	-1		1	
NO or NC Off-Leakage Current		$V_{\rm NC} = 1.5 V, 0.3 V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	-10		10	nA
		V _{COM} = 0.3V, 1.5V, V _{NO} or	+25°C	-1		1	1
COM Off-Leakage Current	ICOM(OFF)	$V_{NC} = 1.5V, 0.3V$	T _{MIN} to T _{MAX}	-10		10	nA
	1	$\begin{array}{c} V_{COM} = 1.5V, \ 0.3V, \ V_{NO} \ or \\ V_{NC} = 1.5V, \ 0.3V, \ or \ floating \end{array}$	+25°C	-2		2	
COM On-Leakage Current	ICOM(ON)		T _{MIN} to T _{MAX}	-10		10	nA
DYNAMIC			· · · · · ·				
Turn-On Time	tou	$t_{ON} \qquad \begin{array}{l} V_{NO} \text{ or } V_{NC} = 1.5 \text{V}, \ \text{R}_{\text{L}} = 50 \Omega, \\ C_{\text{L}} = 35 \text{pF}, \ \text{Figure 1} \end{array}$	+25°C		18	25	20
Tum-On time	LON		$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			30	ns
Turne Off Time a		$V_{\rm NO} \text{ or } V_{\rm NC} = 1.5 \text{V}, \text{ R}_{\rm L} = 50 \Omega,$	+25°C		9	20	
Turn-Off Time	tOFF	$C_L = 35 pF$, Figure 1	T _{MIN} to T _{MAX}			25	ns
Charge Injection	Q	$V_{GEN} = 0$, $R_{GEN} = 0$, $C_L = 1nF$, Figure 2	+25°C		40		рС

0.4 Ω , Low-Voltage, Single-Supply SPST Analog Switches in SC70

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply (continued)

(V+ = +1.8V, V_{IH} = +1V, V_{IL} = +0.4V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	ТА	MIN	TYP	MAX	UNITS
LOGIC INPUT							
Input Voltage Low	VIL					0.4	V
Input Voltage High	VIH			1			V
Input Leakage Current	lin	$V_{IN} = 0 \text{ or } V_{+}$				1	μΑ
SUPPLY							
Positive Supply Current I+	1.	V _{IN} = 0 or V+	+25°C		0.04	0.2	
	V N = 0 or V +	T_{MIN} to T_{MAX}			2	μA	

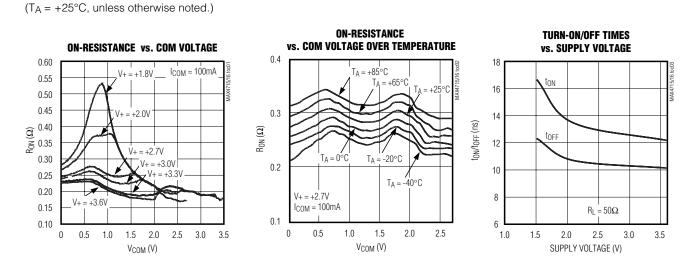
Note 2: The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.

Note 3: SC70-packaged parts are 100% tested at +25°C. Limits across the full temperature range are guaranteed by design and correlation.

Note 4: Flatness is defined as the difference between the maximum and minimum values of on-resistance as measured over the specified analog signal range.

Note 5: Off-Isolation = $20\log_{10} [V_{COM} / (V_{NC} \text{ or } V_{NO})], V_{COM} = \text{output}, V_{NC} \text{ or } V_{NO} = \text{input to off switch}.$

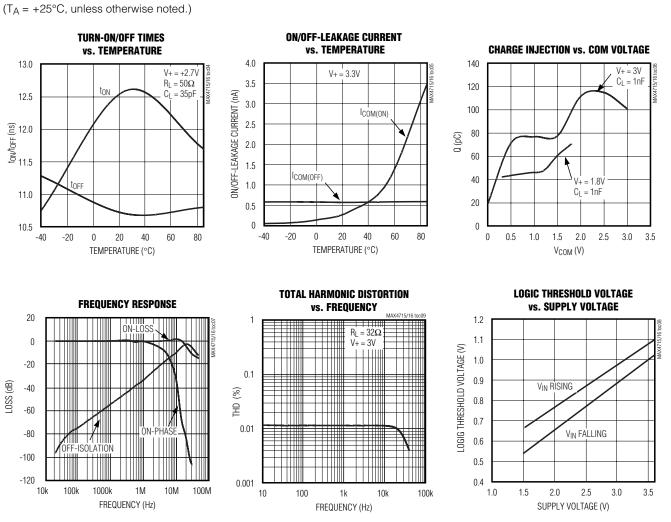
Note 6: Guaranteed by design.



Typical Operating Characteristics

0.4Ω, Low-Voltage, Single-Supply SPST Analog Switches in SC70

Typical Operating Characteristics (continued)



_Pin Description

PI	IN	NAME	FUNCTION	
MAX4715	MAX4716		Fonction	
1	1	COM	Analog Switch—Common	
2	_	NO	NO Analog Switch—Normally Open	
_	2	NC	Analog Switch—Normally Closed	
3	3	GND	Ground	
4	4	IN	Digital Control Input	
5	5	V+	Positive Supply Input	

MAX4715/MAX4716

0.4 Ω , Low-Voltage, Single-Supply SPST Analog Switches in SC70

MAX4715/MAX4716

Detailed Description

The MAX4715/MAX4716 are low on-resistance (RON), low-voltage, single-pole/single-throw (SPST) analog switches that operate from a +1.6V to +3.6V single supply. The MAX4715 is normally open (NO), and the MAX4716 is normally closed (NC).

When powered from a +3V supply, their 0.4 Ω R_{ON} allows high continuous currents to be switched in a variety of applications.

Applications Information

Logic Inputs

The MAX4715/MAX4716 logic inputs can be driven up to +3.6V regardless of the supply voltage. For example, with a +3.3V supply. IN may be driven low to GND and high to +3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Analog Signal Levels

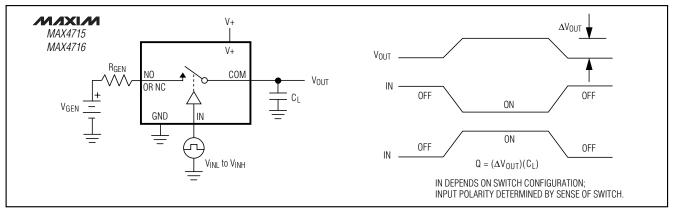
Analog signals that range over the entire supply voltage (V+ to GND) can be passed with very little change in on-resistance (see Typical Operating Characteristics). The switches are bidirectional, so the NO, NC, and COM pins can be used as either inputs or outputs.

Rail-to-Rail is a registered trademark of Nippon Motorola Ltd.

Test Circuits/Timing Diagrams

MIXIM MAX4715 V+ MAX4716 VINH LOGIC 50% V+ INPUT VINI NO СОМ VN VOUT OR NC R CL toff 50Ω 35pF IN VOUT $0.9 \times V_{0UT}$ 0.9 x V_{OUT} GND LOGIC SWITCH 0V INPUT OUTPUT ton CI INCLUDES FIXTURE AND STRAY CAPACITANCE. LOGIC INPUT WAVEFORMS INVERTED FOR SWITCHES $V_{OUT} = V_{N_{-}} \left(\frac{R_{L}}{R_{I} + R_{ON}} \right)$ THAT HAVE THE OPPOSITE LOGIC SENSE.

Figure 1. Switching Time





0.4Ω, Low-Voltage, Single-Supply SPST Analog Switches in SC70

Test Circuits/Timing Diagrams (continued)

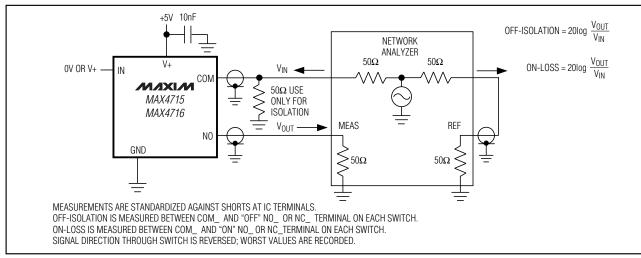


Figure 3. On-Loss and Off-Isolation

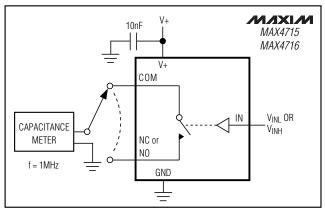
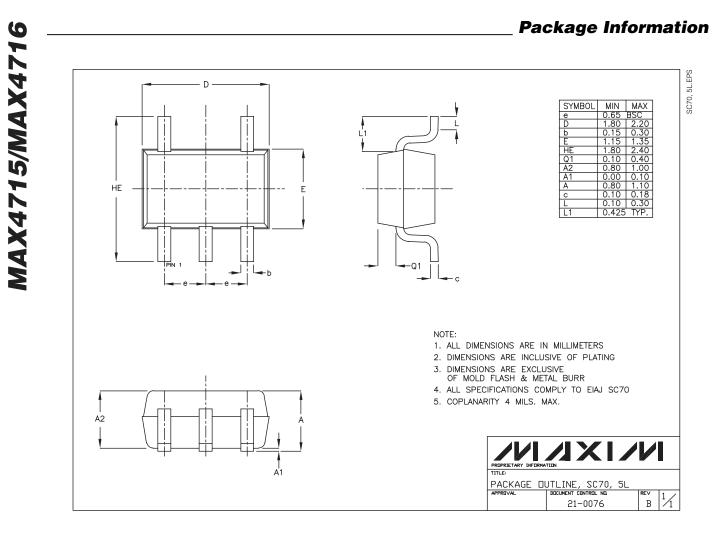


Figure 4. Channel Off/On-Capacitance

Chip Information

TRANSISTOR COUNT: 135 PROCESS: CMOS

0.4 Ω , Low-Voltage, Single-Supply SPST Analog Switches in SC70



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