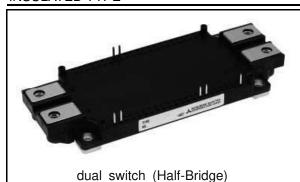


<IGBT Modules>

CM400DX-12A

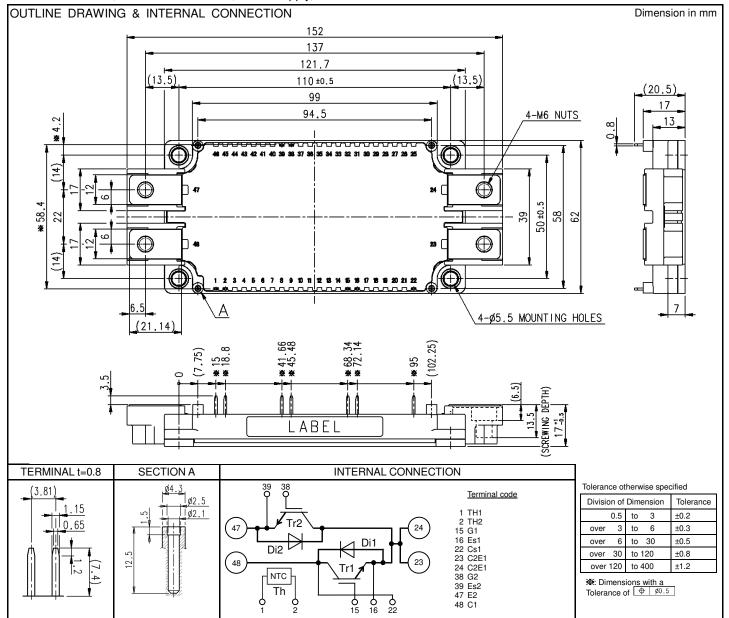
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- •Copper base plate (non-plating)
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



1

<IGBT Modules>

CM400DX-12A

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	600	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callactor augreent	DC, T _C =60 °C (Note2, 4)	400	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	800	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1340	W
I _E (Note1)	Facilities assumed	DC (Note2)	400	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	800	Α

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
Tj	Junction temperature	-	-40 ~ +150	۰.
T _{stg}	Storage temperature	-	-40 ~ +125	10

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions	Conditions		Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	V _{CE} =V _{CES} , G-E short-circuited		-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =40 mA, V _{CE} =10 V		5	6	7	V
		I _C =400 A, V _{GE} =15 V (Note5)	T _j =25 °C	-	1.7	2.1	
V_{CEsat}	Collector-emitter saturation voltage	Refer to the figure of test circuit	T _j =125 °C	-	1.9	-	V
		I _C =400 A, V _{GE} =15 V, chip (Note5)		-	1.6	-	
Cies	Input capacitance			-	-	50	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	5.3	nF
Cres	Reverse transfer capacitance	7		-	-	1.6	
Q _G	Gate charge	V _{CC} =300 V, I _C =400 A, V _{GE} =15 V		-	1100	-	nC
t _{d(on)}	Turn-on delay time			-	-	200	
t _r	Rise time	V_{CC} =300 V, I_{C} =400 A, V_{GE} =±15 V,		-	-	200	
t _{d(off)}	Turn-off delay time	_	-	-	400	ns	
tf	Fall time	$R_{\rm G}$ =3.6 Ω, Inductive load		-	-	600	1
	Emitter-collector voltage	I _E =400 A, G-E short-circuited (Note5)	T _j =25 °C	-	2.0	2.8	V
V _{EC} (Note1)		Refer to the figure of test circuit	T _j =125 °C	-	1.95	-	
		I _E =400 A, G-E short-circuited, chip	(Note5)	-	1.9	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =300 V, I _E =400 A, V _{GE} =±15 V,		-	-	200	ns
Q _{rr} (Note1)	Reverse recovery charge	R_{G} =3.6 Ω , Inductive load		-	11	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =300 V, I _C =I _E =400 A,		-	13.5	-	
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=3.6 \Omega, T_{j}=125 \text{ °C},$		-	23	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	3.8	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	1.1	-	mΩ
r _g	Internal gate resistance	Per switch, T _C =25 °C (Note4)		-	0	-	Ω

<IGBT Modules>

CM400DX-12A

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_i=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Itam	Conditions		Limits		Unit
	Item	Conditions	Min.	Тур.	Max.	Uniit
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	ltom Conditions	Conditions		Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic		
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	0.093	K/W		
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	-	0.16	IN/VV		
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7)	-	15	-	K/kW		

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions			Limits		Unit	
	item	Conditions		Min.	Тур.	Max.	Unit	
M_t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N⋅m	
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N⋅m	
٦	Craenage distance	Terminal to terminal		11.55	-	-	mm	
ds	Creepage distance	Terminal to base plate		12.32	-	-		
٦	Clearance	Terminal to terminal		10.00	-	-	mm	
da	Clearance	Terminal to base plate		10.85	-	-	mm	
m	mass	-		-	330	-	g	
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm	

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

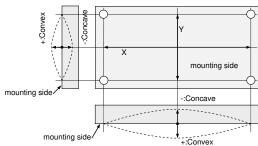
- 2. Junction temperature (T_j) should not increase beyond $T_{j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{imax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

$$6.\,B_{\left(25/50\right)}=\text{In}(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}}\!-\!\frac{1}{T_{50}})\;,$$

R₂₅: resistance at absolute temperature T₂₅ [K]; T₂₅=25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



- 9. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.
 - "φ2.3×10 or φ2.3×12, B1 tapping screw"

The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

HIGH POWER SWITCHING USE

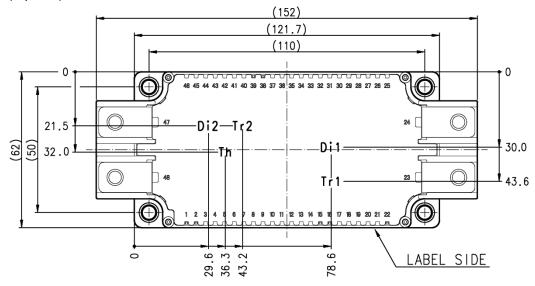
INSULATED TYPE

RECOMMENDED OPERATING CONDITIONS

Symbol	ltem	Conditions	Limits		Unit	
Symbol	item	Min. Typ. M	Max.	- Offic		
V _{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	300	400	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	1.6	-	16	Ω

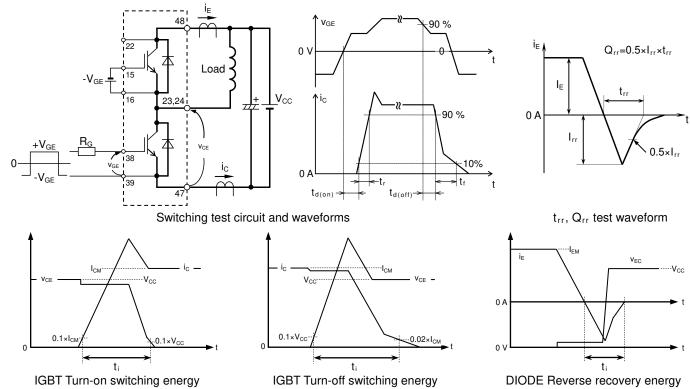
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



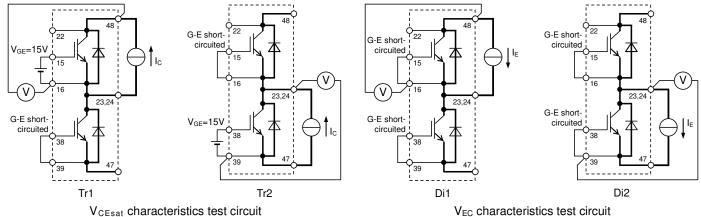
Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS



Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

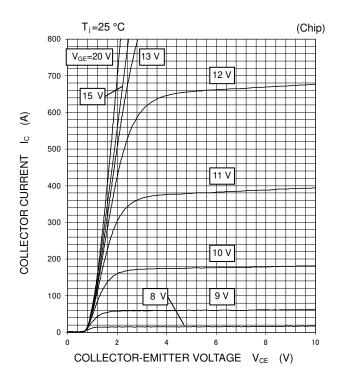




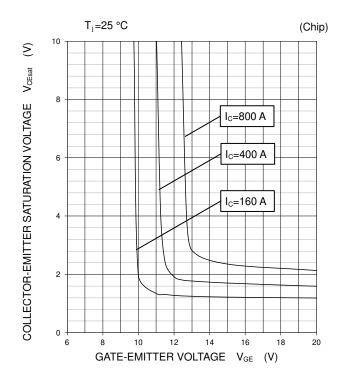
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES INVERTER PART

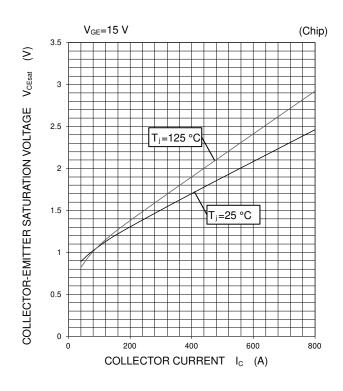
OUTPUT CHARACTERISTICS (TYPICAL)



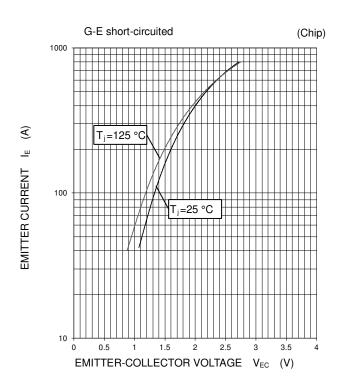
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



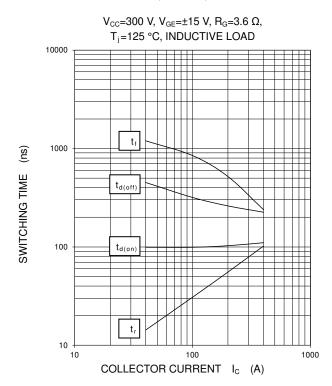
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



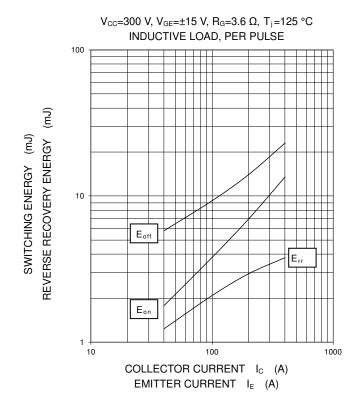
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES INVERTER PART

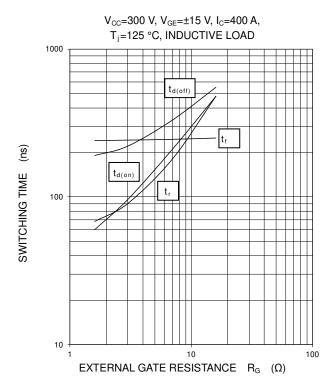
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



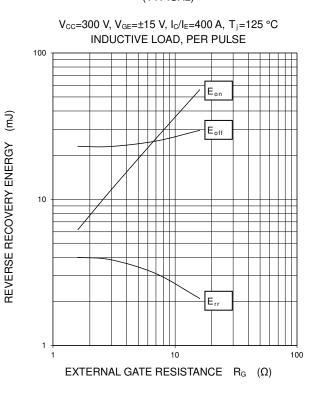
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

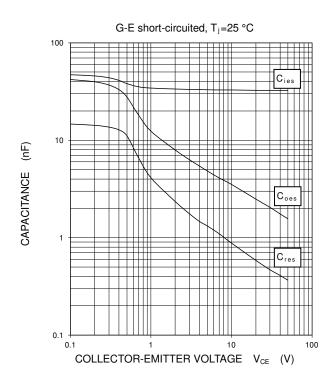


SWITCHING ENERGY (mJ)

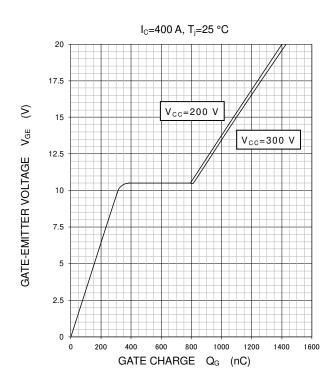
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES **INVERTER PART**

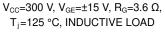
CAPACITANCE CHARACTERISTICS (TYPICAL)

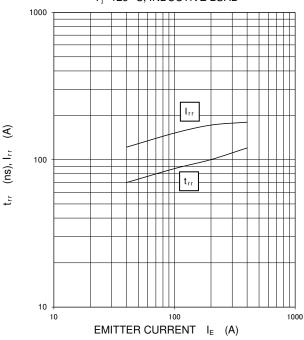


GATE CHARGE CHARACTERISTICS (TYPICAL)

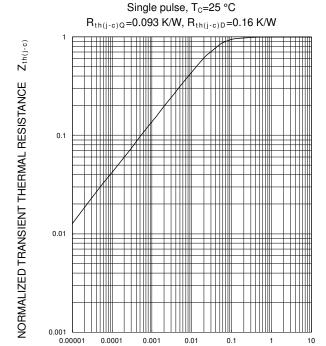


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)





TRANSIENT THERMAL IMPEDANCE CHARACTERISTIC S (MAXIMUM)



0.0001

0.001

0.01

TIME (S)

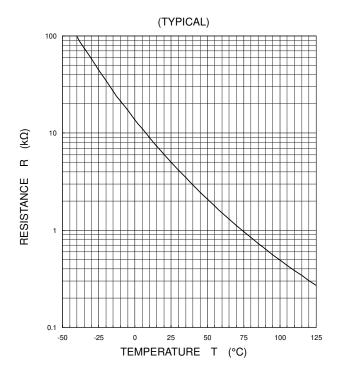
0.1

HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS



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