PL602-20-K52

Programming Specification

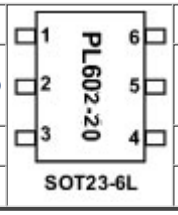
OPERATING PARAMETERS

Input Clock (MHz)	CLK0/1		Operating Voltage (V)
	Frequency (MHz)	Accuracy (ppm)	
100	100	0	3.3

PIN DESCRIPTION

Name	Pin #	Type	Description
CLK1	1	O	Programmable Clock Input
GND	2	P	GND Connection
FIN	3	I	Reference Input
OE	4	I	Output Enable Input
VDD	5	P	VDD Connection
CLK0	6	O	Programmable Clock Output

Package Pin Configuration



Note: please refer to PhaseLink's PL602-20T datasheet for detailed electrical specifications.

ORDERING /SHIPPING INFORMATION

Package Option	Temp. Range	Shipping Method
SOT23	Commercial	T&R

Part/Order Number	Verification Code	Marking
PL602-20-K52TC	02-02-04-01-60-40-00-00-33	C9K52 LLL

For Package/Marking specifications please see Packaging Spec.
Note: LLL / LLLLL designate Production Lot.

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