# **5LP01SS**

# P-Channel Small Signal MOSFET –50V, –0.07A, 23Ω, Single SSFP



http://onsemi.com

#### **Features**

- · Low ON-resistance
- · Ultrahigh-speed switching
- · 1.5V drive
- · Halogen free compliance

### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

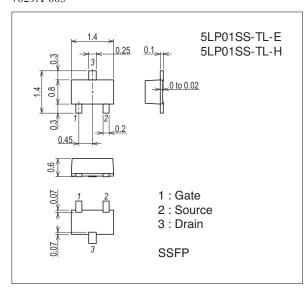
Parameter	Symbol	Conditions	Ratings	Unit
Drain to Source Voltage	V <sub>DSS</sub>		-50	V
Gate to Source Voltage	V <sub>GSS</sub>		±10	V
Drain Current (DC)	ID		-0.07	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-0.28	Α
Allowable Power Dissipation	PD		0.15	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

This product is designed to "ESD immunity < 200V\*", so please take care when handling.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Package Dimensions**

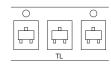
unit : mm (typ) 7029A-003



#### **Ordering & Package Information**

Device	Package	Shipping	memo	
5LP01SS-TL-E	SSFP SC-81	8,000 pcs./reel	Pb-Free	
5LP01SS-TL-H	SSFP SC-81	8,000 pcs./reel	Pb-Free and Halogen Free	

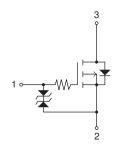
#### Packing Type: TL



# Marking



#### **Electrical Connection**



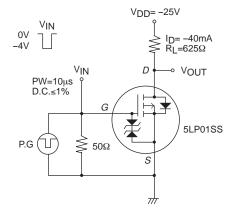
<sup>\*</sup> Machine Model

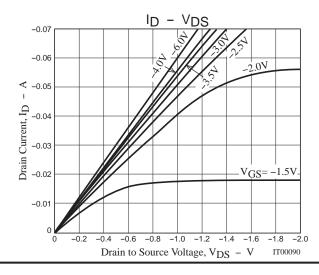
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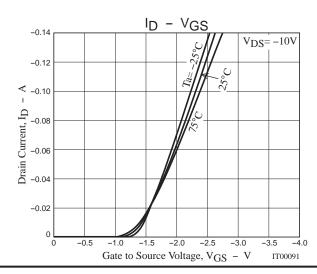
#### Electrical Characteristics at Ta=25°C

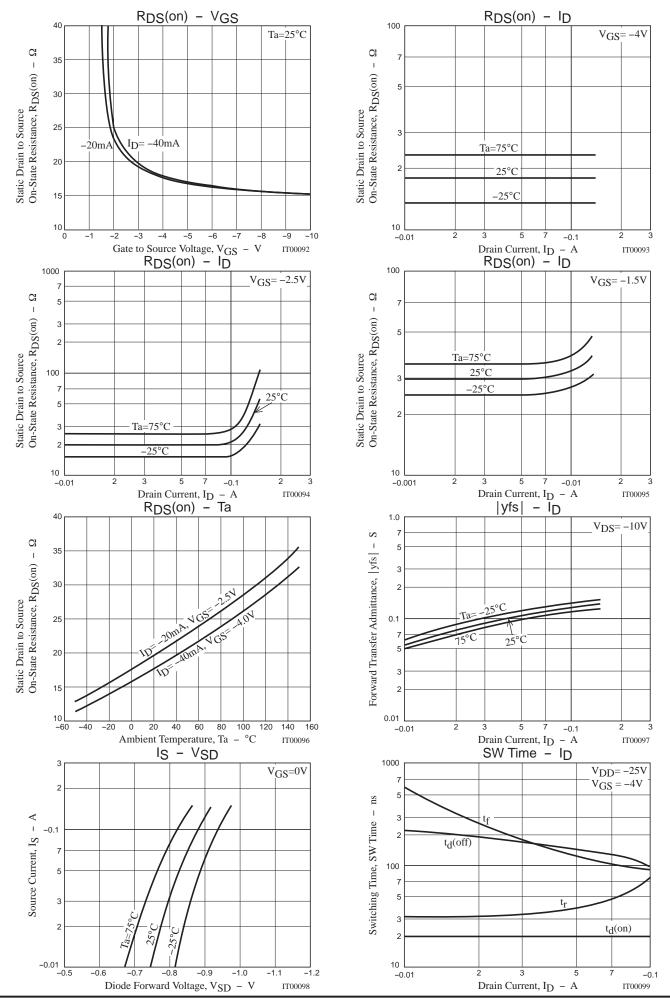
Parameter	Symbol	Conditions	Ratings			Unit
Farameter		Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID= -1mA, VGS=0V	-50			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = -50V, V <sub>GS</sub> =0V			-1	μΑ
Gate to Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> = -10V, I <sub>D</sub> = -100μA	-0.4		-1.4	٧
Forward Transfer Admittance	yfs	V <sub>DS</sub> = -10V, I <sub>D</sub> = -40mA	70	100		mS
	R <sub>DS</sub> (on)1	I <sub>D</sub> = -40mA, V <sub>G</sub> S= -4V		18	23	Ω
Static Drain to Source On-State Resistance	R <sub>DS</sub> (on)2	I <sub>D</sub> = -20mA, V <sub>GS</sub> = -2.5V		20	28	Ω
	R <sub>DS</sub> (on)3	I <sub>D</sub> = -5mA, V <sub>G</sub> S= -1.5V		30	60	Ω
Input Capacitance	Ciss			7.4		pF
Output Capacitance	Coss	VDS= -10V, f=1MHz		4.2		pF
Reverse Transfer Capacitance	Crss			1.3		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			20		ns
Rise Time	t <sub>r</sub>	Constitution Total Circuit		35		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		160		ns
Fall Time	tf			150		ns
Total Gate Charge	Qg			1.40		nC
Gate to Source Charge	Qgs	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -70mA		0.16		nC
Gate to Drain "Miller" Charge	Qgd			0.23		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = -70mA, V <sub>GS</sub> =0V		-0.85	-1.2	V

# **Switching Time Test Circuit**



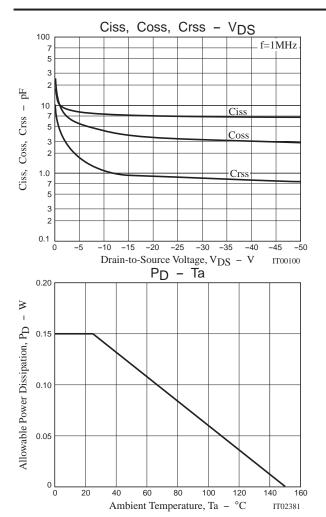


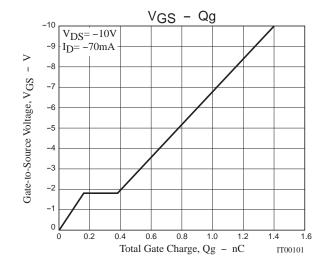




# **5LP01SS**

IT02381



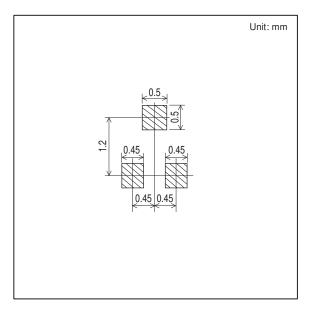


# **Outline Drawing**

5LP01SS-TL-E, 5LP01SS-TL-H

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# **Land Pattern Example**



Note on usage: Since the 5LP01SS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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