



Features

- Formerly **J. W. Miller**® model
- Current rating up to 20 A
- Inductance range: 3.35 μ H to 1000 μ H
- RoHS compliant*

Applications

- EMI filters
- Output chokes
- Smoothing coils

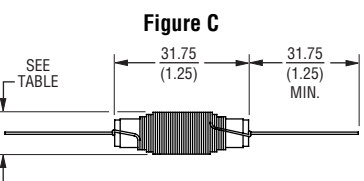
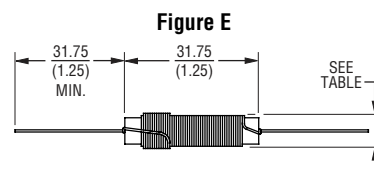
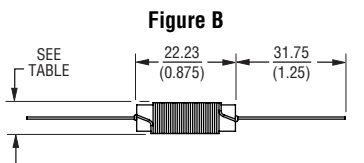
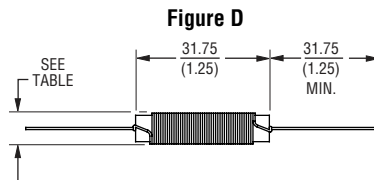
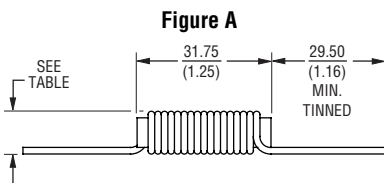
5200 Series - Hash Choke

Electrical Specifications

Part Number	Inductance (μ H)	Tol.	Test Freq.	DCR (Ω) Max.	I dc (A)	Lead Wire Size (AWG)	Figure	Diameter Max.
5218-RC	3.35	± 20 %	7.96 MHz	0.010	20	12	A	$\frac{15.24}{(0.60)}$
5219-RC	4.9	± 20 %	7.96 MHz	0.016	15	14	A	$\frac{15.24}{(0.60)}$
5220-RC	8.8	± 20 %	7.96 MHz	0.021	10	16	A	$\frac{14.22}{(0.56)}$
5230-RC	4.0	± 20 %	7.96 MHz	0.012	8.0	20	B	$\frac{9.65}{(0.38)}$
5240-RC	40	± 20 %	1.0 KHz	0.082	3.0	20	D	$\frac{7.87}{(0.31)}$
5248-RC	68	± 20 %	1.0 KHz	0.054	5.0	20	C	$\frac{14.22}{(0.56)}$
5250-RC	100	± 20 %	1.0 KHz	0.216	2.0	20	E	$\frac{9.65}{(0.38)}$
5252-RC	125	± 20 %	1.0 KHz	0.08	3.5	20	C	$\frac{12.70}{(0.50)}$
5254-RC	250	± 20 %	1.0 KHz	0.17	2.5	20	C	$\frac{11.18}{(0.44)}$
5256-RC	500	± 20 %	1.0 KHz	0.26	2.0	20	C	$\frac{14.22}{(0.56)}$
5258-RC	1000	± 20 %	1.0 KHz	0.55	1.0	20	C	$\frac{12.70}{(0.50)}$

DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Product Dimensions



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Additional Information

Click these links for more information:



General Specifications

Rated Current..... Inductance drop 10 %
or 35 °C temperature rise at I dc
Operating Temperature
.....-55 °C to +105 °C
Storage Temperature
.....-55 °C to +105 °C
Moisture Sensitivity Level..... 1
ESD Classification (HBM)..... N/A

Materials

Core Material
5218-RC through 5220-RCIron
5230-RC through 5258-RCFerrite
WireEnameled copper
Terminal Coating.....Sn

Marking

..... Marking on label on box only

Packaging

5218-RC 40 pcs. per box
5219-RC 42 pcs. per box
5220-RC 46 pcs. per box
5230-RC ~ 5258-RC.... 144 pcs. per box

Electrical Schematic



*RoHS Directive 2015/863, Mar 31, 2015 and Annex.
Specifications are subject to change without notice.

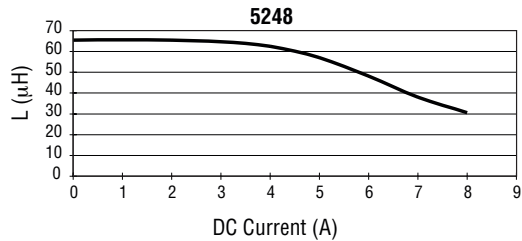
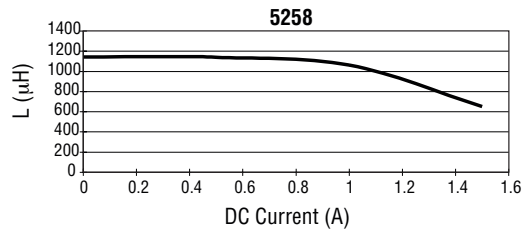
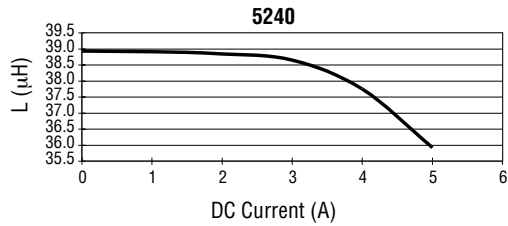
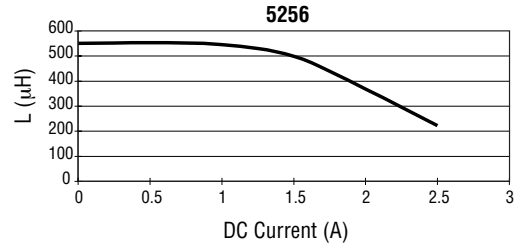
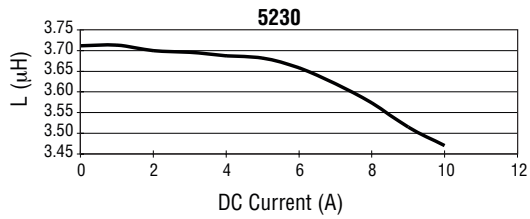
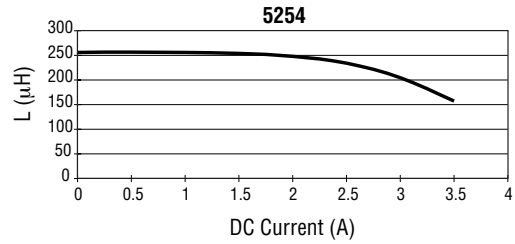
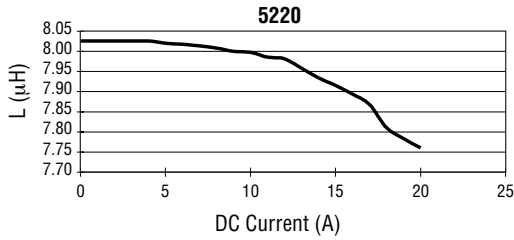
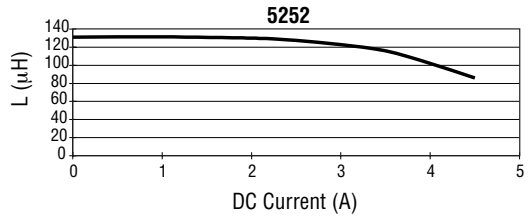
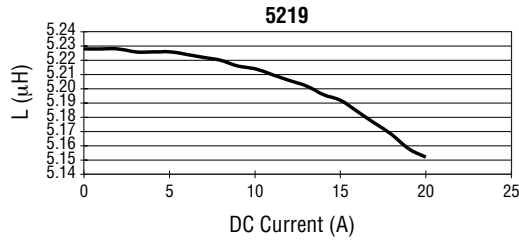
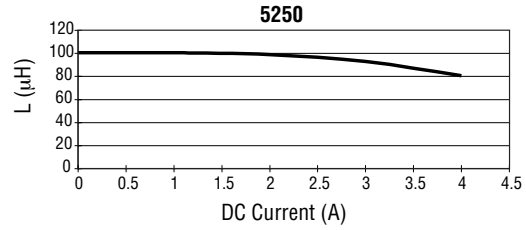
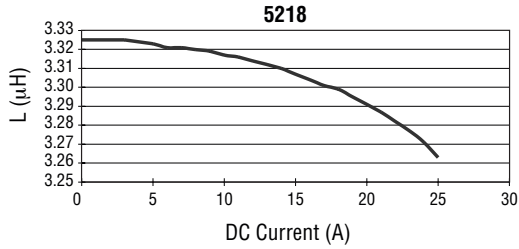
Users should verify actual device performance in their specific applications.

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5200 Series - Hash Choke

BOURNS®

L vs I Graphs



REV. 09/22

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