PT79ST2

Series

2.0 AMP NEGATIVE STEP-DOWN

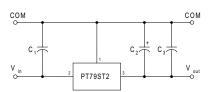
SLTS079 (Revised 5/31/2000)

- High Efficiency
- Self-Contained Inductor
- Short Circuit Protection
- Over-Temperature Protection

The PT79ST2 Series are Negative 3-terminal Integrated Switching Regulators (ISR). These ISRs have a maximum output current of -2.0 Amps and an output voltage that is laser trimmed. They have excellent

line and load regulation with internal short circuit and over-temperature protection. With high conversion efficiency, these ISRs can power a diversity of circuits used in a wide variety of industrial applications.

Standard Application



C1 = Optional ceramic (1µF)

C2 = Required Electrolytic (100µF)

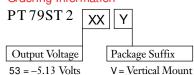
C3 = Optional ceramic $(1-5\mu F)$

Pin-Out Information

Pin	Function
1	GND
2	$-V_{in}$
3	-V _{out}

(For dimensions and PC board layout, see Package Style 500)

Ordering Information



Specifications

Characteristics				PT79ST2 SERIES			
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units	
Output Current	I_{o}	Over V _{in} range	-0.1*	_	-2.0	A	
Short Circuit Current	I_{sc}	$V_{in} = V_o - 3V$	_	-3.5	_	Apk	
Input Voltage Range	V_{in}	$V_{\rm in}$		_	-20	V	
Output Voltage Tolerance ΔV_o Over Vin range, $I_o = -\frac{1}{T_a} = 0^{\circ} C$ to shutdown		Over Vin range, I_0 = -2.0 Amp T_a = 0°C to shutdown	_	±1.0	±3.0	$% V_{o}$	
Line Regulation	Regline	Over V _{in} range	_	40	75	mV	
Load Regulation	Reg _{load} $-0.1 \le I_o \le -2.0 \text{ Amp}$		_	30	50	mV	
V _o Ripple/Noise	V_n	V_{in} = -8V, I_o = -2.0 A, V_o = -5.13V	_	70	_	mV_{pp}	
Transient Response (with req'd output capacitor)			=	100 5	_	μSec %V _o	
Efficiency	η	V_{in} = -10V, I_{o} = -2.0 A, V_{o} = -5.13V	_	85	_	%	
Switching Frequency	f_{\circ}	Over V_{in} range, $I_o = -2.0A$	600	650	700	kHz	
Recommended Operating Temperature Range	T_a	Free Air Convection, (40-60LFM) Over V _{in} and I _o ranges	0	_	+65	°C	
Thermal Resistance	θ_{ja}	Free Air Convection, (40-60LFM)		45		°C/W	
Storage Temperature	T_s	_	-40	_	+125	°C	
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3	_	500	_	G's	
		Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board		5		G's	
Weight	_	_	_	7.0	_	Grams	

 $^{^{\}star}$ ISR will operate down to no load with reduced specifications.



PACKAGE OPTION ADDENDUM

13-May-2005

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins F	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
PT79ST253V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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Mailing Address: Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

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