Description

The Smart Power Relay E-1048-8C.- is a remotely controllable electronic load disconnecting relay with three functions in a single unit:

- electronic relay
- electronic overcurrent protection
- status indication

The 7 pin CUBIC version is designed for use with standard automotive relay sockets. A choice of current ratings is available from 1 A through 25 A. An operating voltage range of DC 9...32 V allows the connection of DC 12 V and DC 24 V loads.

In order to switch and protect loads remotely, it has until now been necessary to connect several discrete components together

- an electro-mechanic relay, control cable and integral contact to close the load circuit
- an additional protective element (circuit breaker or fuse) for
- cable or equipment protection
- a device for current measurement (shunt)

Now type E-1048-8C combines all these functions in a single unit, thus minimising the number of connections in the circuit and thereby reducing the risk of failures.

Applications

Type E-1048-8C. is suited to all applications with DC 12 V or DC 24 V circuits, where magnetic valves, motors or lamp loads have to be switched, protected or monitored:

- road vehicles (utility vehicles, buses, special vehicles)
- rail vehicles
- marine industry (ships, boats, yachts etc.)

The Power Relay is also suitable for industrial use (process control, machine-building, engineering) as an electronic coupling relay between PLC and DC 12 V or DC 24 V load

Features

- Integral power electronics provide a wear-resistant switching function, insensitive to shock and vibration.
- Only a fraction of the control power needed by electro-mechanical relays is required for switching loads. This is important for battery buffered load circuits which have to remain controlled even with the generator off line.
- The extremely low induced current consumption of less than 1 mA is absolutely necessary for battery buffered applications.
- The load circuit is disconnected in the event of an overload or short circuit, the trip curve is also suitable for smaller motor loads.
- The load circuit is permanently monitored for wire breakage.
- Two status outputs for control signal AS and group signal SF provide status indication. For processing the actual value of the current flow in a power management system an analogue output from 0 to 5 V is provided. This voltage signal may also be used as an input to a control circuit or to switch off the unit by means of external control in the event of low load current value.
- For switching and monitoring loads of 25 A plus it is possible to connect several units in parallel. Uniform power distribution between units must be ensured by symmetrical design of the supply cables (length and cross section).
- Coloured label, e. g. red = 10 A, see ordering information.



Technical Data (T_U = 25°C, U_B = DC 24 V) (T_U = ambient temperature at U_N)

Power supply LINE +				
Туре		er supply w		Ri
	battery and generator etc.			
Voltage ratings U _N	DC 12 V / DC 24 V			
Operating voltage U _B :	DC 932 V			
Load circuit LOAD				
Load output	Power MOSFET, high side switching 25 A resistive, inductive, capacitive, lamp			
Max. current rating I _N				
Types of loads		, inductive, lotors (dep		
		current)		duration
Current rating range IN) A (fixed ra	atings)	
		5 °C ambie		it load
		n, 25 A up		
		ic versions	with fact	ory pre-
set ratings: <u>version 1:</u> 1A/2A/3A/5A			3A/5A	7.5 A / 10 A
	version 2: 15 A / 20 A / 25 A			
Induced current consumption				
I_0 of the unit (OFF condition)	< 1 mA			
Typical voltage drop U_{ON} at rated current I_N (at 25 °C)	L.	U _{ON}	I	11
at fated current in (at 25 °C)	I _N	0.1	I _N	U _{ON}
	1 A 2 A	50 mV 55 mV	10 A 15 A	110 mV 70 mV
	3 A	60 mV	20 A	90 mV
	5 A	80 mV	25 A	120 mV
	7.5 A	90 mV		
Switching point	typically	1.3 x I _N		
		+85 °C: 1		
Trip time (standard curve)		200 ms w		
Current limitation	overload and/or load increase on duty <u>version 1</u> : typically 75 A <u>version 2</u> : typically 350 A power transistor >150 °C			
Temperature disconnection				
After trip	 resettable via external control signal (low-high) at control input IN+ 			
				IN+
Parallel connection of channels	- reset of supply voltage Is for loads of 25 A plus, several units of			
	identical current ratings may be			
	connected in parallel. To ensure equal			
	distribution of current between units, symmetrical design of the supply feed			
		rical desigr sary (length		
Leakage current in OFF	13 110065			5 500101 J.
condition	version	<u>1</u> : max. 10	0 μΑ	
		<u>2</u> : max. 50	Ο μΑ	
Free-wheeling diode for connected load	integral	1. mov 40	^	
for connected load	version 1: max. 40 A version 2: max. 100 A			
	<u>+0101011</u>	<u>-</u>		

Т	ec	hni	cal	D	Data (τι	= 25°C, U _B = DC 24 V) (T _U = ambient temperature at U _N)	
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Delay time t _{on} / t _{off} (resistive load)	typ. 5 ms / typ. 1.5 ms (EMC filter in control input)	Temperature ambient tempe
Wire breakage monitoring in ON and OFF	wire breakage thresholds: in OFF-condition (version1):	
condition of load	R_{load} > typically 100 k Ω in OFF-condition (version2):	Tests
	R_{load} > typically 10 k Ω	Humid heat
	in ON-condition: I_{load} < typically 0.2 x I_N	
	indication via group fault signalisation	_
	FM (switching output)	Temperature c
	Fault indication will not be stored, i.e. after remedy of wire breakage fault	
	indication will disappear	Vibration (rand
	(possible options:	,
	- wire breakage indication only in ON	<u>.</u>
	condition	Shock
	 wire breakage indication only in OFF condition 	Corrosion
	- no wire breakage indication)	Protection clas
Short circuit, overload	- disconnection of load, indication via	
in load circuit	group signal SF	
	 no automatic re-start after remedy of the fault unit has to 	EMC requirem
	be reset via control input IN+	
Control input IN+		
Control voltage IN+	05 V = "OFF", 8.532 V = "ON"	
Control current IE	110 mA (8.5DC32V)	Terminals of C
Reset in the event of a failure	- reset via external control signal (low	(7 pin, standa
	 high) at control input IN+ via reset of supply voltage 	
Dimmer operation	possible, see max. switching frequency	
(e.g. PWM signal)		Mounting:
Switching frequency at resistive or inductive load	max. 100 Hz	
Status and diagnostic funct	tions	Housing CUB max. dimensio
Control signal AS	transistor output minus switching (LSS),	
	open collector, short circuit and overload	Materials
	proof, max. load: DC 32 V/2 A 0 V-level: when unit is set	Mass
	0 V-level: when unit is set (at IN+ = 8.432 V)	Mass
Group signal SF	0 V-level: when unit is set (at $IN+ = 8.432$ V) transistor output minus switching (LSS),	Mass Approvals
Group signal SF	0 V-level: when unit is set (at $IN+ = 8.432$ V) transistor output minus switching (LSS), open collector, short circuit and overload	
Group signal SF	0 V-level: when unit is set (at IN + = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A	Approvals
Group signal SF	0 V-level: when unit is set (at $IN+ = 8.432$ V) transistor output minus switching (LSS), open collector, short circuit and overload	Approvals
Group signal SF Analogue output U(I)	0 V-level: when unit is set (at IN + = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit	Approvals
	0 V-level: when unit is set (at IN + = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current:	Approvals
	0 V-level: when unit is set (at IN + = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N	Approvals
	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = $0.2 \times I_N$ 5 V = $1.0 \times I_N$	Approvals
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	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N	Approvals
Analogue output U(I)	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N <u>version 2:</u> \pm 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND	Approvals
Analogue output U(I) Trip times	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N <u>version 2:</u> \pm 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND response time when switching on a load:	Approvals
Analogue output U(I) Trip times definition of t ₉₀	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> ± 5 % of I _N <u>version 2:</u> ± 8 % of I _N max. output current 5 mA load resistance >1 kΩ against GND response time when switching on a load: t ₉₀ = typically 20 ms	Approvals
Analogue output U(I) Trip times	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N <u>version 2:</u> \pm 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND response time when switching on a load:	Approvals
Analogue output U(I) Trip times definition of t ₉₀	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> ± 5 % of I _N <u>version 2:</u> ± 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty:	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> ± 5 % of I _N <u>version 2:</u> ± 8 % of I _N max. output current 5 mA load resistance >1 kΩ against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS group fault signal SF	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N <u>version 2:</u> \pm 8 % of I _N max. output current 5 mA load resistance >1 kΩ against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS group fault signal SF General data	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N <u>version 2:</u> \pm 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms LED yellow LED red	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS group fault signal SF General data Reverse polarity protection	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> ± 5 % of I _N <u>version 2:</u> ± 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms LED yellow LED red	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS group fault signal SF General data	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> \pm 5 % of I _N <u>version 2:</u> \pm 8 % of I _N max. output current 5 mA load resistance >1 k Ω against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms LED yellow LED red	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS group fault signal SF General data Reverse polarity protection Control circuit	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> ± 5 % of I _N <u>version 2:</u> ± 8 % of I _N max. output current 5 mA load resistance >1 kΩ against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms LED yellow LED red	Approvals
Analogue output U(I) Trip times definition of t ₉₀ reached 90% of final value Visual status indication control signal AS group fault signal SF General data Reverse polarity protection Control circuit Load circuit	0 V-level: when unit is set (at IN+ = 8.432 V) transistor output minus switching (LSS), open collector, short circuit and overload proof, load max. DC 32 V/2 A 0 V-level with overload and short circuit disconnection, wire breakage indication voltage output 0-5 V proportional to load current: 1 V = 0.2 x I _N 5 V = 1.0 x I _N 5 V = 1.0 x I _N 5 V typically 6.5 V = overload range tolerance: (for I _{load} > 0.2 x I _N) <u>version 1:</u> ± 5 % of I _N <u>version 2:</u> ± 8 % of I _N max. output current 5 mA load resistance >1 kΩ against GND response time when switching on a load: t ₉₀ = typically 20 ms response time of load change on duty: t ₉₀ = typically 1 ms LED yellow LED red	Approvals

Technical Data (TU = 25°C, UB = DC 24 V) (TU = ambient temperature at UN)

Temperature range ambient temperature	- standard: -40+85 °C	
	without load reduction (60 °C at 25 A)	
	- for other temperature ranges please	
	see ordering key	
Tests		
Humid heat	combined test, 9 cycles with	
	functional test	
Temperature change	test to DIN EN 60068-2-30, Z/AD	
Temperature change	min. temperature -40 °C, max. temperature +90 °C	
	test to DIN IEC 60068-2-14, Nb	
Vibration (random)	in operation, with temperature change	
	6 g eff. (10 Hz2000 Hz)	
	test to DIN EN 60068-2-64	
Shock	25 g/11 ms, 10 shocks	
Osmosian	test to DIN EN 60068-2-27	
Corrosion Protection class	test to DIN EN 60068-2-52, severity 3 housing -8C4 IP30 to DIN 40050	
FIOLECTION Class	housing -8C5 IP54 to DIN 40050,	
	higher protection class upon request	
EMC requirements	EMC directive:	
	emitted interference EN 50081-1	
	noise immunity EN 61000-6-2	
	Automotive directive:	
	emitted interference, noise immunity: 72/245/EW6 und 95 / 54 / E6	
Terminals of CUBIC version		
(7 pin, standard)	5 blade terminals 6.3 mm x 0.8 mm	
	and 2 blade terminals	
	2.8 mm x 0.6 mm to DIN 46244	
	Contact material CuZn37F44	
Mounting:	 on automotive relay socket 7 pole or 9 pole 	
Housing CUBIC		
max. dimensions	30 x 30 x 40 mm when plugged in	
	30 x 30 x 51.6 mm including terminals	
Materials	CUBIC: housing PA66-GF30	
	base plate PA6-GF30	
Mass	approx. 23 g43 g, depending on version	
Approvals		
CE, e1 logo	according to EU, EMC and automotive	
	directives, approvals no. e1 023880	

図画示A Smart Power Relay E-1048-8C. (CUBIC)

Dimensions CUBIC (7 pin version)

30 1.18 footprint to ISO 7588

> 51.6 2.03

Ordeni		
Туре		
E-1048-8C	Smart Power Relay DC 12 V/24 V - 1 A20 A (25 A) in CUBIC housing	with all options: - LED indications AS/SF - signal outputs AS/SF - analogue output U (I)
	Housing / temperature range	- analogue output o (i)
	4 with housing -40 °C85 °C (60 °C at I _N = 25 A)	
	5 with housing -40 °C85 °C (60 °C at I _N = 25 A)	1 3 2 1 1 5 2 4 5 6 and 8 - blade terminals 6 3 x 0.8
	increased environmental	1 3 2 1 2 4 5 , 6 and 8 - blade terminals 6.3 x 0.8 1 and 3 - blade terminals 2.8 x 0.6
	requirements (IP protection class etc.)	5
	Control input	
	C with control input (+ control 8.532 V)	
	0 without	
	3 2 LEDs: AS yellow, SF red	footprint
	Status output minus-switching	`to IŚO 7588
	A without	
	B with control signal AS	
	C with group fault signal SF D with AS and SF	
	D with AS and SF Contents of group fault signal SF/	
	LED indication SF	3 9 9 1 2 2 1 2 2 1
	0 without	2.03
	1 short circuit / overload	
	2 short circuit / overload + wire breakage off	
	3 short circuit / overload + wire breakage on	
	4 short circuit / overload + wire breakage off - wire breakage on	
	off + wire breakage on Analog output	
	V0 without	
	V1 05V	30
	Characteristic curve	1.16
	1 50 ms (switch-off delay with	
	overload)	
	2 100 ms (switch-off delay with overload)	
	4 200 ms standard	
	(switch-off delay with	
	overload)	
	Voltage rating	
	U3 DC 12/24 V	LED yellow LED red
	Current ratings / colour of label	
	1 A / black	
	2 A / grey	Dimensions BASIC (4 pin version)
	3 A / purple	
	5 A / light-brown	
	7.5 A / brown	without options: - LED indication AS/SF
	10 A / red	- signal outputs AS/SF - analogue output U (I)
	15 A / blue 20 A / yellows	
	25 A / white	
		2 2, 4, 6 and 8 - blade terminals 6.3 x 0.8
E-1048-8C		
	ample 1: "DELUXE"-version 7 pin	
E-1048-8C		
ordering exa	ample 2: "BASIC"-version 4 pin	

Ordering Information

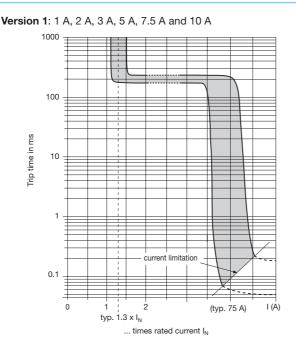
7

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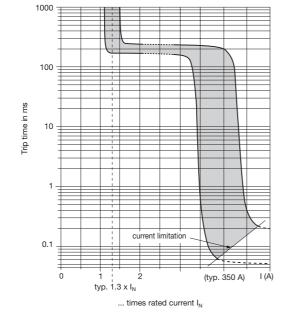
40

30 1.18 0

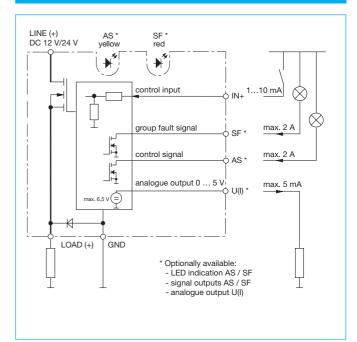
Typical time/current characteristics (standard 200 ms)



Version 2: 15 A, 20 A and 25 A



Connection diagram



Pin selection (7 pin = "DELUXE")

E-1048	-8C.	Cubic	
AS	(1)	control signal ([^] = LED yellow)	
LINE +	(2) (3)	plus U _B (DC 12 V/24 V) group fault signal ([≙] LED red)	3 2 1
IN+	(4)	control input	6 - 4
U(I)	(5)	0 5 V analogue output	
GND	(6)	minus U _B	
LOAD	(8)	load output	

Pin selection (4 pin = "BASIC")

E-1048	-8C.	Cubic	
LINE +	(1) (2) (3)	plus U _B (DC 12 V/24 V)	
IN+ (4)	(5)	control input	6 4
GND	(6)	minus U _B	8
LOAD	(8)	load output	

All dimensions without tolerances are for reference only. In the interest of improved design, performance and cost effectiveness the right to make changes in these specifications without notice is reserved. Product markings may not be exactly as the ordering codes. Errors and omissions excepted.