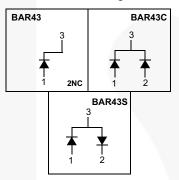


August 2015

# BAR43 / BAR43C / BAR43S Schottky Diodes



#### **Connection Diagram**



### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
BAR43	D95	SOT-23 3L	Tape and Reel
BAR43C	DB2	SOT-23 3L	Tape and Reel
BAR43S	DA5	SOT-23 3L	Tape and Reel

#### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage	30	V
I <sub>F(AV)</sub>	Average Rectified Forward Current	200	mA
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current Pulse Width = 1.0 second	750	mA
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C
T <sub>J</sub>	Operating Junction Temperature	150	°C

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## **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
$P_{D}$	Power Dissipation	290	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	430	°C/W

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>R</sub>	Breakdown Voltage	I <sub>R</sub> = 100 μA	30		V
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 2.0 mA	260	330	mV
		I <sub>F</sub> = 15 mA		450	mV
		I <sub>F</sub> = 100 mA		0.8	V
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 25 V		0.5	μА
		V <sub>R</sub> = 25 V, T <sub>A</sub> = 100°C		100	
t <sub>rr</sub>	Reverse Recovery Time	$I_F = I_R = 10$ mA, $I_{RR} = 1.0$ mA, $I_{L} = 100$ $\Omega$		5.0	ns
Minimum Detection Recovery Time		$I_F = I_R = 10 \text{ mA}, I_{RR} = 1.0 \text{ mA},$ $R_L = 100 \Omega$		80	%

## **Typical Performance Characteristics**

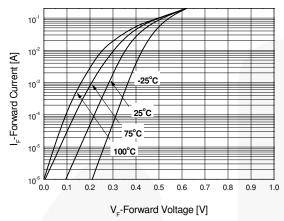


Figure 1. Forward Voltage vs. Temperature

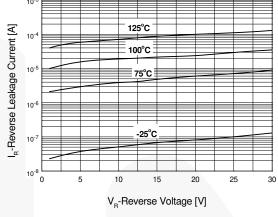


Figure 2. Reverse Leakage Current vs. Temperature

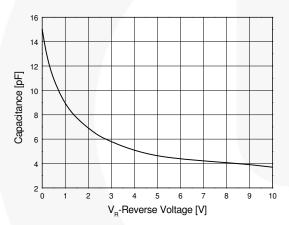


Figure 3. Capacitance vs. Reverse Bias Voltage

## **Physical Dimensions** 0.95 2.92±0.20 3 1.40 1.30<sup>+0.20</sup><sub>-0.15</sub> 2.20 2 0.60 0.37 (0.29) -0.95 ⊕ 0.20 M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A 1.20 MAX 0.10 (0.93)0.10(M) C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H.

A) REFERENCE JEDEC REGISTRATION
TO-236, VARIATION AB, ISSUE H.
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS ARE INCLUSIVE OF BURRS,
MOLD FLASH AND TIE BAR EXTRUSIONS.
D) DIMENSIONING AND TOLERANCING PER
ASME Y14.5M - 1994.
E) DRAWING FILE NAME: MA03DREV10

DETAIL A
SCALE: 2X

Figure 4. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE





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Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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