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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<u>http://www.renesas.com</u>)

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HZU-G Series

Silicon Planar Zener Diode for Surge Absorption

REJ03G1215-0300 Rev.3.00 Jun 08, 2006

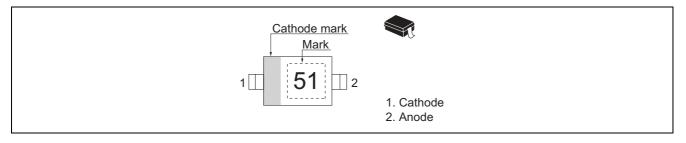
Features

- Zener diode for surge absorption suitable for IEC 1000-4-2.
- Ultra small Resin Package (URP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code	
HZU-G Series	Let to Mark Code	URP	PTSP0002ZA-A	

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

			()
Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Тј	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: See Fig2.

Electrical Characteristics

							$(Ta = 25^{\circ}C)$
Zener Voltage		Reverse Current		Dynamic Resistance		ESD-Capability * ²	
	_	Test		Test		Test	_
V _z (V) * ¹	Condition	I _R (∝A)	Condition	r _d (Ω)	Condition	— (kV) * ²
Min	Max	l _z (mA)	Max	V _R (V)	Max	l _z (mA)	Min
4.84	5.37	5	5	1.5	130	5	30
5.31	5.92	5	5	2.5	80	5	30
5.86	6.53	5	2	3.0	50	5	30
6.47	7.14	5	2	3.5	30	5	30
7.06	7.84	5	2	4.0	30	5	30
7.76	8.64	5	2	5.0	30	5	30
8.56	9.55	5	2	6.0	30	5	30
9.45	10.55	5	2	7.0	30	5	30
11.42	12.60	5	2	9.0	35	5	30
12.47	13.96	5	2	10.0	35	5	30
	Vz (Min 4.84 5.31 5.86 6.47 7.06 7.76 8.56 9.45 11.42	Vz (V)*1 Min Max 4.84 5.37 5.31 5.92 5.86 6.53 6.47 7.14 7.06 7.84 7.76 8.64 8.56 9.55 9.45 10.55 11.42 12.60	Vz (V)*1 Test Condition Min Max Iz (mA) 4.84 5.37 5 5.31 5.92 5 5.86 6.53 5 6.47 7.14 5 7.06 7.84 5 7.76 8.64 5 9.45 10.55 5 11.42 12.60 5	$\begin{array}{c c c c c c c } & Test \\ Condition & I_R (\sim A) \\\hline Min & Max & I_Z (mA) & Max \\\hline 4.84 & 5.37 & 5 & 5 \\\hline 5.31 & 5.92 & 5 & 5 \\\hline 5.86 & 6.53 & 5 & 2 \\\hline 5.86 & 6.53 & 5 & 2 \\\hline 6.47 & 7.14 & 5 & 2 \\\hline 7.06 & 7.84 & 5 & 2 \\\hline 7.76 & 8.64 & 5 & 2 \\\hline 7.76 & 8.64 & 5 & 2 \\\hline 8.56 & 9.55 & 5 & 2 \\\hline 9.45 & 10.55 & 5 & 2 \\\hline 11.42 & 12.60 & 5 & 2 \\\hline \end{array}$	$\begin{tabular}{ c c c c c } \hline Test \\ \hline Condition \\ \hline V_Z(V)^{*1} \\ \hline Test \\ \hline Condition \\ \hline I_R(\mbox{\simA$}) \\ \hline I_R($\sim$A$$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

Notes: 1. Tested with pulse (Pw = 40 ms).

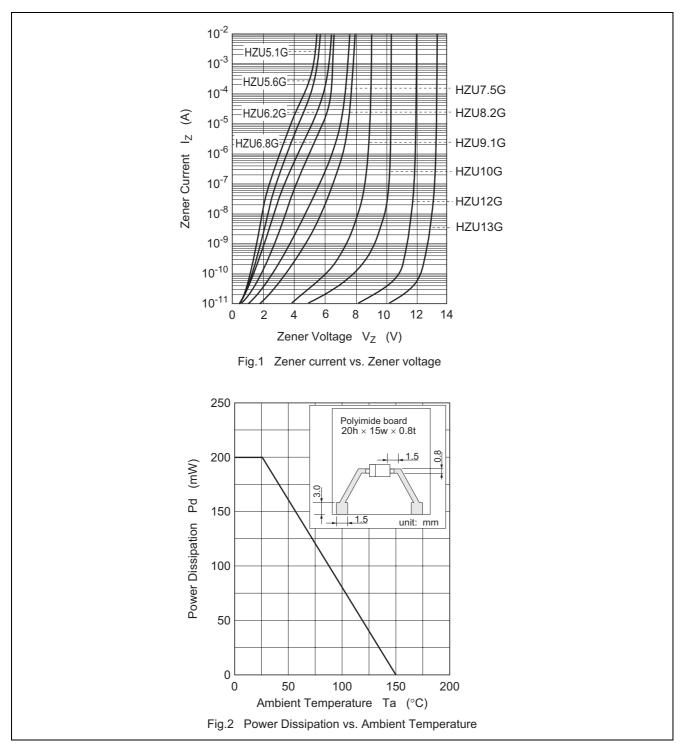
2. C =150 pF, R = 330 Ω , Both forward and reverse direction 10 pulse Failure criterion ; According to IR spec

Mark Code

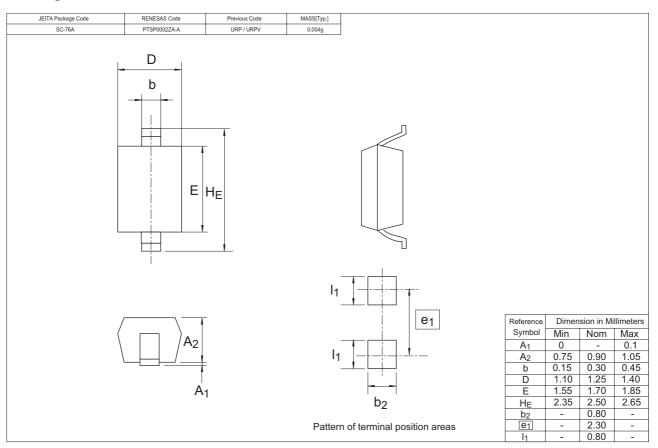
Type No.	Mark No.
HZU5.1G	51
HZU5.6G	56
HZU6.2G	62
HZU6.8G	68
HZU7.5G	75
HZU8.2G	82
HZU9.1G	91
HZU10G	10
HZU12G	12
HZU13G	13



Main Characteristic



Package Dimensions





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