**PNP/NPN Epitaxial Planar Silicon Transistors** 



2SA1520/2SC3914

# Switching Applications (with Bias Resistance)

An ON Semiconductor Company

### **Applications**

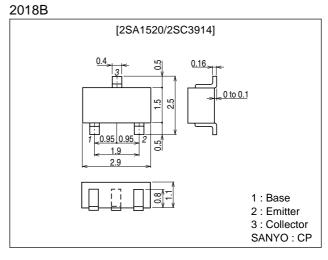
• Switching circuits, inverter circuits, interface circuits, driver circuits.

### **Features**

- · On-chip bias resistance :  $R1=2.2k\Omega$ ,  $R2=10k\Omega$ .
- · Small-sized package : CP.
- $\cdot$  Large current capacity : I<sub>C</sub>=500mA.

## **Package Dimensions**

unit:mm



():2SA1520

# **Specifications**

### Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		(–)50	V
Collector-to-Emitter Voltage	V <sub>CEO</sub>		(–)50	V
Emitter-to-Base Voltage	VEBO		(–)6	V
Collector Current	ΙC		(–)500	mA
Collector Current (Pulse)	ICP		(–)800	mA
Collector Dissipation	PC		200	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings			Unit
Falanielei			min	typ	max	Unit
Collector Cutoff Current	ICBO	$V_{CB}=(-)40V, I_{E}=0$			(–)0.1	μΑ
	ICEO	V <sub>CE</sub> =(-)40V, I <sub>B</sub> =0			(–)0.5	μΑ
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =(-)5V, I <sub>C</sub> =0	(–)315	(–)410	(–)590	μΑ
DC Current Gain	hFE	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)10mA	50			
Gain-Bandwidth Product	fT	V <sub>CE</sub> =(-)10V, I <sub>C</sub> =(-)5mA		250		MHz
				(200)		MHz
				<u> </u>	1	

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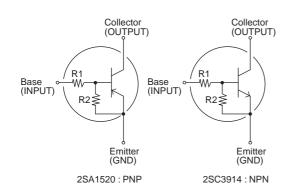
SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

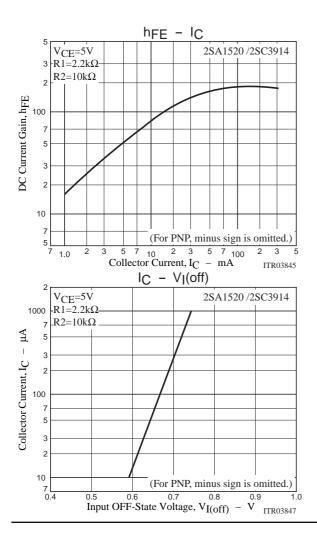
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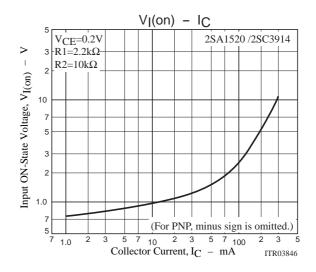
Parameter	Cumhal	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =(-)10V, f=1MHz		3.7		pF
				(5.5)		pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	I <sub>C</sub> =(-)50mA, I <sub>B</sub> =(-)2.5mA		(-)0.1	(–)0.3	V
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =(-)10μA, I <sub>E</sub> =0	(–)50			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =(−)100µA, R <sub>BE</sub> =∞	(–)50			V
Input OFF-State Voltage	V <sub>I(off)</sub>	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)100µA	(-)0.5	(-)0.67	(–)0.9	V
Input ON-State Voltage	V <sub>I(on)</sub>	V <sub>CE</sub> =(-)0.2V, I <sub>C</sub> =(-)50mA	(-)0.7	(–)1.6	(–)3.0	V
Input Resistance	R1		1.5	2.2	2.9	kΩ
Resistance Ratio	R1/R2		0.198	0.22	0.242	

Marking 2SA1520 : NL, 2SC3914 : VY

### **Electrical Connection**







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