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Vishay Semiconductors

RoHS

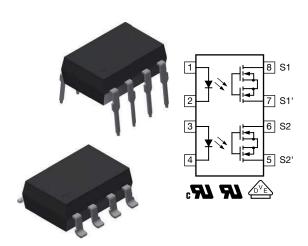
COMPLIANT

HALOGEN FREE

**GREEN** 

(5-2008)

# **Dual 1 Form A Solid-State Relay (Normally Open)**



### **LINKS TO ADDITIONAL RESOURCES**







#### **DESCRIPTION**

The VOR2121 is a 250 V dual channel normally open optically isolated solid-state relay (SPST - 1 form A). Based on hybrid architecture which allows fast switching times with a wide operating ambient temperature range. A high efficient GaAlAs IRED enables low forward current on the input side. On the output side high performance MOSFET switches provide a low  $R_{\text{ON}}$  and can switch both DC and AC signals.

#### **FEATURES**

- Isolation test voltage 5300 V<sub>RMS</sub>
- Typical R<sub>ON</sub> 12 Ω
- Load voltage 250 V
- Load current 200 mA / 140 mA
- · Clean bounce free switching
- Low power consumption
- Wide temperature range
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **APPLICATIONS**

- · General telecom switching
- Metering
- Security equipment
- Instrumentation
- · Industrial controls
- Battery management systems
- · Automatic test equipment

#### **AGENCY APPROVALS**

- <u>UL 1577</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)

ORDERING INFORMATION	
V O R 2 1 2 1  PART NUMBER	# 8 #  PACKAGE CONFIGURATION
PACKAGE	UL, cUL, VDE
SMD-8, tape and reel	VOR2121B8T
SMD-8, tube	VOR2121B8
DIP-8, tube	VOR2121A8



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
INPUT				
IRED continuous forward current		I <sub>F</sub>	50	mA
IRED reverse voltage		$V_{R}$	5	V
Input power dissipation		P <sub>diss</sub>	80	mW
Junction temperature		T <sub>j</sub>	125	°C
OUTPUT				
DC or peak AC load voltage		V <sub>L</sub>	250	V
Continuous DC load current at 25 °C, one channel		IL	200	mA
Continuous DC load current at 25 °C, two channels		IL	140	mA
SSR output power dissipation		P <sub>diss</sub>	550	mW
Junction temperature		Tj	125	°C
SSR				
Ambient temperature range		T <sub>amb</sub>	-40 to +100	°C
Storage temperature range		T <sub>stg</sub>	-40 to +150	°C
Soldering temperature	t = 10 s max.	T <sub>sld</sub>	260	°C

#### Note

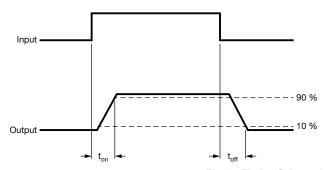
Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
IRED forward current, switch turn-on	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I <sub>Fon</sub>	ı	0.4	2	mA
IRED forward current, switch turn-off	$V_{L} = \pm 200 \text{ V}$	I <sub>Foff</sub>	0.05	0.35	-	mA
IRED forward voltage	I <sub>F</sub> = 10 mA	$V_{F}$	-	1.36	1.5	V
IRED reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	ı	-	10	μA
OUTPUT						
On-resistance	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	R <sub>ON</sub>	ı	12	15	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R <sub>OFF</sub>	1.0	5000	-	GΩ
Off state leaders assument	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	I <sub>O</sub>	ı	< 1	100	nA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 200 \text{ V}$	Io	ı	< 1	500	nA
Outt	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}, 1 \text{ MHz}$	C <sub>O</sub>	ı	39	-	pF
Output capacitance pin 3 to 4	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}, 1 \text{ MHz}$	Co	ı	6	-	pF
Current limit AC/DC	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	I <sub>limit</sub>	300	440	550	mA
TRANSFER						
Capacitance (input to output)	V <sub>IO</sub> = 1 V	C <sub>IO</sub>	ı	0.4	-	pF

### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t <sub>on</sub>	-	0.20	0.5	ms
Turn-off time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t <sub>off</sub>	-	0.03	0.2	ms



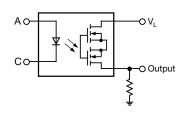
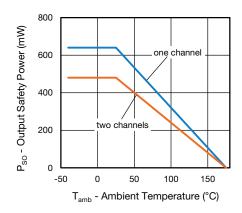


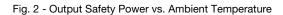
Fig. 1 - Timing Schematic

PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 100 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	5300	$V_{RMS}$
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	890	V <sub>peak</sub>
landation maintage	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Insulation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Output safety power	One channel	ם	640	mW
	Two channels	$P_{SO}$	480	
lanut asfati a umant	One channel		240	A
Input safety current	Two channels	I <sub>SI</sub> 200		mA
Safety temperature		T <sub>S</sub>	175	°C
Creepage distance	DIP-8		≥ 7	mm
Clearance distance	DIP-6		≥ 7	mm
Creepage distance	SMD-8		≥ 8	mm
Clearance distance	21VID-0		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	V <sub>IORM</sub> x 1.875 = V <sub>PR</sub> , 100 % production test with t <sub>M</sub> = 1 s, partial discharge < 5 pC	V <sub>PR</sub>	1669	V <sub>peak</sub>
Input to output test voltage, method A	$V_{IORM}$ x 1.6 = $V_{PR}$ , 100 % sample test with $t_M$ = 10 s, partial discharge < 5 pC	V <sub>PR</sub>	1424	V <sub>peak</sub>

### Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits





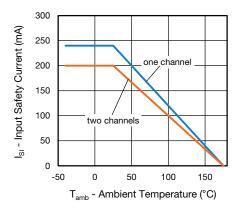


Fig. 3 - Input Safety Current vs. Ambient Temperature

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

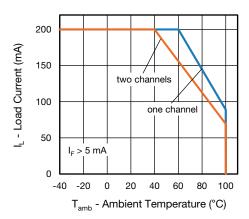


Fig. 4 - Load Current vs. Ambient Temperature

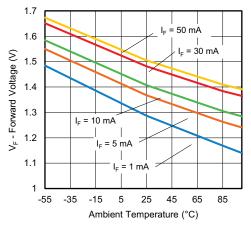


Fig. 5 - Forward Voltage vs. Ambient Temperature

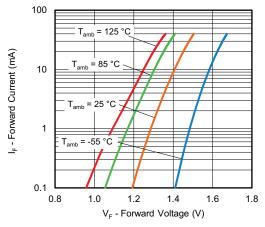


Fig. 6 - Forward Current vs. Forward Voltage

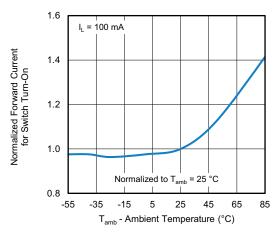


Fig. 7 - Normalized Forward Current vs. Ambient Temperature

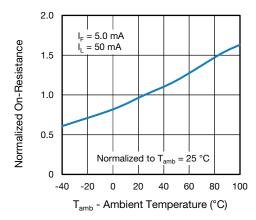


Fig. 8 - Normalized On-Resistance vs. Ambient Temperature

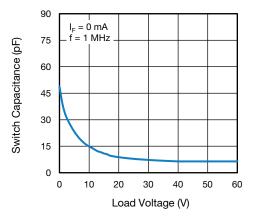


Fig. 9 - Switch Capacitance vs. Load Voltage

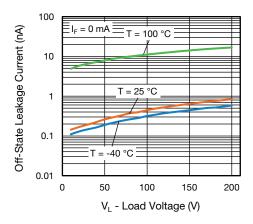


Fig. 10 - Leakage Current vs. Load Voltage

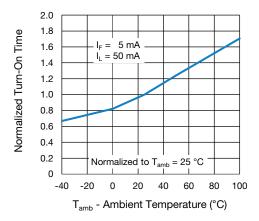


Fig. 11 - Normalized Turn-On Time vs. Ambient Temperature

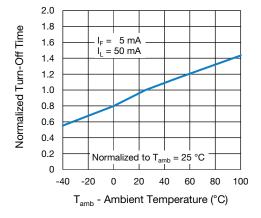


Fig. 12 - Normalized Turn-Off Time vs. Ambient Temperature

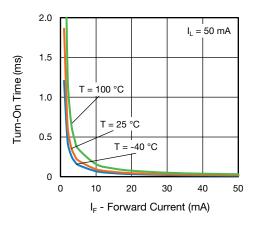


Fig. 13 - Turn-On Time vs. Forward Current

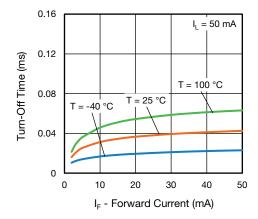
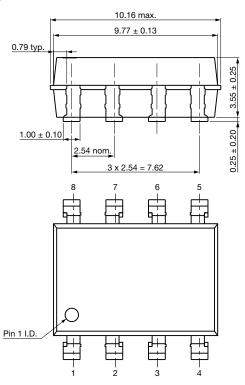


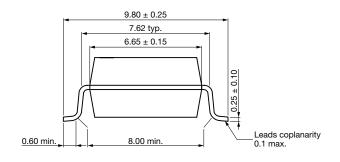
Fig. 14 - Turn-Off Time vs. Forward Current

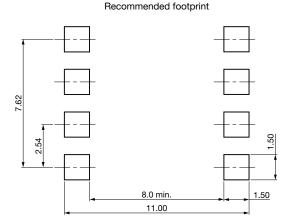


### **PACKAGE DIMENSIONS** in millimeters

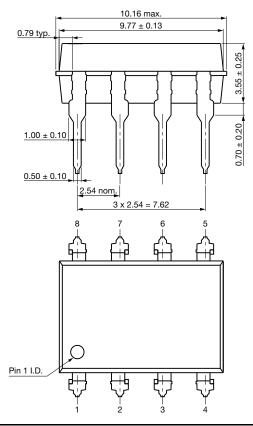
### SMD-8

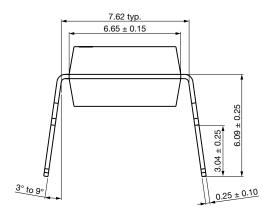






DIP-8





### **PACKAGE MARKING** (example)

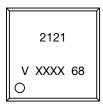


Fig. 15 - VOR2121

#### **Notes**

- XXXX = LMC (lot marking code)
- Tape and reel suffix (TR) is not part of the package marking

### **PACKING INFORMATION** (in millimeters)

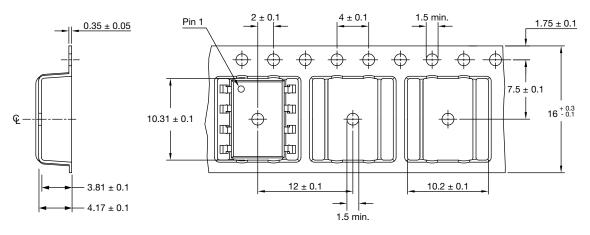


Fig. 16 - Tape and Reel Packing

TAPE AND REEL PACKING		
TYPE	UNITS/REEL	
SMD-8	1000	

TUBE PACKING			
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX
SMD-8	50	40	2000
DIP-8	50	40	2000



### **SOLDER PROFILES**

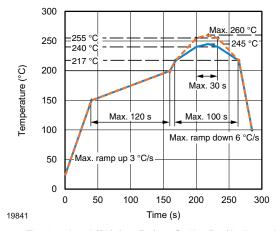


Fig. 17 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

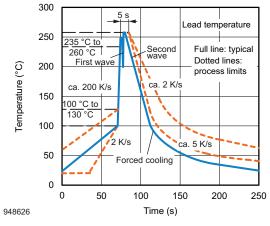


Fig. 18 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



# **Footprint and Schematic Information**

Vishay Semiconductors

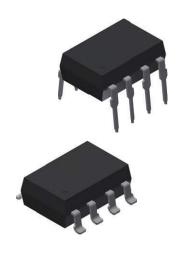
# **Footprint and Schematic Information for VOR2121**

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC	
VOR2121A8	www.snapeda.com/parts/VOR2121A8/Vishay/view-part	
VOR2121B8	www.snapeda.com/parts/VOR2121B8/Vishay/view-part	
VOR2121B8T	www.snapeda.com/parts/VOR2121B8T/Vishay/view-part	

For technical issues and product support, please contact optocoupleranswers@vishay.com.





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