# NSR05402

## 500 mA, 40 V Schottky Barrier Diode

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current and are offered in a Chip Scale Package (CSP) to reduce board space. The low thermal resistance enables designers to meet the challenging task of achieving higher efficiency and meeting reduced space requirements.

#### Features

- Low Forward Voltage Drop 570 mV (Typ.) @ I<sub>F</sub> = 500 mA
- Low Reverse Current 3.0  $\mu$ A (Typ.) @ V<sub>R</sub> = 40 V
- ESD Rating Human Body Model: Class 3B – Machine Model: Class C
- High Switching Speed
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping and Protection

#### MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Reverse Voltage	Reverse Voltage		40	V
Forward Current (DC)		١ <sub>F</sub>	500	mA
Forward Surge Current (60 Hz @ 1 cycle)		I <sub>FSM</sub>	8.0	A
Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%)		I <sub>FRM</sub>	1.8	A
ESD Rating:	Human Body Model Machine Model	ESD	>8.0 >400	kV V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

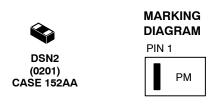


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### 40 V SCHOTTKY BARRIER DIODE

1 0 2 CATHODE ANODE



P = Specific Device Code M = Date Code

#### **ORDERING INFORMATION**

Device	Package	Shipping†
NSR05402NXT5G	DSN2 (Pb-Free)	5000 / Tape & Reel

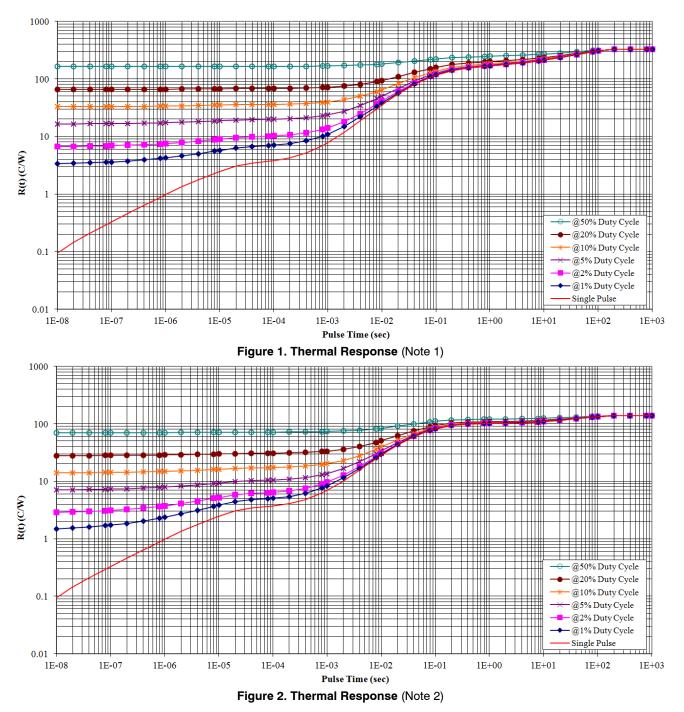
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ $T_A = 25^{\circ}C$	R <sub>θJA</sub> P <sub>D</sub>			329 380	°C/W mW
Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			140 895	°C/W mW
Storage Temperature Range	T <sub>stg</sub>			-40 to +125	°C
Junction Temperature	TJ			+150	°C

1. Mounted onto a 4 in square FR-4 board 50 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

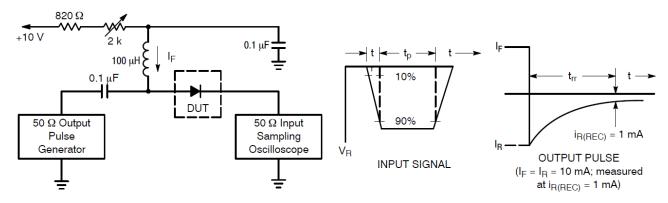
2. Mounted onto a 4 in square FR-4 board 650 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.



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#### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

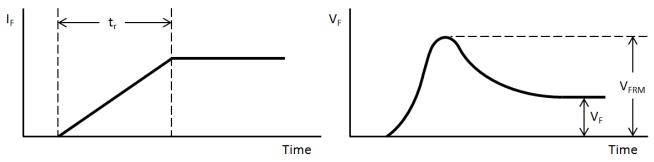
	'				
Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage $(V_R = 10 V)$ $(V_R = 40 V)$	۱ <sub>R</sub>		0.3 3.0	4.0 20	μΑ
Forward Voltage $(I_F = 0.1 \text{ mA})$ $(I_F = 1 \text{ mA})$ $(I_F = 10 \text{ mA})$ $(I_F = 100 \text{ mA})$ $(I_F = 200 \text{ mA})$ $(I_F = 500 \text{ mA})$	VF		180 230 310 400 450 570	220 255 350 440 480 620	mV
Total Capacitance $(V_R = 5.0 \text{ V}, \text{ f} = 1.0 \text{ MHz})$	C <sub>T</sub>		9.5		pF
Reverse Recovery Time ( $I_F = I_R = 10 \text{ mA}, I_{R(REC)} = 1.0 \text{ mA}$ ), Figure 3	trr		5.9		ns
Peak Forward Recovery Voltage (V <sub>R</sub> = 1.0 V, f = 1.0 MHz), Figure 4	V <sub>FRM</sub>		558		mV



Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA. 2. Input pulse is adjusted so I<sub>R(peak)</sub> is equal to 10 mA.

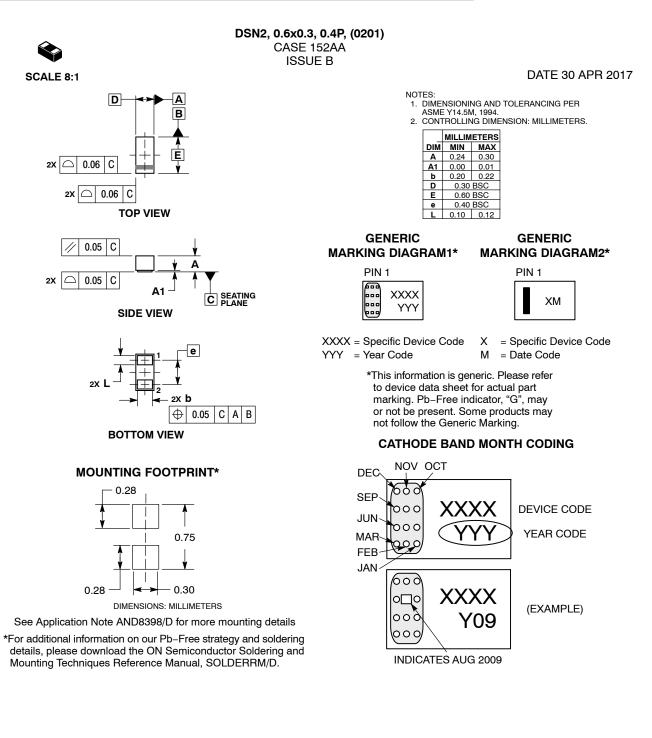


#### Figure 3. Recovery Time Equivalent Test Circuit









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DESCRIPTION:	DSN2, 0.6X0.3, 0.4P, (0201)		PAGE 1 OF 1			
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