Unit: mm

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TOSHIBA Photocoupler PHOTORELAY

TLP3241

Measurement Instruments Logic IC Testers / Memory Testers **Board Testers / Scanners**

The TOSHIBA TLP3241 is a super small-outline photorelay, suitable for surface-mount assembly. The TLP3241 consists of an infrared emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Its characteristics also include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measuring instruments.

Features

4 pin SSOP (SSOP4)

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- 1-Form-A
- Peak off-state voltage
- Trigger LED current
- On-state current
- On-state resistance
- Output capacitance
- Isolation voltage
- UL-recognized

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Schematic 10-

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- : 1.8 mm high, 1.27 mm pitch
- : 40 V (min) : 3 mA (max)
- : 140 mA (max)
- $: 10\Omega(\text{max}), 7\Omega(\text{typ.})$

2

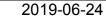
- : 1.3 pF (max), 0.7 pF (typ.)
- : 1500 Vrms (min)
- : UL 1577, File No.E67349

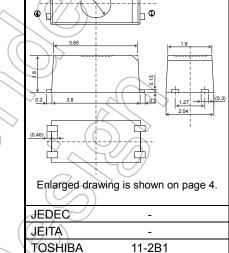
Pin configuration (top view)

1 : Anode 2 : Cathode 3 : Drain 4 : Drain

> Start of commercial production 2007-08

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Weight: 0.03 g (typ.)

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o 4

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	30	mA
	Forward current derating (Ta≥25°C)	∆I _F /°C	-0.3	mA/°C
Q	Reverse voltage	VR	5	V _
LED	Diode power dissipation	PD	50	mW
	Diode power dissipation derating (Ta ≥25°C)	∆P _D /°C	-0.5	mW/°C
	Junction temperature	Tj	125	°C
	Off-State output terminal voltage	Voff	40	VV
	On-State current	ION	140	mA
Detector	On-State current derating (Ta≥25°C)	∆lon/°C	-1.4	mA/°C
Dete	Output power dissipation	Po	196	mV
	Output power dissipation derating (Ta \ge 25°C)	ΔP _o /°C	-1.96	mW / °C
	Junction temperature	Tj	125	°C
Stora	ge temperature range	T _{stg}	-40 to 125	°C
Oper	ating temperature range	Topr	-20 to 85	°C
Lead	soldering temperature (10 s)	T _{sol}	260	°C
Isola	tion voltage (AC, 60 s, R.H.≤ 60 %) (Note 1)	BVs	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

(Note 1): Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Precautions

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VDD	_	—	32	V
Forward current	IF	_	—	20	mA
Operating temperature	T _{opr}	25		60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	$I_F = 5 \text{ mA}$	1.15	1.30	1.45	V
LED	Reverse current	I _R	$V_R = 5 V$	_	_	10	μA
	Capacitance	Ст	V _F = 0 V, f = 1 MHz	_	30	_	pF
Detector	Off-state current	IOFF	Voff = 35 V	_	10	200	pА
Dete	Capacitance	COFF	V = 0 V, f = 100 MHz, t < 1 s	_	0.7	1.3	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 100 mA	—	_	3	mA
Return LED current	I _{FC}	$I_{OFF} = 1 \ \mu A$	0.1			mA
On-state resistance	Ron	I _{ON} = 140 mA, I _F = 5 mA, t < 1 s	X	7	10	Ω

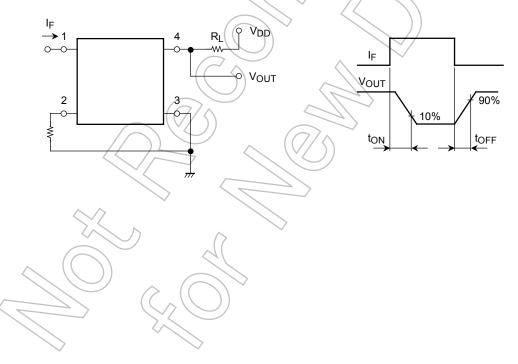
Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition Min Typ. Max	Unit					
Capacitance input to output	CS	V _S = 0 V, f = 1 MHz – 0.6 –	pF					
Isolation resistance	Rs	$V_{S} = 500 \text{ V}, \text{ R.H.} \le 60 \text{ \%}$ $5 \times 10^{10} \text{ 10}^{14} $	Ω					
Isolation voltage	BVs	AC, 60 s 1500	Vrms					
vitching Characteristics (Ta = 25°C)								

Switching Characteristics (Ta = 25°C)

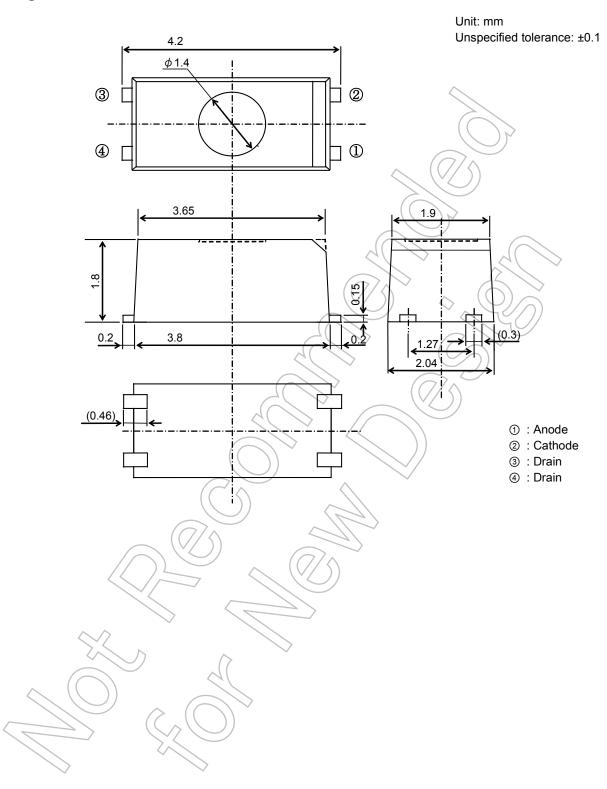
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton	R _L = 200 Ω	(Note 2)	26	200	
Turn-off time	tOFF	V _{DD} = 10 V, I _F ≠ 5 mA	(γ)	45	200	μS

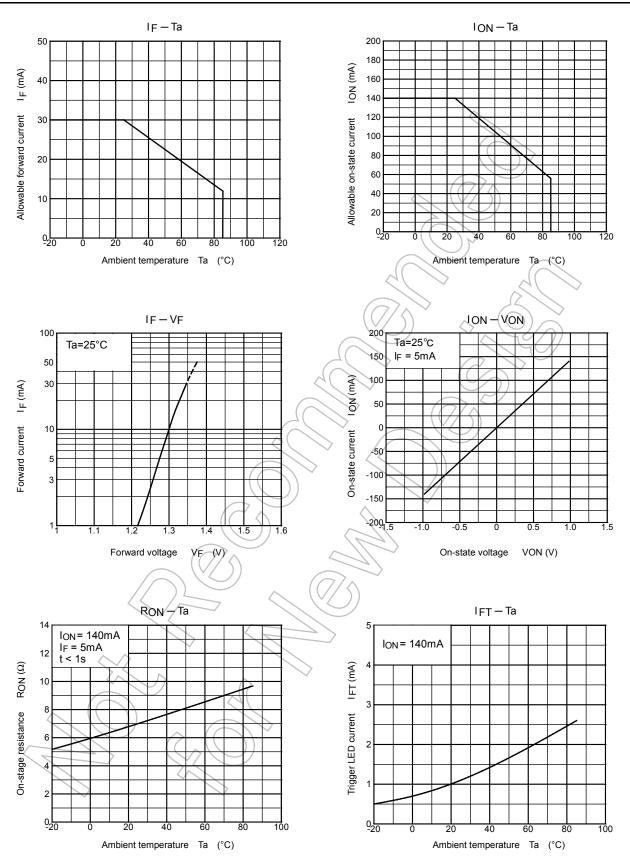
(Note 2): switching time test circuit



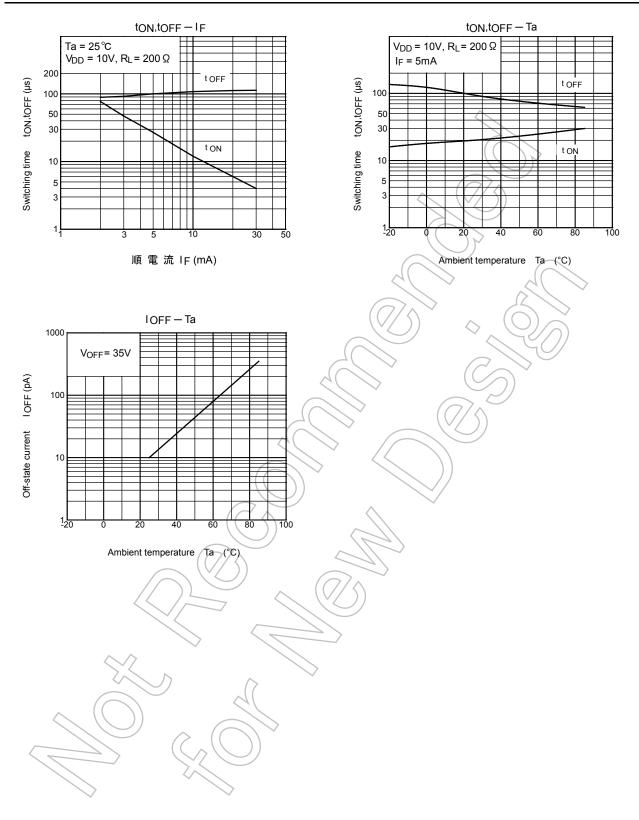
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Package Dimensions





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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