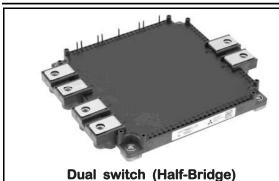


< IGBT MODULES >

CM600DXL-24S

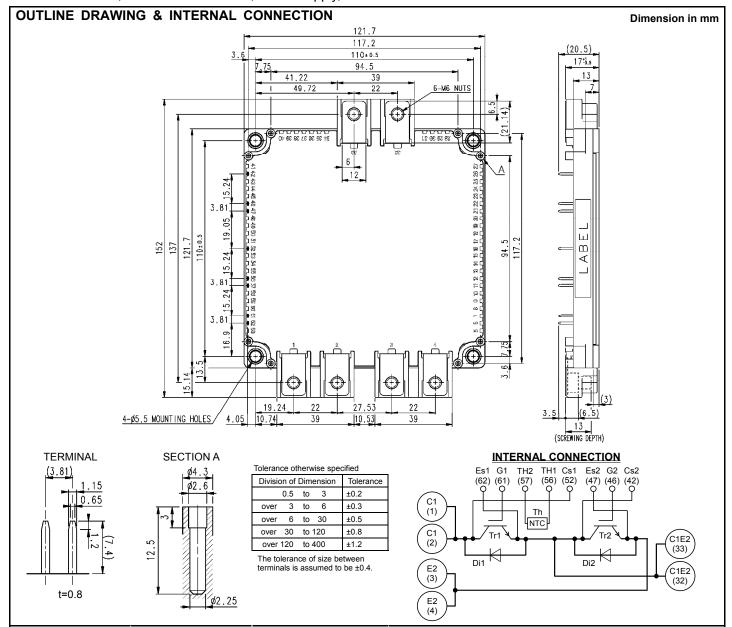
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



< IGBT MODULES > CM600DXL-24S HIGH POWER SWITCHING USE INSULATED TYPE

MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T _C =119 °C (Note2, 4)	600	Λ	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	1200	A	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	4545	W	
I _E (Note1)	Emitter current	DC (Note2, 4)	600	۸	
I _{ERM} (Note1)	Enliner current	Pulse, Repetitive (Note3)	1200	Α	

MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{sta}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =60 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =600 A, V _{GE} =15 V,	T _j =25 °C	-	1.85	2.30	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.05	-	V
(Terrilliai)	Collector-emitter saturation voltage	(Note5)	T _j =150 °C	-	2.10	-	
.,	Collector-entitler saturation voltage	I _C =600 A,	T _j =25 °C	-	1.70	2.15	
V _{CEsat} (Chip)		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
(Criip)		(Note5)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	60	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	12	nF
Cres	Reverse transfer capacitance			-	-	1.0	
Q _G	Gate charge	V _{CC} =600 V, I _C =600 A, V _{GE} =15 V		-	1400	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =600 A, V _{GE} =±15 V,			-	800	ns
t _r	Rise time	V _{CC} =600 V, I _C =600 A, V _{GE} =±15 V,		-	-	200	
t _{d(off)}	Turn-off delay time	B =0 O Industrias load		-	-	600	IIS
t _f	Fall time	R_G =0 Ω, Inductive load		-	-	300	
(Note1)		-	T _j =25 °C	-	1.85	2.30	V
V _{EC} (Note1)			T _j =125 °C	-	1.85	-	
(Terminal)	Emitter collector valtage	(Note5)	T _j =150 °C	-	1.85	-	
(Note1)	Emitter-collector voltage	I _E =600 A,	T _j =25 °C	-	1.70	2.15	
V _{EC} (Note1)		G-E short-circuited,	T _j =125 °C	-	1.70	-	V
(Chip)		(Note5)	T _j =150 °C	-	1.70	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =600 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =0 Ω, Inductive load		-	32	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =600 A,		-	20.3	-	m l
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R_{G} =0 Ω , T_{j} =150 °C,		-	60.1	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	69.2	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	-	0.8	mΩ
r _g	Internal gate resistance	Per switch		-	3.3	-	Ω

< IGBT MODULES > CM600DXL-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_j =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	33	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	-	63	K/kW
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7)	-	7	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Condition	Conditions		Limits		
Syllibol	item	Conditions		Min.	Тур.	Max.	Unit
M_t	Manuation at Assess	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
m	mass	-		-	690	-	g
ds	Creepage distance	Terminal to terminal		13.69	-	-	- mm
u _s		Terminal to base plate		15.7	-	-	
d	Clearance	Terminal to terminal		13.69	-	-	- mm
d _a		Terminal to base plate		14.88	-	-	
e _c	Flatness of base plate	On the centerline X, Y (Note8))	±0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

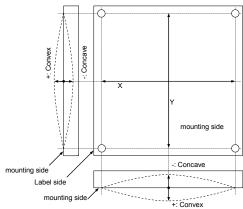
- 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{imax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
 Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs. " ϕ 2.6×10 or ϕ 2.6×12 B1 tapping screw"

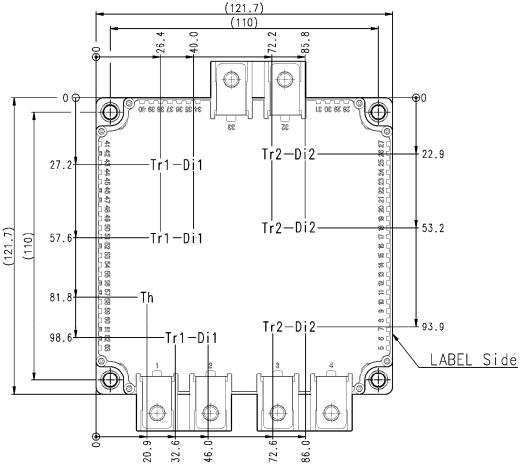
The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

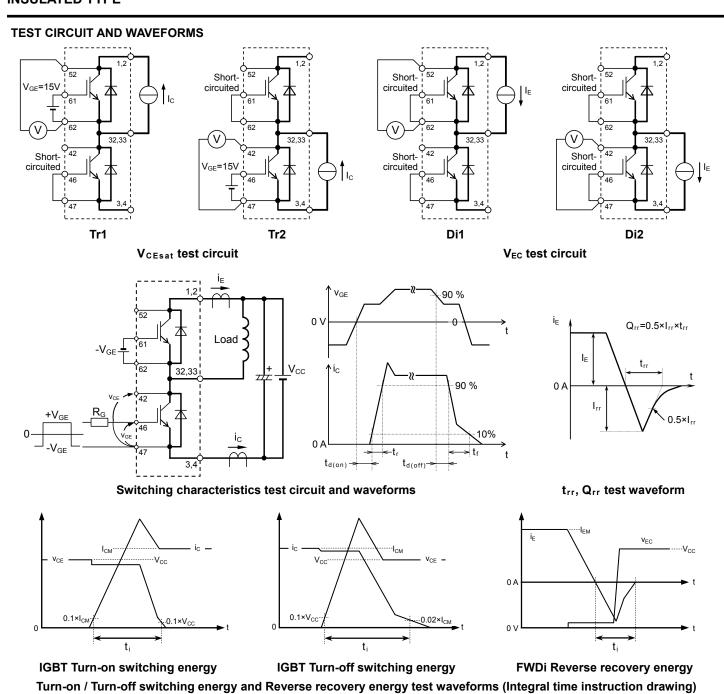
Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max. 850	Offic
V _{CC}	(DC) Supply voltage	Applied across C1-E2	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	6.8	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

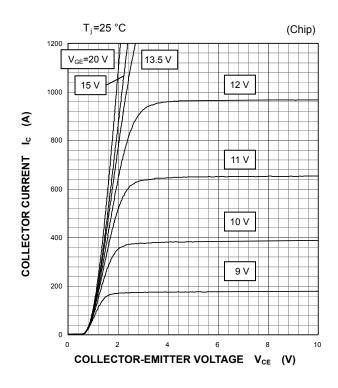


Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

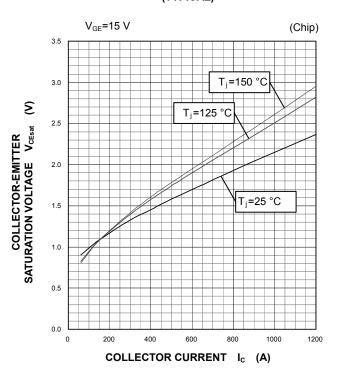


INVERTER PART

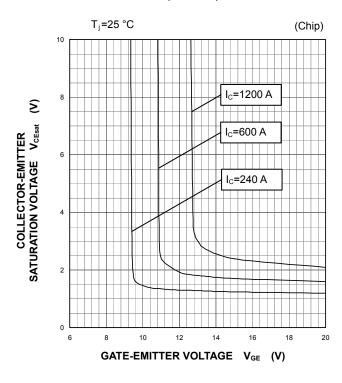
OUTPUT CHARACTERISTICS (TYPICAL)



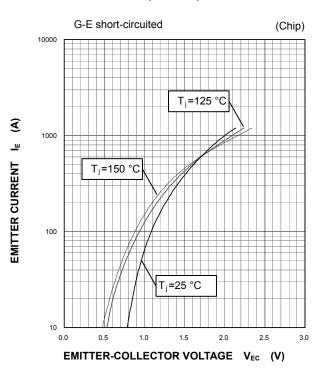
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



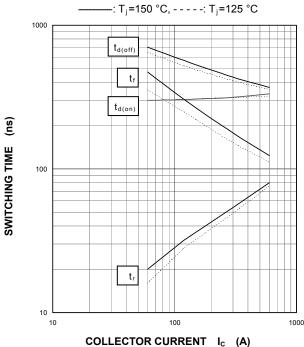
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



INVERTER PART

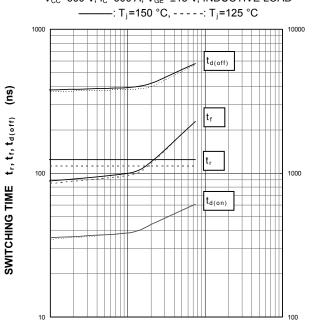
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{\text{CC}}\text{=}600~\text{V},\,V_{\text{GE}}\text{=}\pm15~\text{V},\,R_{\text{G}}\text{=}0~\Omega,\,\text{INDUCTIVE LOAD}$



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, I_{C} =600 A, V_{GE} =±15 V, INDUCTIVE LOAD



(us)

SWITCHING TIME td(on)

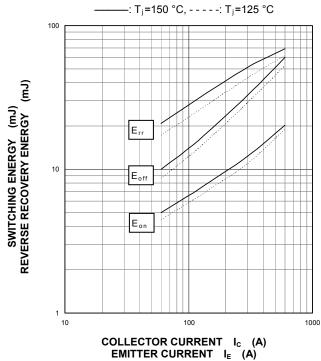
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REVERSE RECOVERY ENERGY

EXTERNAL GATE RESISTANCE R_G

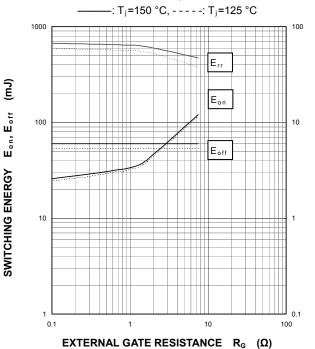
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD, PER PULSE



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

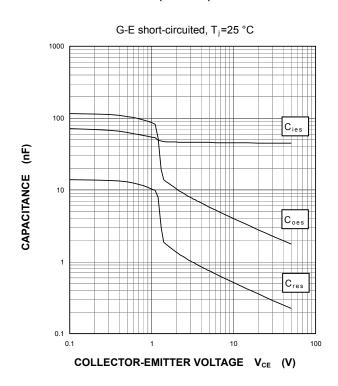
 V_{CC} =600 V, I_C/I_E =600 Å, V_{GE} =±15 V, INDUCTIVE LOAD, PER PULSE



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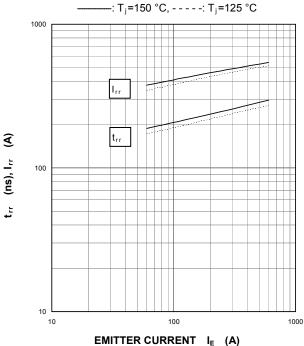
INVERTER PART

CAPACITANCE CHARACTERISTICS (TYPICAL)

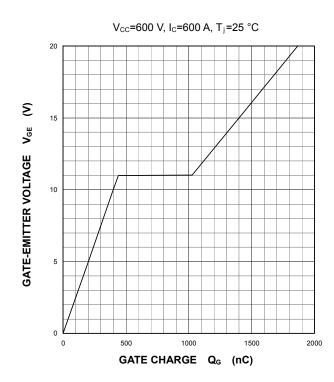


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD: T_i =150 °C, ----: T_i =125 °C

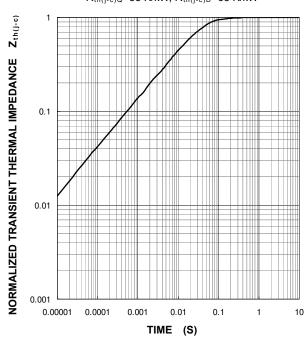


GATE CHARGE CHARACTERISTICS (TYPICAL)



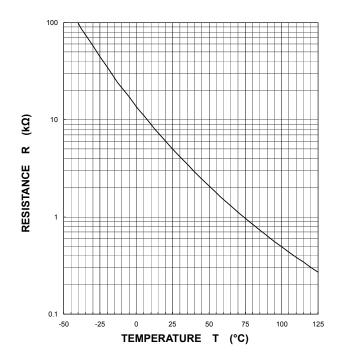
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C =25 °C $R_{th(j-c)Q}$ =33 K/kW, $R_{th(j-c)D}$ =63 K/kW



NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



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