



## OCXO Model: OS400-1005-016

### Issue 2; 11th May 2022

#### **Features**

- Temperature stability to ±20ppb
- Low phase noise
- Frequency 10MHz
- Industry standard package
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements



- Temperature stability: ±20ppb over (-40 to +70)°C
- Output: Sinewave +7dBm nominal Harmonics: -20dBc Voltage: 12.0V Warm up current: 220mA **Ouiescent current:** 120mA



FO<sub>0</sub>+10Hz -125 dBc/Hz F0<sub>0</sub>+100Hz -145 dBc/Hz F0<sub>0</sub>+1KHz -155 dBc/Hz F0<sub>0</sub>+10KHz -165 dBc/Hz -168 dBc/Hz F0<sub>0</sub>+100KHz

# Voltage / Load change

- ±5% supply voltage change: ±2ppb
- ±10% load change: ±10ppb

Values based on a 10MHz unit

### Ageing

Bases on 10MHz unit after 30 days continuous operation:

- Per day: ±0.7ppb max. Per year: ±200ppb max.
- Warm up time: 5 minutes to within 1 ppm

### Voltage Trim

- ±0.5ppm minimum
- Trim impedance  $50K\Omega$

### **Reference Options**

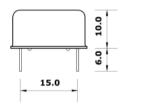
4.5V

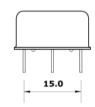
### Environmental

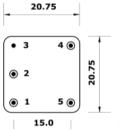
- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range: (-40 to +125)°C



### Dimensions (mm)







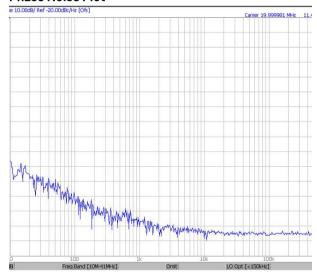
#### PIN CONNECTIONS

#1: Vcc #2: Output #3: Ground/Case Trim/Tune

#5: Vref if fitted

Pins viewed from the bottom pin diameter 0.45 mm

### **Phase Noise Plot**



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- Mechanical shock: MIL standard 202F, method 213, condition J
- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B
- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

### Compliance

- RoHS Status (2011/65/EU) Compliant
- REACH Status Compliant

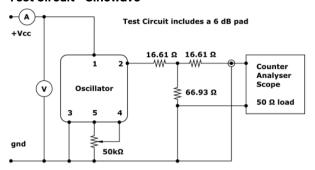
#### **Packaging**

■ Pack Style: Bulk

### **Ordering Information**

- Unique customer part number and custom specification issued with each application
- OCXO Model: 0S400-1005-016
- Frequency: 10MHz
- Stability/Output/Voltage: Option C
- Supply voltage code: V3 = +12.0Vd.c. supply
- Add suffix (R) for Vref output on pin #5

### **Test Circuit - Sinewave**



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