DISCRETE SEMICONDUCTORS

DATA SHEET

PHE13007 Silicon Diffused Power Transistor

Product specification

February 2018



Silicon Diffused Power Transistor

PHE13007

GENERAL DESCRIPTION

The PHE13007 is a silicon npn power switching transistor in the TO220AB envelope intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

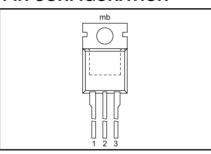
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	700	V
V _{CBO}	Collector-Base voltage (open emitter)		-	700	V
V _{CEO}	Collector-emitter voltage (open base)		-	400	V
V _{EBO}	Emitter-Base voltage $(I_B = 0)$		-	9	V
I _C	Collector current (DC)		_	8	Α
1 17	Collector current peak value		-	16	A
Piot	Total power dissipation	$T_{mb} \le 25 ^{\circ}C$	-	80	W
P _{tot} V _{CEsat}	Collector-emitter saturation voltage	$I_{\rm C} = 5.0 \text{A}; I_{\rm B} = 1.0 \text{A}$	0.35	2.0	V
t, CLSat	Fall time	$I_{\rm C} = 5 \text{A}; I_{\rm B1} = 1 \text{A}$	40	120	ns

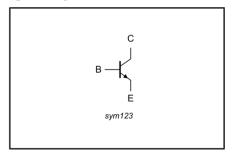
PINNING - TO220AB

PIN	DESCRIPTION
1	base
2	collector
3	emitter
tab	collector

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CESM}	Collector to emitter voltage	$V_{BE} = 0 V$	-	700	V
V _{CEO}	Collector to emitter voltage (open base)		-	400	V
V_{CBO}	Collector to base voltage (open emitter)		-	700	V
V _{EBO}	Emitter-Base voltage $(I_B = 0)$		-	9	V
I _C	Collector current (DC)		-	8	Α
I _{CM}	Collector current peak value		-	16	Α
I _B	Base current (DC)		-	4	Α
I _{BM}	Base current peak value		-	8	Α
P _{tot}	Total power dissipation	$T_{mb} \le 25 ^{\circ}C$	-	80	W
T _{stq}	Storage temperature		-65	150	°C
T _i	Junction temperature		-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th i-mb}	Junction to mounting base		ı	1.56	K/W
R _{th j-a}	Junction to ambient	in free air	60	-	K/W

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STATIC CHARACTERISTICS

 $T_{mb} = 25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES}	Collector cut-off current ¹	V _{BE} = 0 V; V _{CE} = V _{CESMmax} V _{BE} = 0 V; V _{CE} = V _{CESMmax} ; T _i = 125 °C	1 1		0.2 1.0	mA mA
${f V}_{\sf CEOsust}$	Emitter cut-off current Collector-emitter sustaining voltage	$V_{EB} = 9 \text{ V; } I_{C} = 0 \text{ A}$ $I_{B} = 0 \text{ A; } I_{C} = 10 \text{ mA;}$ $I_{C} = 25 \text{ mH}$	400	- -	1.0	mA V
V _{CEsat} V _{CEsat} V _{CEsat}	Collector-emitter saturation voltage	$\begin{aligned} & _{C} = 2.0 \text{ A;} _{B} = 0.4 \text{ A} \\ & _{C} = 5.0 \text{ A;} _{B} = 1.0 \text{ A} \\ & _{C} = 5.0 \text{ A;} _{B} = 1.0 \text{ A} \\ & _{C} = 100 ^{\circ} \text{C}) \end{aligned}$	-	0.15 0.35 0.51	1.0 2.0 3.0	V V
V _{BEsat} V _{BEsat} V _{BEsat}	Base-emitter saturation voltage	$\begin{aligned} I_{C} &= 2.0 \text{ A}; I_{B} = 0.4 \text{ A} \\ I_{C} &= 5.0 \text{ A}; I_{B} = 1.0 \text{ A} \\ I_{C} &= 5.0 \text{ A}; I_{B} = 1.0 \text{ A} \\ (T_{C} &= 100^{\circ}\text{C}) \end{aligned}$	-	0.92 1.05 1.00	1.2 1.6 1.5	V V
h _{FE} h _{FEsat}	DC current gain	$I_{C} = 2.0 \text{ A}; V_{CE} = 5 \text{ V}$ $I_{C} = 5.0 \text{ A}; V_{CE} = 5 \text{ V}$	8 5	17 9	40 30	

DYNAMIC CHARACTERISTICS

 $T_{mb} = 25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = -I_{Boff} = 1 \text{ A}; R_L = 75 \text{ ohms}; V_{BB2} = 4 \text{ V};$			
t _s	Turn-off storage time Turn-off fall time	$N_L = 75$ Offinis, $V_{BB2} = 4$ V,	1.8 0.3	3.0 0.7	μs μs
	Switching times (inductive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = 1 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}$			
t _s	Turn-off storage time Turn-off fall time	-V _{BB} = 3 V	1.2 40	2.0 120	μs ns
	Switching times (inductive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = 1 \text{ A}; L_{B} = 1 \mu\text{H}; \\ -V_{BB} = 5 \text{ V}; T_{i} = 100 \text{ °C}$			
t _s	Turn-off storage time Turn-off fall time	VBB - 5 V, 1 - 100 C	1.6 100	3.0 200	μs ns

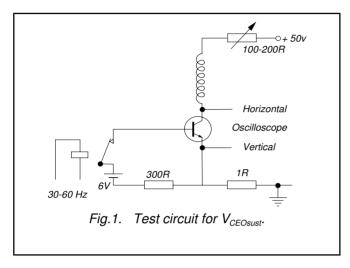
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¹ Measured with half sine-wave voltage (curve tracer).

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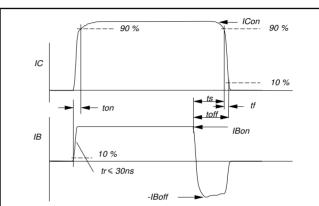
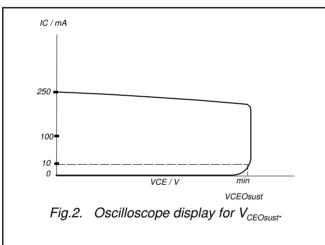
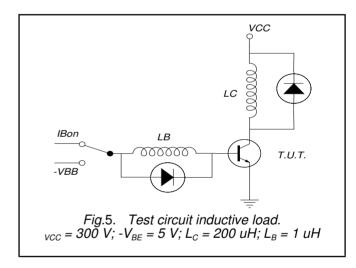
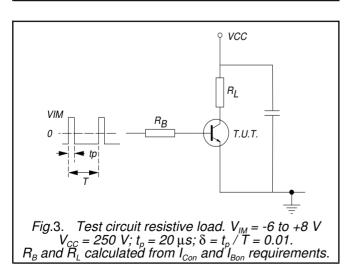
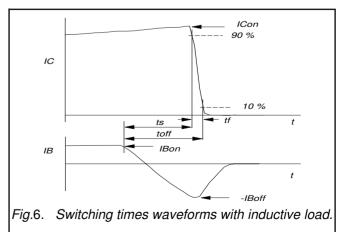


Fig.4. Switching times waveforms with resistive load.



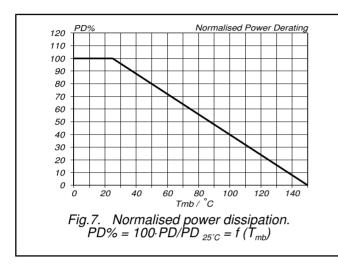


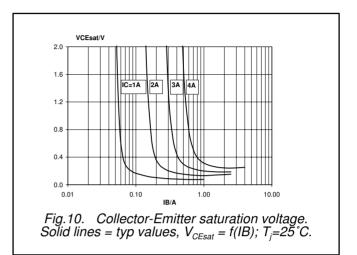


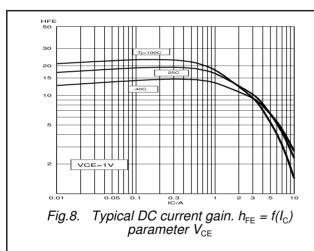


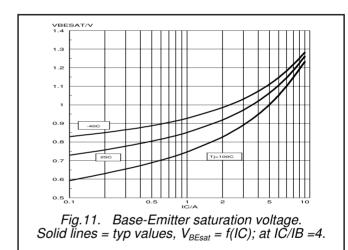
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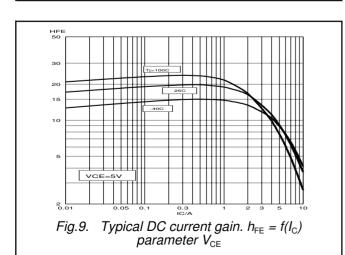
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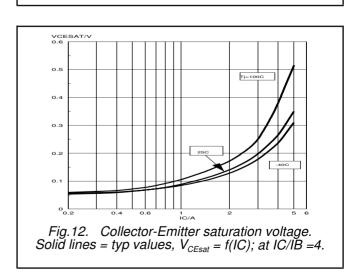








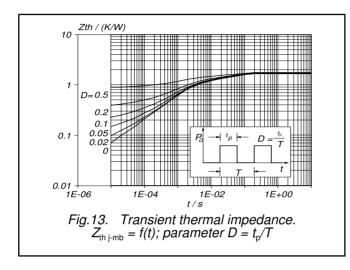




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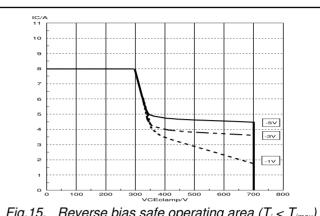


Fig.15. Reverse bias safe operating area $(T_j < T_{jmax})$ for $-V_{BE} = 5V, 3V \& 1V$.

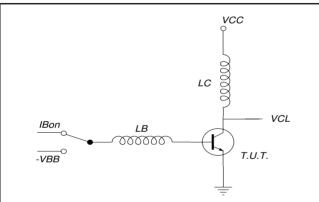


Fig.14. Test circuit for reverse bias safe operating area.

$$\begin{split} V_{clamp} < 700V; \ V_{cc} = 150V; \ -V_{be} = 5V, 3V \ \& \ 1V; \\ L_B = 1 \mu H; \ L_C = 200 \mu H. \end{split}$$

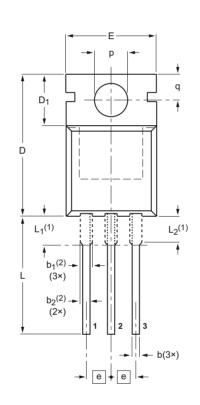
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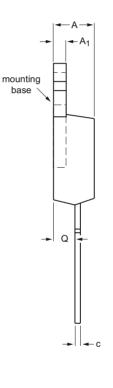
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MECHANICAL DATA

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78







DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁	b	b ₁ ⁽²⁾	b ₂ ⁽²⁾	С	D	D ₁	E	е	L	L ₁ ⁽¹⁾	L ₂ ⁽¹⁾ max.	р	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

Notes

- Lead shoulder designs may vary.
 Dimension includes excess dambar.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE		
SOT78		3-lead TO-220AB	SC-46		08-04-23 08-06-13		

Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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