Nch 45V 20A Power MOSFET

| V _{DSS} | 45V |
|----------------------------|------|
| R _{DS(on)} (Max.) | 28mΩ |
| I _D | ±20A |
| P _D | 20W |

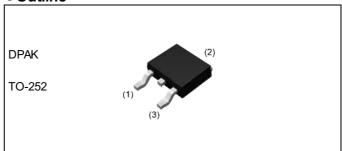
● Features

- 1) Low on resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free lead plating; RoHS compliant

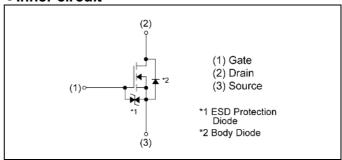
Application

Switching

Outline



Inner circuit



Packaging specifications

| | Packing | Embossed Tape |
|------|---------------------------|------------------|
| | Reel size (mm) | 330 |
| | Tape width (mm) | 16 |
| Type | Basic ordering unit (pcs) | 2500 |
| | Taning and | TL |
| | Taping code | TL1 |
| | Marking | RD3H200SN |

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

| Parameter | Symbol | Value | Unit |
|--|--------------------|-------------|------|
| Drain - Source voltage | V _{DSS} | 45 | V |
| Continuous drain current | I _D *1 | ±20 | Α |
| Pulsed drain current | I _{DP} *2 | ±40 | Α |
| Gate - Source voltage | V _{GSS} | ±20 | V |
| Power dissipation | P _D *3 | 20 | W |
| Junction temperature | Tj | 150 | °C |
| Operating junction and storage temperature range | T _{stg} | -55 to +150 | °C |

●Thermal resistance

| Parameter | Symbol | Values | | | Lleit |
|-------------------------------------|----------------------|--------|------|------|-------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} *3 | - | ı | 6.25 | °C/W |

● Electrical characteristics (T_a = 25°C)

| Daramatar | Symbol Conditions | | Values | | | Limit | |
|--|---|--|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Drain - Source breakdown voltage | V _{(BR)DSS} | V _{GS} = 0V, I _D = 1mA | 45 | - | - | V | |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | I _D = 1mA referenced to 25°C | - | 46.8 | - | mV/°C | |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 45V, V _{GS} = 0V | - | - | 1 | μA | |
| Gate - Source leakage current | I _{GSS} | V_{GS} = ±20V, V_{DS} = 0V | - | - | ±10 | μA | |
| Gate threshold voltage | V _{GS(th)} | V _{DS} = 10V , I _D = 1mA | 1.0 | - | 2.5 | V | |
| Gate threshold voltage temperature coefficient | $\frac{\Delta V_{GS(th)}}{\Delta T_j}$ | I _D = 1mA referenced to 25°C | - | -3.9 | - | mV/°C | |
| | | V _{GS} = 10V, I _D = 20A | - | 20 | 28 | | |
| Static drain - source on - state resistance | R _{DS(on)} *4 | V _{GS} = 4.5V, I _D = 20A | - | 25 | 35 | mΩ | |
| | | V _{GS} = 4V, I _D = 20A | - | 28 | 40 | | |
| Gate resistance | R _G | f = 1MHz, open drain | - | 5.3 | - | Ω | |
| Forward Transfer Admittance | Y _{fs} *4 | V _{DS} = 10V, I _D = 20A | 10 | - | - | S | |

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*3} T_C=25°C

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

| Davamatav | Cy made al | Conditions | Values | | | l leit | |
|------------------------------|------------------------|-----------------------------------|--------|------|------|--------|--|
| Parameter | Symbol Conditions | | Min. | Тур. | Max. | Unit | |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 950 | - | | |
| Output capacitance | C _{oss} | V _{DS} = 10V | - | 250 | - | pF | |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | 1 | 120 | - | | |
| Turn - on delay time | t _{d(on)} *4 | $V_{DD} \simeq 25V, V_{GS} = 10V$ | 1 | 10 | - | | |
| Rise time | t _r *4 | I _D = 10A | - | 20 | - | no | |
| Turn - off delay time | t _{d(off)} *4 | $R_L \simeq 2.5\Omega$ | - | 50 | - | ns | |
| Fall time | t _f *4 | $R_G = 10\Omega$ | - | 20 | - | | |

• Gate charge characteristics $(T_a = 25^{\circ}C)$

| | \ a | , | | | | |
|----------------------|--------------------|---|--------|------|------|--------|
| Parameter | Symbol | Conditions | Values | | | l leit |
| raianietei | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Total gate charge | Qg*4 | V _{DD} ≃ 25V. | - | 12 | - | |
| Gate - Source charge | Q _{gs} *4 | $V_{DD} \approx 25V$, $I_D = 20A$, | - | 3.5 | - | nC |
| Gate - Drain charge | Q _{gd} *4 | V _{GS} = 5V | - | 4.0 | - | |

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Darameter | Symbol Conditions | | Values | | | l leit | |
|----------------------------|--------------------|--|--------|------|------|--------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Continuous forward current | I _S *1 | T = 25°C | - | - | 16 | Α | |
| Pulse forward current | I _{SP} *2 | T _a = 25°C | - | - | 40 | Α | |
| Forward voltage | V _{SD} *4 | V _{GS} = 0V, I _S = 20A | - | - | 1.2 | V | |

Fig.1 Power Dissipation Derating Curve

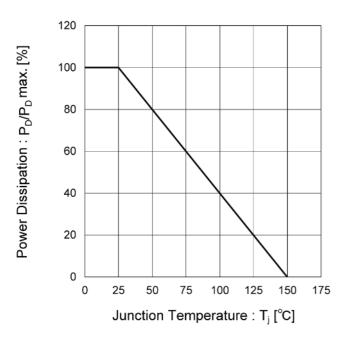


Fig.2 Maximum Safe Operating Area

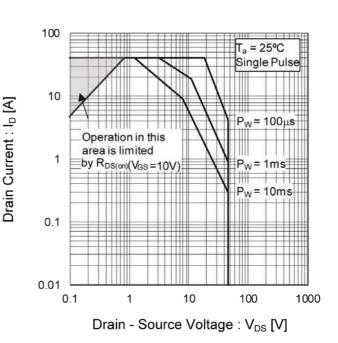


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

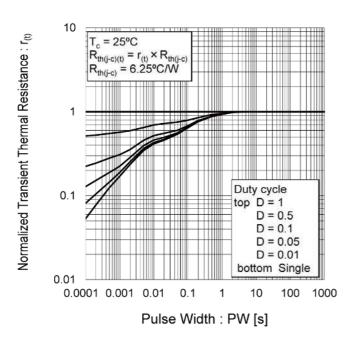


Fig.4 Single Pulse Maximum Power dissipation

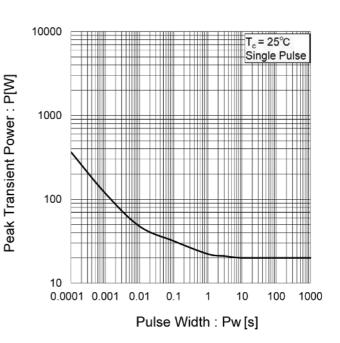


Fig.5 Typical Output Characteristics(I)

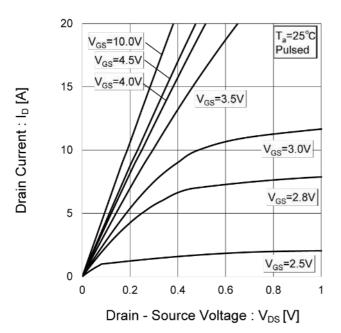
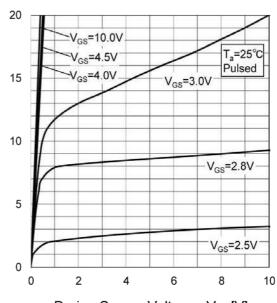


Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

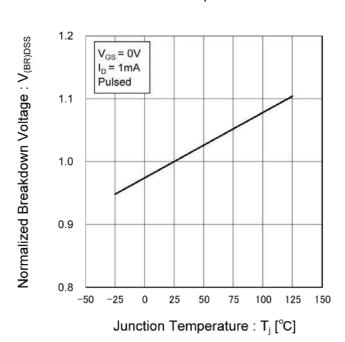


Fig.8 Typical Transfer Characteristics

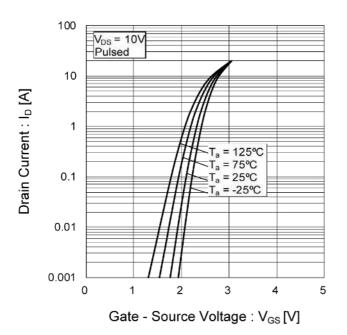


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

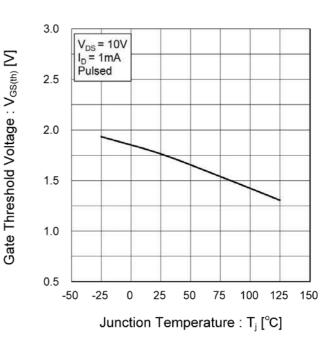


Fig.10 Forward Transfer Admittance vs.
Drain Current

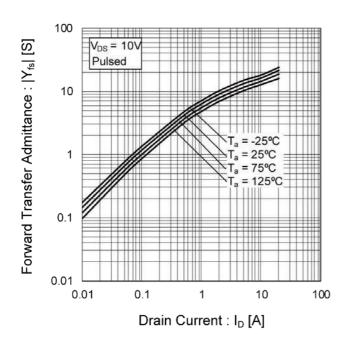


Fig.11 Drain Current Derating Curve

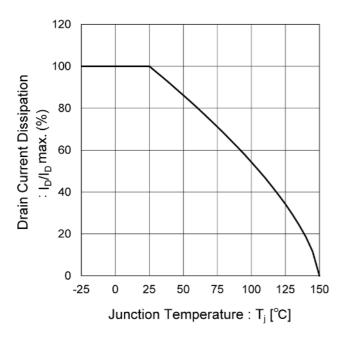


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

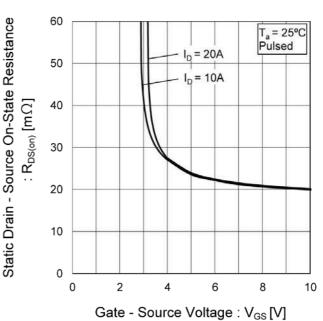


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

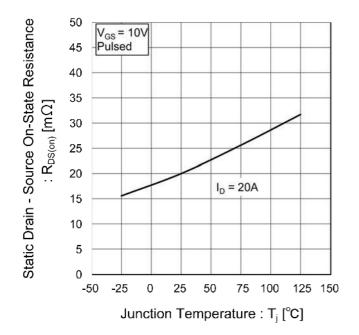


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

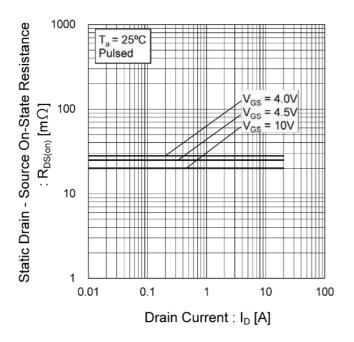


Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current(II)

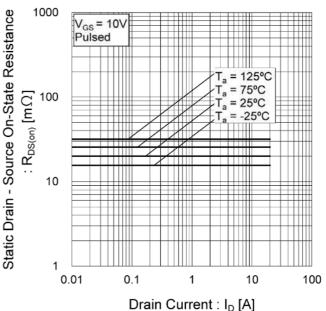


Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current(III)

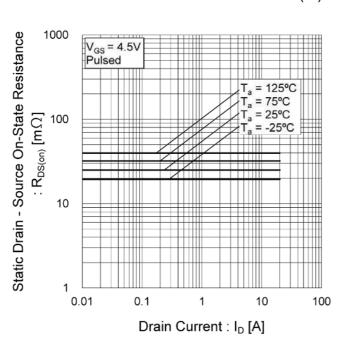
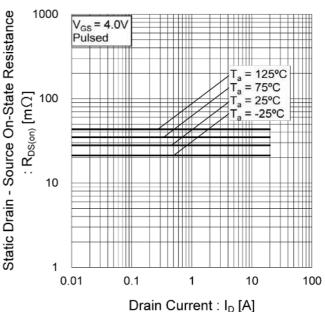


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)



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Fig.18 Typical Capacitance vs.

Drain - Source Voltage

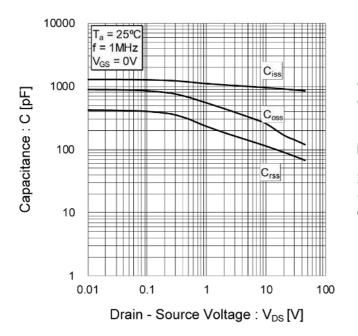


Fig.19 Switching Characteristics

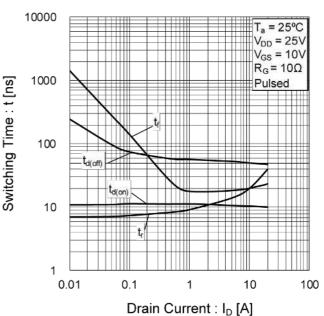


Fig.20 Dynamic Input Characteristics

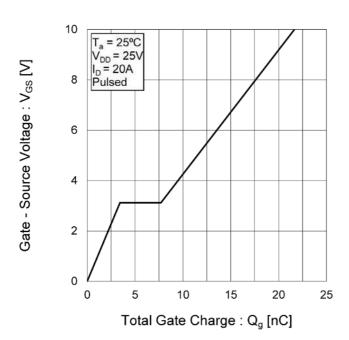
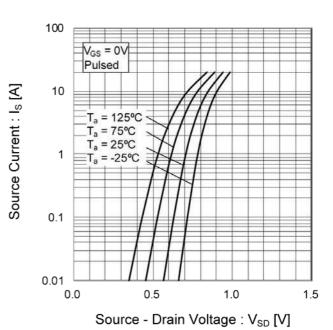


Fig.21 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

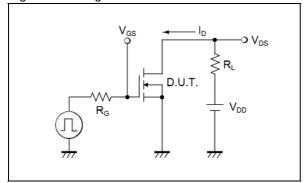


Fig.2-1 Gate Charge Measurement Circuit

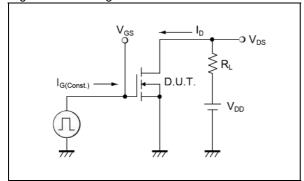


Fig.1-2 Switching Waveforms

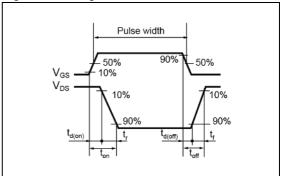
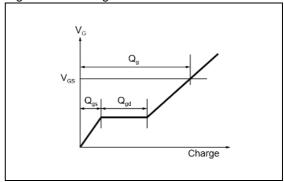
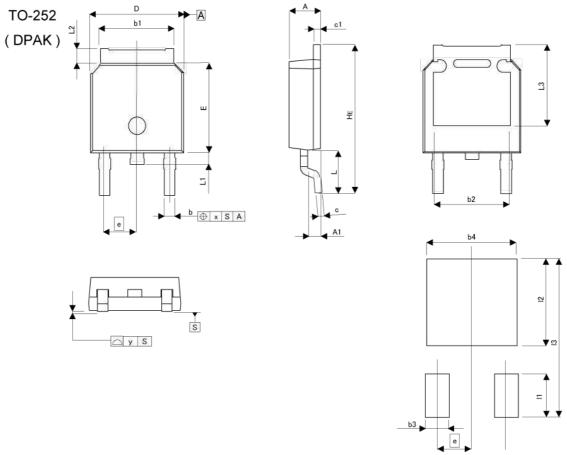


Fig.2-2 Gate Charge Waveform



ullet Dimensions (TL)



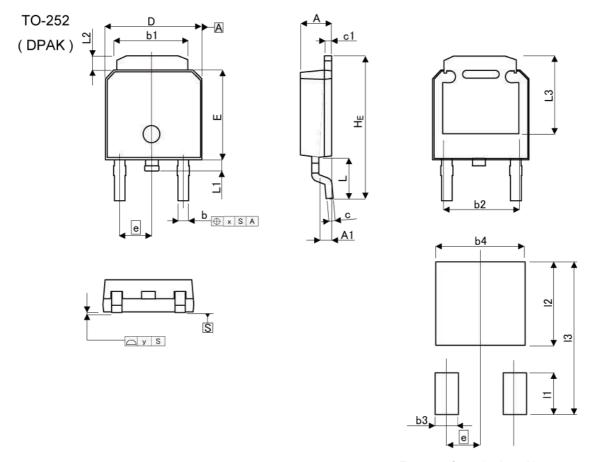
Pattern of terminal position areas [Not a recommended pattern of soldering pads]

| DIM - | MILIME | ETERS | INC | HES |
|-------|--------|-------|-------|-------|
| DIIVI | MIN | MAX | MIN | MAX |
| Α | 2.10 | 2.30 | 0.083 | 0.091 |
| A1 | 0.70 | 1.10 | 0.028 | 0.043 |
| b | 0.65 | 0.85 | 0.026 | 0.033 |
| b1 | 5.10 | 5.40 | 0.201 | 0.213 |
| b2 | 5. | 10 | 0.2 | 201 |
| С | 0.40 | 0.60 | 0.016 | 0.024 |
| c1 | 0.40 | 0.60 | 0.016 | 0.024 |
| D | 6.40 | 6.80 | 0.252 | 0.268 |
| е | 2. | 30 | 0.091 | |
| E | 6.00 | 6.40 | 0.236 | 0.252 |
| HE | 9.50 | 10.50 | 0.374 | 0.413 |
| L | 2. | 90 | 0.114 | |
| L1 | 0.70 | 0.90 | 0.028 | 0.035 |
| L2 | 0.70 | 1.30 | 0.028 | 0.051 |
| L3 | 5.30 | | 0.2 | 209 |
| Х | - | 0.10 | - | 0.004 |
| у | - | 0.10 | | 0.004 |

| DIM | MILIM | ETERS | INC | HES |
|-----|----------|-------|------------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| b3 | ₽ | 1.10 | (328 | 0.043 |
| b4 | | 5.40 | S = 3 | 0.213 |
| I1 | <u>#</u> | 2.90 | WZ | 0.114 |
| 12 | * | 5.50 | S = 1 | 0.217 |
| 13 | E | 10.50 | W29 | 0.413 |

Dimension in mm/inches

● Dimensions (TL1)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

| DIM - | MILIME | ETERS | INCHES | |
|--------|--------|-------|--------|-------|
| ן ואוט | MIN | MAX | MIN | MAX |
| Α | 2.20 | 2.40 | 0.087 | 0.094 |
| A1 | 0.70 | 1.10 | 0.028 | 0.043 |
| b | 0.60 | 0.90 | 0.024 | 0.035 |
| b1 | 5.20 | 5.50 | 0.205 | 0.217 |
| b2 | 4. | 4.80 | | 89 |
| С | 0.40 | 0.60 | 0.016 | 0.024 |
| c1 | 0.40 | 0.60 | 0.016 | 0.024 |
| D | 6.40 | 6.80 | 0.252 | 0.268 |
| е | 2.30 | | 0.091 | |
| E | 6.00 | 6.40 | 0.236 | 0.252 |
| HE | 9.40 | 10.40 | 0.370 | 0.409 |
| L | 2. | 90 | 0.114 | |
| L1 | 0.60 | 1.00 | 0.024 | 0.039 |
| L2 | 0.70 | 1.30 | 0.028 | 0.051 |
| L3 | 5. | 30 | 0.209 | |
| х | * | 0.25 | (4) | 0.010 |
| у | 76 | 0.10 | (E) | 0.004 |
| - T | MILIME | ETERS | INCI | HES |
| DIM | MIN | MAX | MIN | MAX |
| b3 | * | 1.15 | S#8 | 0.045 |
| b4 | - | 5.55 | 950 | 0.219 |
| 11 | E . | 2.77 | (4) | 0.109 |
| 12 | Wi Wi | 5.50 | 95% | 0.217 |
| 13 | ¥: | 10.40 | 2000 | 0.409 |

Dimension in mm/inches



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| CLASSⅢ | CLASSⅢ | CLASS II b | CLASSIII |
| CLASSIV | CLASSIII | CLASSⅢ | CLASSIII |

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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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