

Features

- Lead free as standard
- RoHS compliant*
- Low capacitance 1.2 pF
- No insertion loss to 2 GHz
- ESD, EFT, surge protection

Applications

- USB 2.0 & USB OTG
- Multimedia card interface
- SD card interface
- SIM ports
- Gigabit Ethernet

CDDFN6-0504P - TVS/Steering Diode Array

General Information

The CDDFN6-0504P device provides ESD, EFT and surge protection for high speed data ports meeting IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. The Transient Voltage Suppressor array, protecting up to 4 data lines, offers a Working Peak Reverse Voltage of 5 V and Minimum Breakdown Voltage of 6 V.

The molded packaged device will mount directly onto the industry standard DFN6 or QFN6 footprint. Bourns® Chip Diodes are easy to handle with standard pick and place equipment and their flat configuration minimizes roll away.



Absolute Maximum Ratings

Parameter	Symbol	CDDFN6-0504P	Unit
Peak Pulse Power (tp = 8/20 µs) ^(NOTE 1)	P _{pk}	150	W
Peak Pulse Current (tp = $8/20 \ \mu s$) (NOTE 1)	I _{PP}	6.5	А
Storage Temperature	TSTG	-55 to +150	°C
Operating Temperature	T _{OPR}	-55 to +125	°C
Operating Supply Voltage	VDC	6	V
ESD per IEC 61000-4-2 (Air)(I/O to GND) ESD per IEC 61000-4-2 (Contact) (I/O to GND)	V _{ESD_IO}	18 14	kV
ESD per IEC 61000-4-2 (Air)(V _{CC} to GND) ESD per IEC 61000-4-2 (Contact)(V _{CC} to GND)	V _{ESD_VCC}	30 30	kV
DC Voltage at any I/O Pin	V _{IO}	(GND-0.5) to (V _{CC} +0.5)	V

Note 1. See Power Derating Curve.

Electrical Characteristics (@T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CDDFN6-0504P	Unit
Maximum Reverse Standoff Voltage ¹	V _{RWM}	5.0	V
Maximum Leakage Current ¹ @ V _{RWM}	I _D	5.0	μA
Maximum Channel Leakage Current @ V _{RWM}	I _{CD}	1.0	μA
Minimum Reverse Breakdown Voltage1 @ I _{BV} =1 mA	V _{BR}	6.0	V
Maximum Forward Voltage ⁴ @ IF = 15 mA	V _F	1.0	V
Typical Clamping Voltage ²	V _C	8.1	V
Typical ESD Clamping Voltage - I/O per IEC 61000-4-2 +6 kV, Contact ²	V _{clamp_io}	12.5	V
Typical ESD Clamping Voltage-V _{CC} 1	V _{clamp_VCC}	9.0	V
Maximum Channel Input Capacitance ² @ V _{PIN5} =5 V, V _{PIN2} =0 V, V _{IN} =2.5 V, f=1 MHz	C _{IN}	1.6	pF
Maximum Channel to Channel Input Capacitance ³ @ V _{PIN5} =5 V, V _{PIN2} =0 V, V _{IN} =2.5 V, f=1 MHz	C _{CROSS}	0.14	pF
Maximum Variation of Channel Input Capacitance @ $V_{PIN5}=5$ V, $V_{PIN2}=0$ V, $V_{IN}=2.5$ V, f=1 MHz. (I/O Pin to GND)	ΔC _{IN}	0.06	pF

Note 1. Pin 5 to Pin 2 (ground). Note 2. Pin 1, 3, 4 or 6 to Pin 2 (ground). Note 3. Between any two of pins 1, 3, 4, 6. Note 4. Pin 2 (ground) to Pin 5.



*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

CDDFN6-0504P - TVS/Steering Diode Array

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Product Dimensions

This is a molded DFN6 package with lead free Nickel-Paladium-Gold (Ni/Pd/Au) on the lead frame. It has a flammability rating of UL 94V-0.





Pin Out

Pin	Function
1	I/O
2	GND
3	I/O
4	I/O
5	V _{CC}
6	I/O
Center Tab	GND

Recommended Footprint



Typical Part Marking

CDDFN6-0504P		.54	4
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Typical Application I/0 1 I/0 1 T0 DATA LINE TO I/O-PORT PROTECTED DATA LINE CONNECTOR IC 1/0.2I/0 2 6 GND RAIL 5 *OPTIONAL 0.1 μF CHIP CAP. 4 _____3 I/0 3 Τ0 T0 DATA LINE I/O-PORT PROTECTED -0 CONNECTOR IC DATA LINE 1/0.4 I/0 4

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Rating & Characteristic Curves

Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Line Voltage

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Pulse Waveform

Forward Voltage vs. Forward Current

Capacitance vs. Temperature

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CDDFN6-0504P - TVS/Steering Diode Array

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Packaging Information

The product will be dispensed in tape and reel format (see diagram below).

Devices are packed in accordance with EIA standard RS-481-A.

Item	Symbol	DFN-6
Carrier Width	А	$\frac{1.78 \pm 0.05}{(0.070 \pm 0.002)}$
Carrier Length	В	$\frac{1.78 \pm 0.05}{(0.070 \pm 0.002)}$
Carrier Depth	с	$\frac{0.69 \pm 0.05}{(0.027 \pm 0.002)}$
Sprocket Hole	d	$\frac{1.55 \pm 0.05}{(0.061 \pm 0.002)}$
Reel Outside Diameter	D	<u>178</u> (7.008)
Reel Inner Diameter	D ₁	<u>50.0</u> (1.969) MIN.
Feed Hole Diameter	D ₂	$\frac{13.0 \pm 0.20}{(0.512 \pm 0.008)}$
Sprocket Hole Position	E	$\frac{1.75 \pm 0.10}{(0.069 \pm 0.004)}$
Punch Hole Position	F	$\frac{3.50 \pm 0.05}{(0.138 \pm 0.002)}$
Punch Hole Pitch	Р	$\frac{4.00 \pm 0.10}{(0.157 \pm 0.004)}$
Sprocket Hole Pitch	P ₀	$\frac{4.00 \pm 0.10}{(0.157 \pm 0.004)}$
Embossment Center	P1	$\frac{2.00 \pm 0.05}{(0.079 \pm 0.002)}$
Overall Tape Thickness	т	$\frac{0.20 \pm 0.10}{(0.008 \pm 0.004)}$
Tape Width	w	$\frac{8.00 \pm 0.20}{(0.315 \pm 0.008)}$
Reel Width	W ₁	<u>14.4</u> (0.567) MAX.
Quantity per Reel		3000

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