

NOT RECOMMENDED FOR NEW DESIGN **USE DMP2045U**



DMP2100U

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON) MAX}	Package	I _D T _A = +25°C
	38mΩ @ V _{GS} = -10V		-4.3A
-20V	43mΩ @ V _{GS} = -4.5V	SOT23	-4.0A
	75 mΩ @ $V_{GS} = -2.5$ V		-2.8A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- Power Management Functions

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Up To 3kV**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Mechanical Data

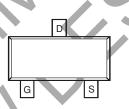
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



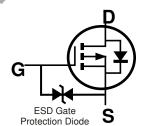


SOT23

Top View



Top View nternal Schematic



Equivalent Circuit (Note 5)

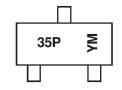
Ordering Information (Notes 5 & 6)

	Part Number	Compliance	Case	Packaging			
	DMP2100U-7	Standard	SOT23	3,000/Tape & Reel			
	DMP2100UQ-7	Automotive	SOT23	3,000/Tape & Reel			
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.						

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and 1000ppm antimony compounds.
- Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
- The ESD gate protection diode is only designed to protect against ESD events. No gate-source voltage greater than the maximum VGSS rating (given on page 2) can be applied.
- 6. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



35P = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018)M = Month (ex: 9 = September)

Date Code Key

Year	2008	~	2017	2018	201	19 20)20 2	2021	2022	2023	2024	2025
Code	V	~	Е	F	G	i	Н	1	J	K	L	М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	-20	V	
Gate-Source Voltage (Note 7)	V _{GSS}	±10	V		
Continuous Dusin Courset (Nate 0) V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-4.3 -3.4	А
Continuous Drain Current (Note 9) V _{GS} = -10V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-5.5 -4.3	Α
Maximum Continuous Body Diodes Forward Curr	ent (Note 9	Is	-2	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	%)	I _{DM}	-30	Α	
Pulsed Body Diodes Forward Current (10µs Pulse	I _{SM}	-30	Α		

Thermal Characteristics

	Symbol		Value	Unit	
$T_A = +25^{\circ}C$	Pn		0.8	W	
$T_A = +70^{\circ}C$, b		0.5		
Steady State	2		161	°C/W	
t<5s	Нөја		96	C/VV	
$T_A = +25^{\circ}C$	-		1.3	W	
$T_A = +70^{\circ}C$	PD		0.8		
Steady State	2		99		
t<5s	Неја		60	°C/W	
	R ₀ JC		15		
	T _J , T _{STC}	1	-55 to +150	°C	
	$T_A = +70^{\circ}C$ Steady State $t < 5s$ $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ Steady State	$\begin{array}{c c} T_A = +25^{\circ}C & P_D \\ \hline T_A = +70^{\circ}C & P_D \\ \hline Steady State & R_{\theta JA} \\ \hline T_A = +25^{\circ}C & P_D \\ \hline T_A = +70^{\circ}C & P_D \\ \hline Steady State & R_{\theta JA} \\ \hline +25^{\circ}S & R_{\theta JA} \\ \hline R_{\theta JA} & R_{\theta JA} \\ \hline \end{array}$	$ \begin{array}{c c} T_A = +70^{\circ}C \\ \hline Steady State \\ \hline t < 5s \\ \hline T_A = +25^{\circ}C \\ \hline T_A = +70^{\circ}C \\ \hline Steady State \\ \hline t < 5s \\ \hline \end{array} \begin{array}{c} P_D \\ \hline P_D \\ \hline P_D \\ \hline \end{array} $	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	+	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	loss	Ų	1	-1	μΑ	$V_{DS} = -20V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 10)			4			
Gate Threshold Voltage	V _{GS(TH)}	-0.3		-1.4	>	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
			25	38		$V_{GS} = -10V, I_D = -3.5A$
Static Drain-Source On-Resistance		1	29	43	mΩ	$V_{GS} = -4.5V, I_D = -3A$
Static Drain-Source Off-nesistance	RDS(ON)	_	37	75	11122	$V_{GS} = -2.5V, I_D = -1A$
			47	_		$V_{GS} = -1.8V, I_D = -0.5A$
Forward Transfer Admittance	Y _{fs}		3		S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	Ciss	1	216	_	рF	V 45V V 0V
Output Capacitance	Coss		90	_	рF	$V_{DS} = -15V, V_{GS} = 0V$ of = 1.0MHz
Reverse Transfer Capacitance	C_{rss}	l	24		рF	1 – 1.0WI IZ
Gate Resistnace	R_g		250		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
SWITCHING CHARACTERISTICS (Note 11)						
Total Gate Charge	Q_g	1	9.1		nC	V 45V V 10V
Gate-Source Charge	Q_{gs}		1.6		nC	$V_{GS} = -4.5V, V_{DS} = -10V$
Gate-Drain Charge	Q_{gd}		2.0	_	nC	I _D = -4A
Turn-On Delay Time	t _{D(ON)}		80		ns	
Turn-On Rise Time	t _R		155	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}		688	_	ns	$R_D = 2.5\Omega, R_G = 3.0\Omega$
Turn-Off Fall Time	t _F		423	_	ns	

Notes:

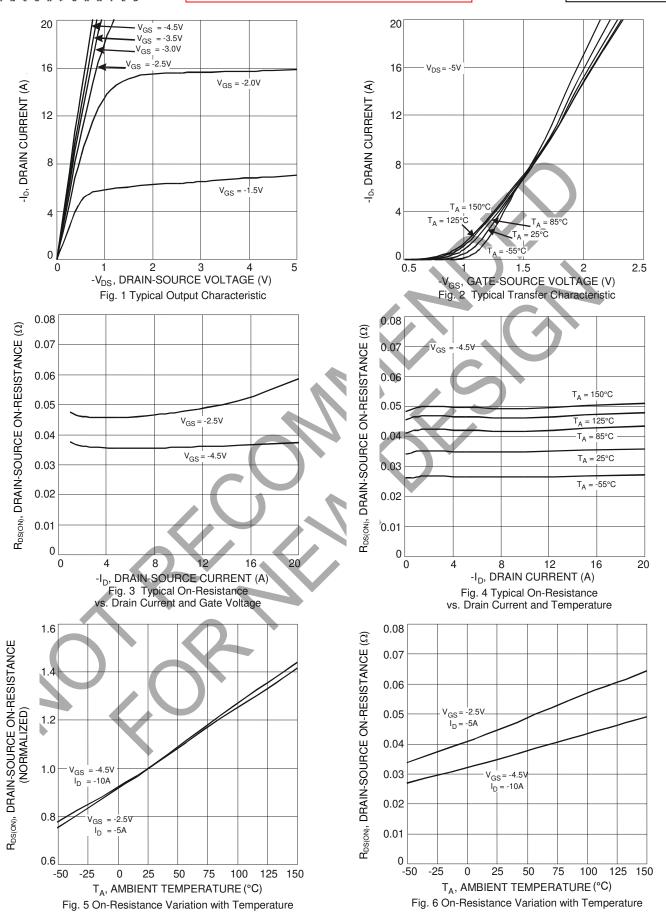
- 7. AEC-Q101 V_{GS} maximum is $\pm 9.6 V$.
- 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

 9. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 10. Short duration pulse test used to minimize self-heating effect.11. Guaranteed by design. Not subject to product testing.



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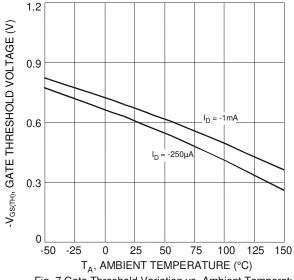
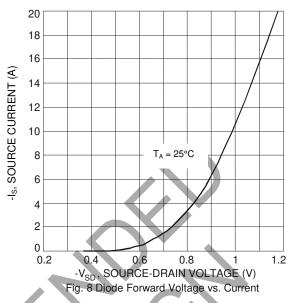
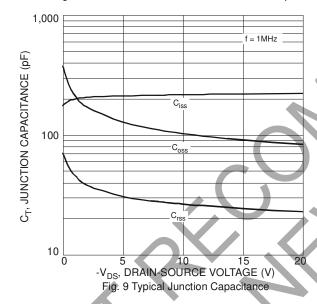
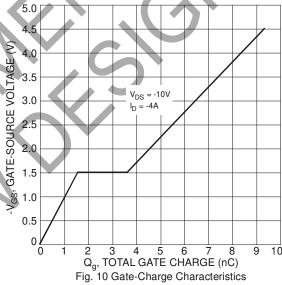


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







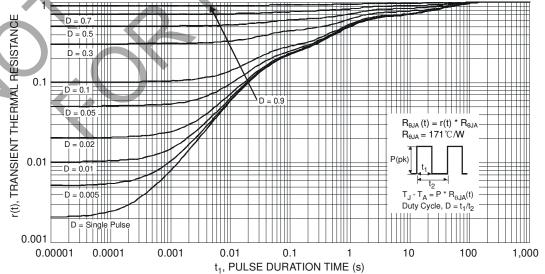


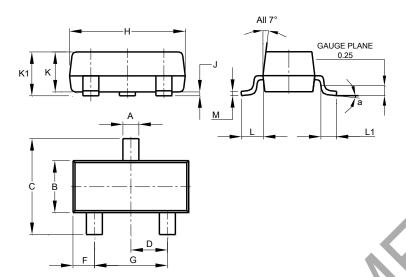
Fig. 11 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

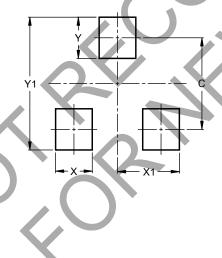


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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