

APPLICATIONS:

- PDAs
- Memory Card Ports
- Mobile Phones
- Instrumentation

Central
Semiconductor Corp.

www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMATVS3V3 Series devices are Low Leakage, Fast Response TVSs packaged in a space saving SOD-923 surface mount package. These devices are designed to protect sensitive equipment against ESD damage.

MARKING CODES: CMATVS3V3: E CMATVS5V0: F

FEATURES:

- Small, 1.0 x 0.6 x 0.4mm, SOD-923 Surface Mount Package
- Low Leakage Current

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Power Dissipation (8x20µs)	PPK	80	W
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40	А
ESD Voltage (IEC 61000-4-2, Air)	V _{ESD}	30	kV
ESD Voltage (IEC 61000-4-2, Contact)	VESD	30	kV
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C) V_F=0.9V MAX @ I_F=10mA (for all types)

Maximum Reverse Stand-off Type Voltage V _{RWM}		Minimum Breakdown Voltage V _{BR} @ I _T		Maximum Maximum Reverse Clamping Leakage Voltage Current (8x20μs) I _R @ V _{RWM} VC @ I _{PP}		Typical TLP Clamping Voltage (Note 1) V _{CL} @ IPP		Typical Dynamic Resistance (Note 1) RDYN	Typical Junction Capacitance @ 0V Bias CJ	
	v	v	mA	μΑ	v	A	v	A	Ω	pF
CMATVS3V3	3.3	5.0	1.0	2.5	11.7	7.0	6.0	11.3	0.5	45
CIMATVS3V3	3.3	5.0	1.0	2.5	11.7	7.0	3.0	5.3		
	E 0	6.0	1.0	10	10.0	7.0	6.0	11.3	0.5	40
CMATVS5V0	5.0	6.2	1.0	1.0	12.3	7.0	3.0	5.3	0.5	40

Note 1: Transmission Line Pulse (TLP) conditions: Z₀=50Ω, tp=100ns

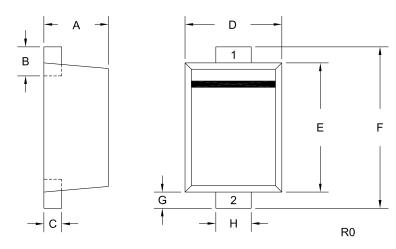
R7 (25-February 2021)



CMATVS3V3 CMATVS5V0

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SURFACE MOUNT SILICON UNI-DIRECTIONAL TRANSIENT VOLTAGE SUPPRESSORS



SOD-923 CASE - MECHANICAL OUTLINE

DIMENSIONS					
	INC	HES	MILLIM	ETERS	
SYMBOL	MIN	MAX	MIN	MAX	
А	0.015	0.016	0.39	0.41	
В	0.004	0.010	0.10	0.26	
С	0.003	0.006	0.08	0.14	
D	0.022	0.026	0.55	0.65	
E	0.030	0.033	0.75	0.85	
F	0.035	0.043	0.90	1.10	
G	0.002	0.006	0.05	0.15	
Н	0.007	0.011	0.17	0.27	
SOD-923 (REV: R0)					

1) Cathode 2) Anode

LEAD CODE:

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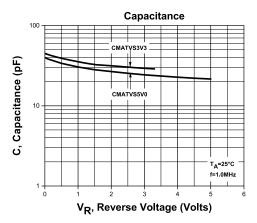




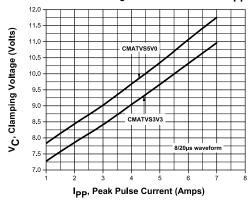
Reverse Voltage 7.2 V_{BR}, Breakdown Voltage (Volts) 7.0 6.8 CMATVS5V0. 6.6 6.4 6.2 сматувзуз l=1.0mA 6.0 | -25 ò 25 50 75 100 125 T_A, Ambient Temperature (°C)

Leakage Current 100 I_R, Reverse Current (nA) CMATVS5V0, Vp=5 10 CMATVS3V3, VR=3.3V 0.1 25 -50 -25 ò 50 75 100 125 150 T_A, Ambient Temperature (°C)

TYPICAL ELECTRICAL CHARACTERISTICS



Clamping Voltage (V_C) vs Peak Pulse Current (I_{PP})



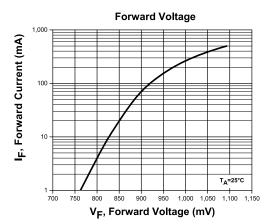


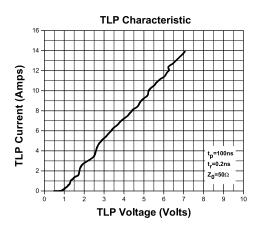
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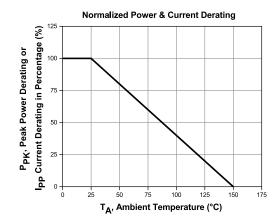


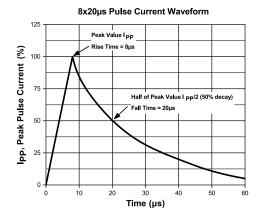


TYPICAL ELECTRICAL CHARACTERISTICS









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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

Special wafer diffusions

· Custom product packing

- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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