



DMTH10H009LPS

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
100V	8mΩ @ V _{GS} = 10V	100A
1000	12.5mΩ @ V _{GS} = 4.5V	81A

Description

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize $R_{DS(ON)}$, yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

Applications

- Motor Control
- DC-DC Converters
- Power Management

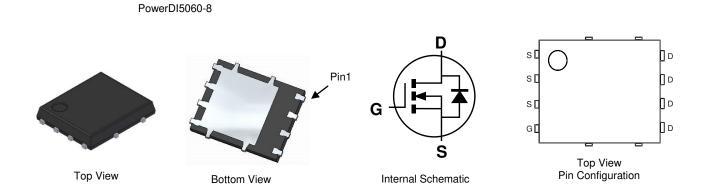
100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications (PowerDI[®])
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH10H009LPS-13	PowerDI5060-8	2,500 / Tape & Reel

I. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

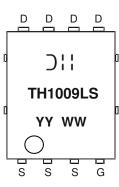
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

PowerDI is a registered trademark of Diodes Incorporated.



Marking Information



); ; = Manufacturer's Marking
TH1009LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 19 = 2019)
WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	100	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current V_{GS} = 10V (Note 6)	Steady State	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	ID	14 10	А
Continuous Drain Current V_{GS} = 10V (Note 7)	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	100 72	А
Pulsed Drain Current (10µs Pulse, T _C =+25°C, Package I	IDM	400	A		
Maximum Continuous Body Diode Forward Current			I _S	85	A
Pulsed Body Diode Current (10µs Pulse, T _C =+25°C, Package Limited)			I _{SM}	400	A
Avalanche Current (Note 8), L=0.3mH			I _{AS}	21	A
Avalanche Energy (Note 8), L=0.3mH			E _{AS}	66	mJ
V _{DS} Spike, L=0.1mH t=10µs			V _{SPIKE}	110	V

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	98	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	3.5	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	43	°C/W
Total Power Dissipation (Note 7)	T _C = +25°C	PD	125	W
Thermal Resistance, Junction to Case (Note 7)		R _θ JC	1.2	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

7. Thermal resistance from junction to soldering point (on the exposed drain pad).

8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.



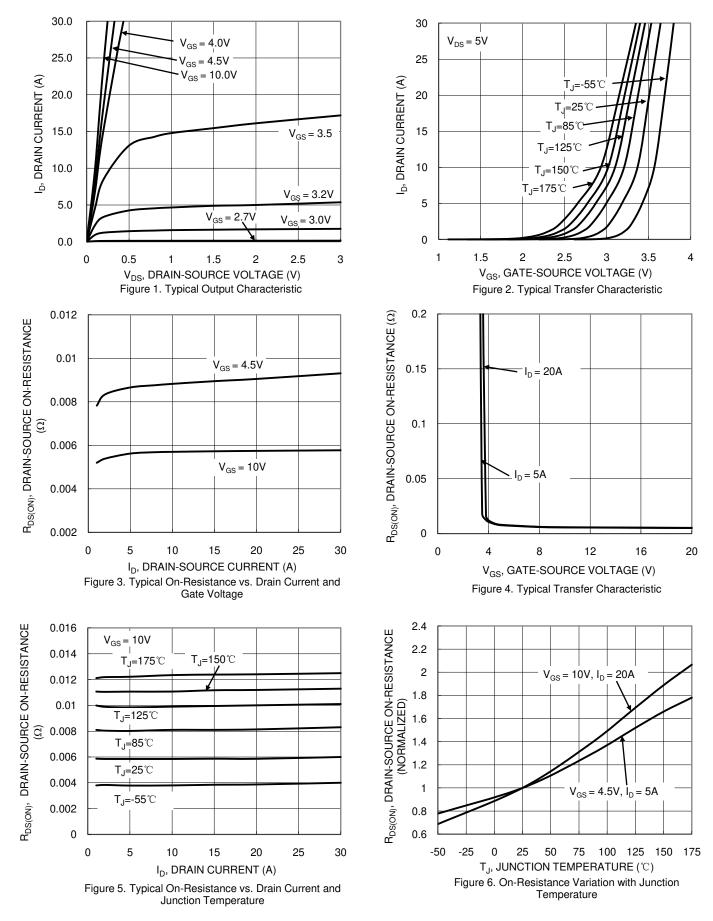
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)						·	
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	—	_	1	μA	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)						·	
Gate Threshold Voltage	V _{GS(TH)}	1.2	-	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance		_	6	8	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	9	12.5	11122	$V_{GS} = 4.5V, I_D = 5A$	
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 13A$	
DYNAMIC CHARACTERISTICS (Note 10)						·	
Input Capacitance	Ciss	_	2309	—		V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	536	—	pF		
Reverse Transfer Capacitance	Crss	—	13.7	_			
Gate Resistance	Rg	—	1.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	40.2	_			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	20.2	_	nC		
Gate-Source Charge	Q _{gs}	—	7.0	_	nc	$V_{DD} = 50V, I_D = 20A,$	
Gate-Drain Charge	Q _{gd}	_	8.5	_			
Turn-On Delay Time	t _{D(ON)}	_	5.4	_			
Turn-On Rise Time	t _R	_	10.6	_		$\label{eq:VDD} \begin{split} V_{DD} &= 50V, \ V_{GS} = 10V, \\ I_D &= 20A, \ R_g = 3\Omega \end{split}$	
Turn-Off Delay Time	tD(OFF)	—	28.3	—	ns		
Turn-Off Fall Time	t _F	—	14.9	_	1		
Reverse Recovery Time	t _{RR}	—	44.3	_	ns		
Reverse Recovery Charge	Q _{RR}	—	65.5	_	nC	I _F = 20A, di/dt = 100A/μs	

Notes: 9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to product testing.



DMTH10H009LPS



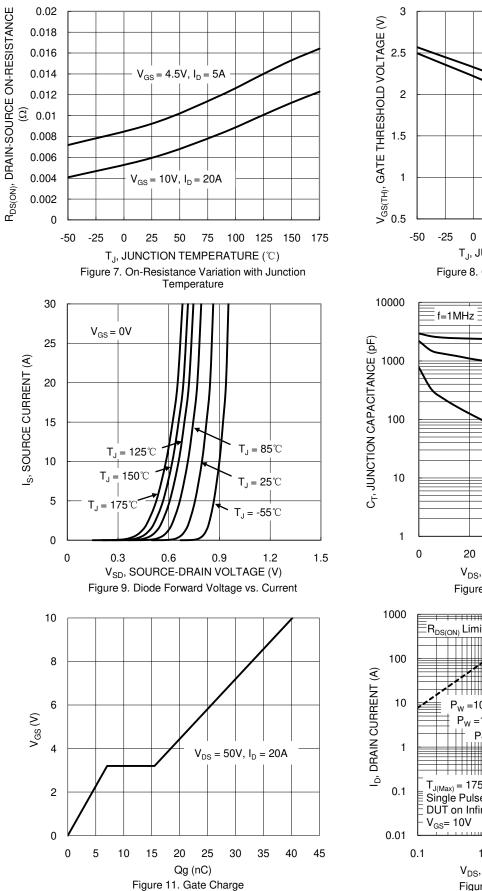
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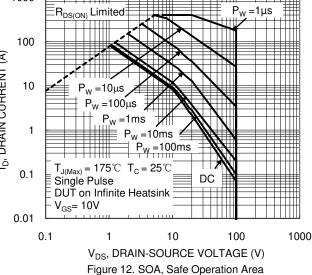
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 $I_D = 1 m A$

 $I_{\rm D} = 250 \mu A$

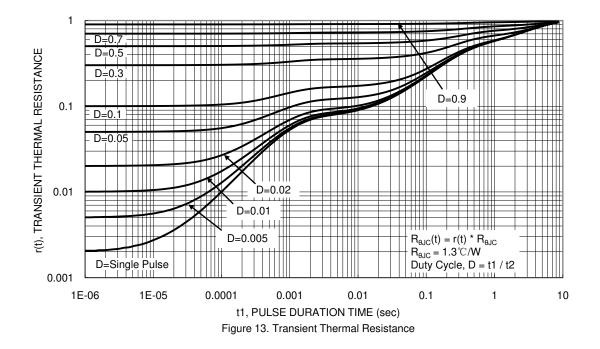


-25 0 25 50 75 100 125 150 175 T., JUNCTION TEMPERATURE (℃) Figure 8. Gate Threshold Variation vs. Junction Temperature f=1MHz \mathbf{C}_{iss} $\mathbf{C}_{\mathrm{oss}}$ C_{rss} 20 40 60 80 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10. Typical Junction Capacitance



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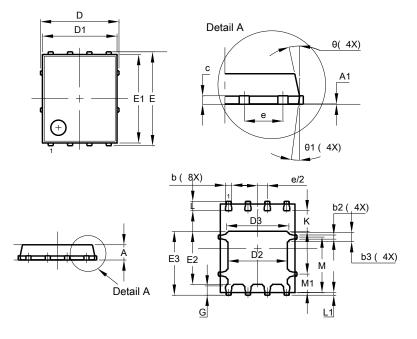






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

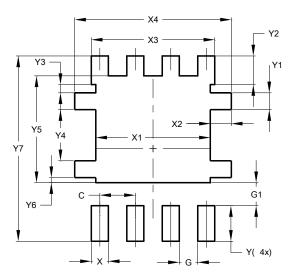


	PowerDI5060-8					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D		5.15 BSC				
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	3.90 4.30 4.1				
Е	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	-	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10º	12º	11º			
Θ1	6º	8º	7⁰			
Al	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
¥7	6.610

PowerDI5060-8



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