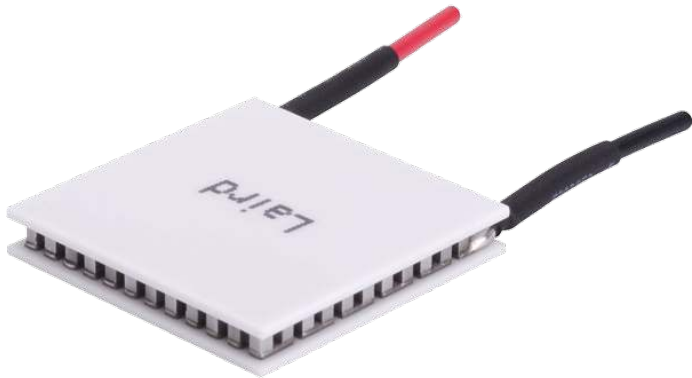


Ceramic Plate Series Thermoelectric Cooler

The CP10-71-06-L1-W4.5 is a high-performance and highly reliable standard Thermoelectric Cooler. Assembled with Bismuth Telluride semiconductor material and thermally conductive Aluminum Oxide ceramics. It has a maximum Q_c of 14.2 Watts when $\Delta T = 0$ and a maximum ΔT of 70.5 °C at $Q_c = 0$.

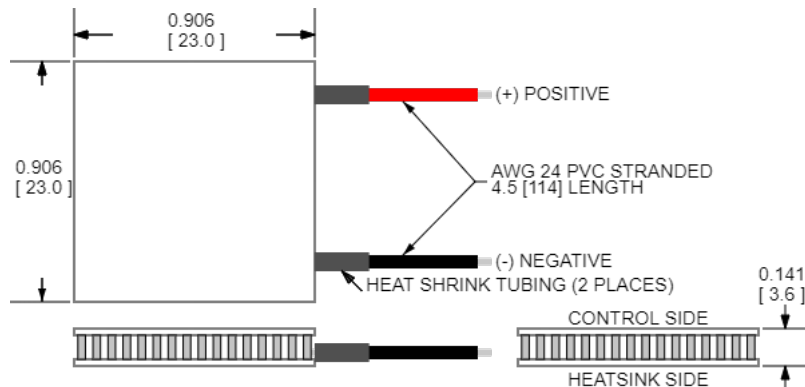


Features

- Compact geometric sizes
- DC Operation
- RoHS-compliant

Applications

- Thermoelectric Coolers for Reagent Storage
- Thermoelectric Coolers for Handheld Cosmetic Lasers
- Cooling for Centrifuges
- Heads-Up Displays, Imaging Sensors
- Peltier Cooling for Machine Vision



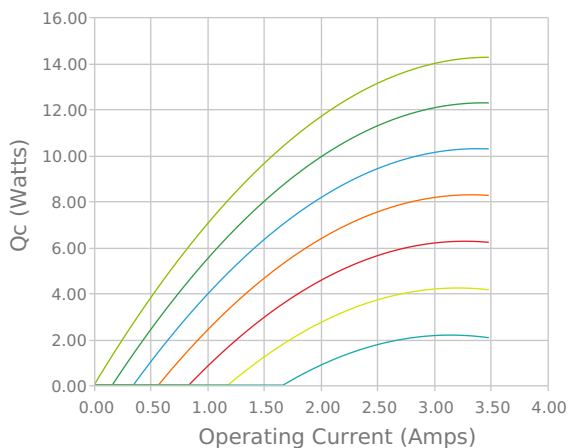
CERAMIC MATERIAL: Al_2O_3
 SOLDER CONSTRUCTION: 138°C, BiSn

INCHES [MM]

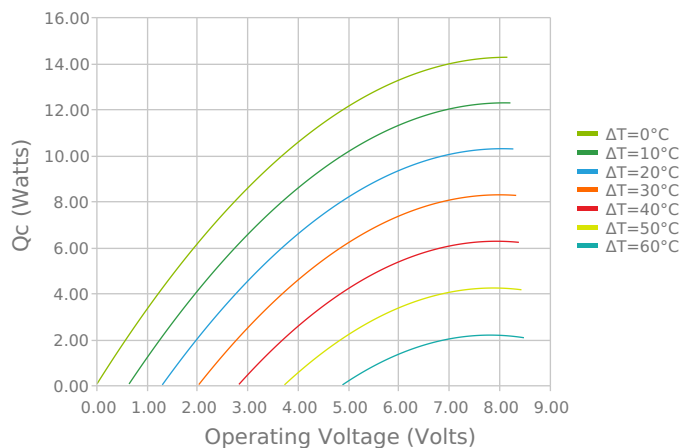
ELECTRICAL AND THERMAL PERFORMANCE

For maximum performance, be sure to orient the CONTROL side of the TEC against the application to be managed and the HEATSINK side against the heat sink or other heat rejection method. The CONTROL side is always opposite the side with lead attachments. Lead attachment is a passive heat loss and less impactful if located on the side that attaches to the heat exchanger.

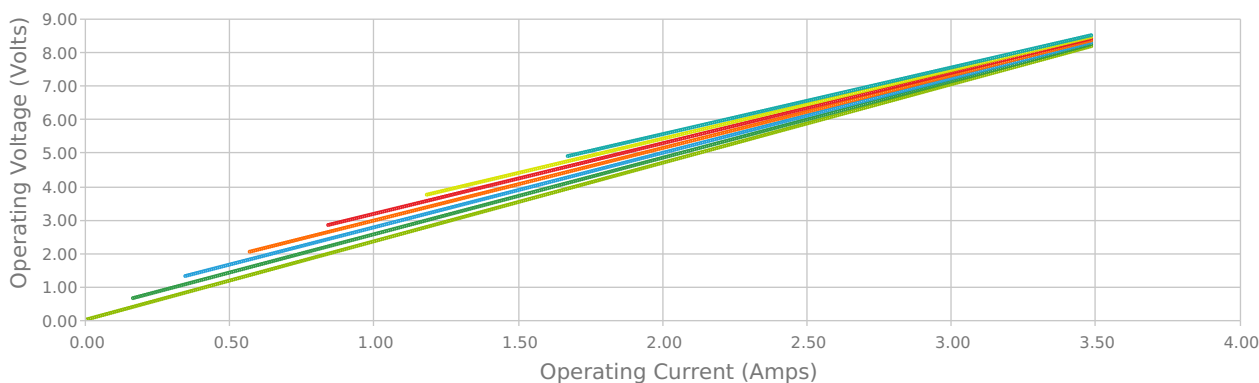
Heat Pumped at Cold Side
 $T_{hot} = 27\text{ °C}$



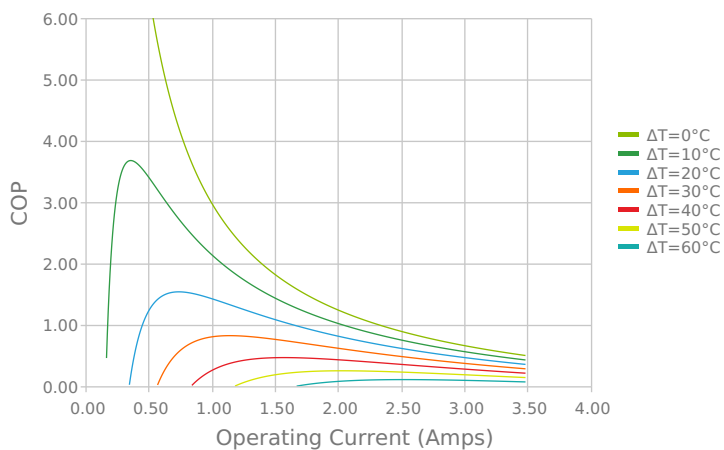
Heat Pumped at Cold Side
 $T_{hot} = 27\text{ °C}$



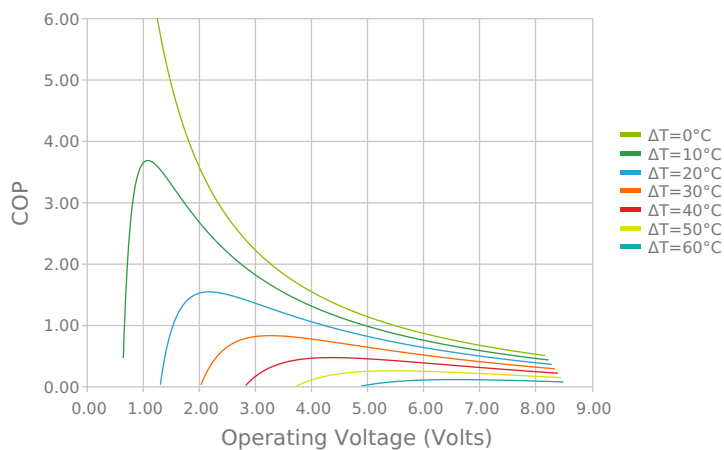
Current vs Voltage (I vs V)
 $T_{hot} = 27\text{ °C}$



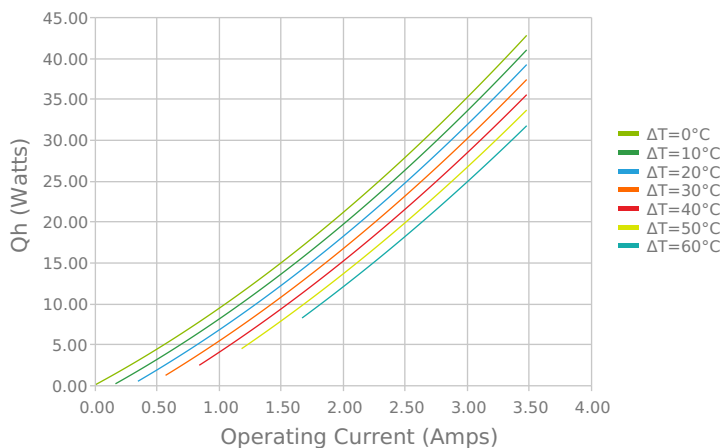
Coefficient of Performance (COP = Q_c/P_{in})
 $T_{hot} = 27\text{ }^{\circ}\text{C}$



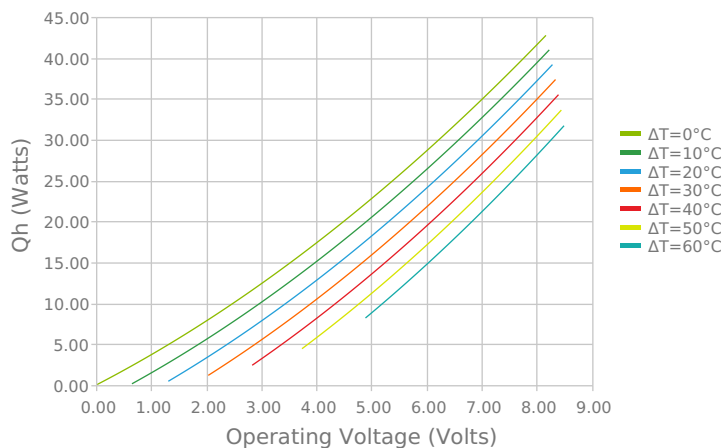
Coefficient of Performance (COP = Q_c/P_{in})
 $T_{hot} = 27\text{ }^{\circ}\text{C}$



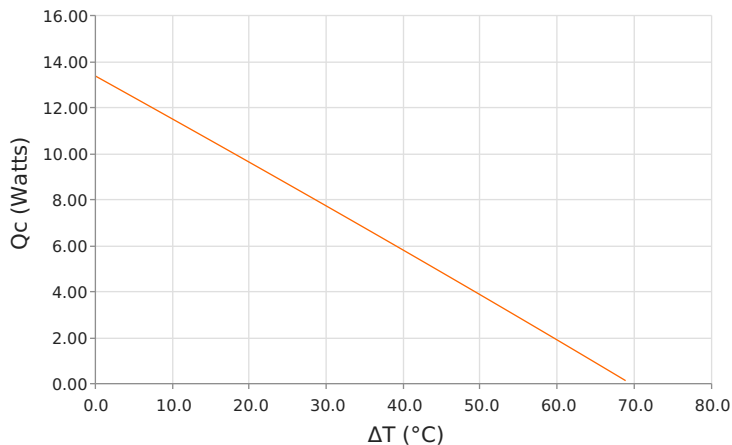
Total Heat Dissipated at Hot Side ($Q_h = Q_c + P_{in}$)
 $T_{hot} = 27\text{ }^{\circ}\text{C}$



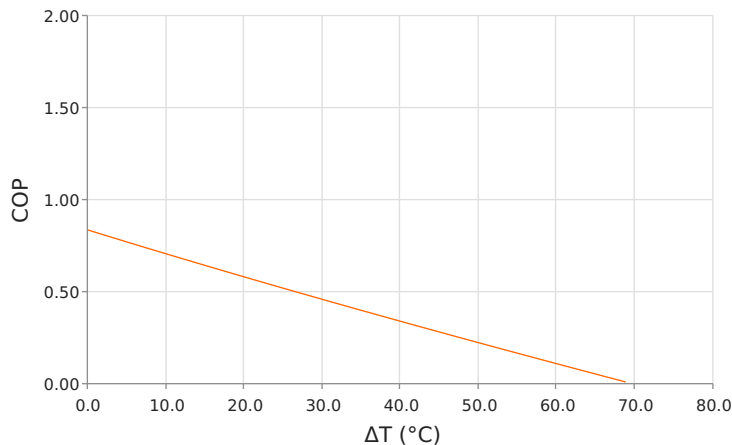
Total Heat Dissipated at Hot Side ($Q_h = Q_c + P_{in}$)
 $T_{hot} = 27\text{ }^{\circ}\text{C}$



Heat Pumped at Cold Side (Q_c)
 $T_{hot} = 27\text{ }^{\circ}\text{C}$ | Current = 2.6 Amps



Coefficient of Performance (COP = Q_c/P_{in})
 $T_{hot} = 27\text{ }^{\circ}\text{C}$ | Current = 2.6 Amps



SPECIFICATIONS*

Hot Side Temperature

Qcmax ($\Delta T = 0$)

ΔT_{max} ($Q_c = 0$)

I_{max} (I @ ΔT_{max})

V_{max} (V @ ΔT_{max})

Module Resistance

Max Operating Temperature

Weight

	27.0 °C	35.0 °C	50.0 °C
Qcmax ($\Delta T = 0$)	14.2 Watts	14.7 Watts	15.4 Watts
ΔT_{max} ($Q_c = 0$)	70.5°C	73.5°C	78.8°C
I _{max} (I @ ΔT_{max})	3.1 Amps	3.1 Amps	3.0 Amps
V _{max} (V @ ΔT_{max})	7.8 Volts	8.1 Volts	8.6 Volts
Module Resistance	2.34 Ohms	2.44 Ohms	2.62 Ohms
Max Operating Temperature	80 °C		
Weight	6.0 gram(s)		

* Specifications reflect thermoelectric coefficients updated March 2020

FINISHING OPTIONS

Suffix	Thickness	Flatness / Parallelism	Hot Face	Cold Face	Lead Length
L1	3.581 ±0.025 mm 0.141 ± 0.0010 in	0.025 mm / 0.025 mm 0.001 in / 0.001 in	Lapped	Lapped	114.3 mm 4.50 in

SEALING OPTIONS

Suffix	Sealant	Color	Temp Range	Description
	None			No sealing specified

NOTES

1. Max operating temperature: 80°C
2. Do not exceed I_{max} or V_{max} when operating module
3. Reference assembly guidelines for recommended installation
4. Solder tinning also available on metallized ceramics

Any information furnished by Laird and its agents, whether in specifications, data sheets, product catalogues or otherwise, is believed to be (but is not warranted as being) accurate and reliable, is provided for information only and does not form part of any contract with Laird. All specifications are subject to change without notice. Laird assumes no responsibility and disclaims all liability for losses or damages resulting from use of or reliance on this information. All Laird products are sold subject to the Laird Terms and Conditions of sale (including Laird's limited warranty) in effect from time to time, a copy of which will be furnished upon request.

© Copyright 2019-2022 Laird Thermal Systems, Inc. All rights reserved. Laird™, the Laird Ring Logo, and Laird Thermal Systems™ are trademarks or registered trademarks of Laird Limited or its subsidiaries.

Revision: 00 Date: 06-01-2022

Print Date: 06-09-2022