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6PS18012E4FG35689



Preliminary data

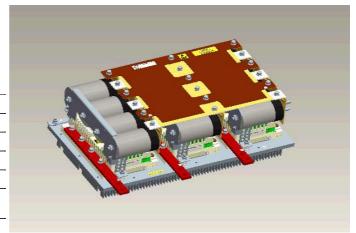
General information

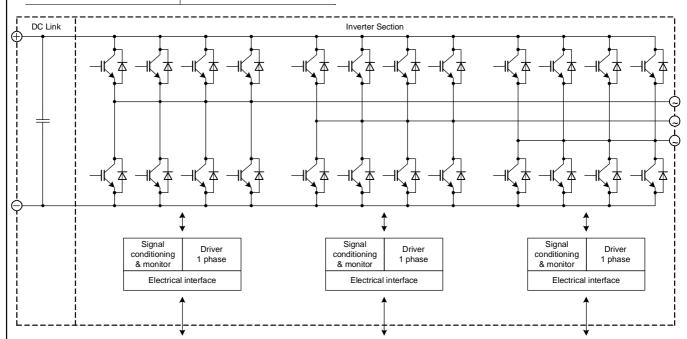
IGBT Stack for typical voltages of up to 400 V_{RMS} Rated output current 729 A_{RMS}

- High power converterSolar powerMotor drives

- · 62mm power module · Trenchstop™ IGBT4

Topology	B6I
Application	Inverter
Load type	Resistive, inductive
Semiconductor (Inverter Section)	12x FF450R12KE4
DC Link	4.8 mF
Heatsink	Forced air cooled (fan not included)
Implemented sensors	Current, voltage, temperature
Design standards	UL 94, prepared for UL 508C
Sales - name	6PS18012E4FG35689
SP - No.	SP000885246





prepared by: OW	date of publication: 2012-07-11
approved by: SD	revision: 2.0

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Absolute maximum rated values

Collector-emitter voltage	IGBT; T _{vj} = 25°C	V _{CES}	1200	V
Repetitive peak reverse voltage	Diode; T _{vj} = 25°C	V_{RRM}	1200	V
DC link voltage		V _{DC}	850	V
Insulation management	according to installation height of 2000 m	V _{line}	500	V _{RMS}
Insulation test voltage	according to EN 50178, f = 50 Hz, t = 1 s	V _{ISOL}	2.5	kV _{RMS}
Repetitive peak collector current inverter section (IGBT)	t _p = 1 ms	I _{CRM2}	2500	А
Repetitive peak forward current inverter section (Diode)	t _p = 1 ms	I _{FRM2}	2440	А
I²t-value inverter section (Diode)	V _R = 0 V, t _p = 10 ms, T _{vj} = 125 °C	l²t	122	kA²s
Continuous current inverter section		I _{AC2}	800	A _{RMS}
Junction temperature	under switching conditions	T _{vjop}	150	°C

NotesFurther maximum ratings are specified in the following dedicated sections

Characteristic values

DC Link			min.	typ.	max.	
Rated voltage		V _{DC}		650	800	V
Over voltage shutdown	within 5000 μs			850		V
Capacitor	1 s, 12 p	C _{DC}		4.8		mF
		type		Foil	•	
Maximum ripple current	per device, T _{amb} = 55 °C	I _{ripple}			49	A _{RMS}
Balance or discharge resistor	per DC link unit	Rb		82		kΩ

Inverter Section			min.	typ.	max.	
Rated continuous current	$ \begin{vmatrix} V_{DC} = 650 \text{ V}, V_{AC} = 400 \text{ V}_{RMS}, \cos(\phi) = 0.85, \\ f_{AC \text{ sine}} = 50 \text{ Hz}, f_{sw} = 5000 \text{ Hz}, T_{inlet} = 40^{\circ}\text{C}, \\ T_{j} \leq 125 ^{\circ}\text{C} $	I _{AC}		729		A _{RMS}
Continuous current at low frequency	$ \begin{vmatrix} V_{DC} = 650 \text{ V}, V_{AC} = 400 \text{ V}_{RMS}, \cos(_{(p)}) = 0.85, \\ f_{AC \text{ sine}} = 0 \text{ Hz}, f_{sw} = 5000 \text{ Hz}, T_{inlet} = 40 \text{ °C}, \\ T_{j} \leq 125 \text{ °C} $	I _{AC low}		360		A _{RMS}
Rated continuous current for 150% overload capability	I _{AC 150%} = 925 A _{RMS} , t _{on over} = 3 s, T _j ≤ 125 °C	I _{AC over1}		617		A _{RMS}
Rated continuous current for 150% overload capability	I _{AC 150%} = 803 A _{RMS} , t _{on over} = 60 s, T _j ≤ 125 °C	IAC over2		535		ARMS
Over current shutdown	within 15 μs	I _{AC OC}		2500		A _{peak}
Power losses	$\begin{array}{l} I_{AC} = 729 \text{ A, V}_{DC} = 650 \text{ V, V}_{AC} = 400 \text{ V}_{RMS}, \\ \cos(\phi) = 0.85, f_{AC \text{ sine}} = 50 \text{ Hz, } f_{sw} = 5000 \text{ Hz,} \\ T_{inlet} = 40 \text{ ^{\circ}C, } T_{j} \leq 125 \text{ ^{\circ}C} \end{array}$	P _{loss}		6790		W

Notes
Maximum junction temperature limited to 125 °C under all operating conditions

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A			
Contro	HΔr	INTO	rtaca

Driver and interface board	ref. to separate Application Note			DR240		
			min.	typ.	max.	
Auxiliary voltage		Vaux	18	24	30	V
Auxiliary power requirement	V _{aux} = 24 V	Paux			120	W
Digital input level	resistor to GND 10 kΩ, capacitor to GND 1 nF	V _{in low}	0		4	V
		V _{in high}	11		15	V
Digital output level	open collector, logic low = no fault, max. 15 mA	V _{out low}	0		1.5	V
		V _{out high}		15		V
Analog current sensor output inverter section	load max 1 mA, @ 729 A _{RMS}	VIU ana2 VIV ana2 VIW ana2	2.9	3	3.1	V
Analog DC link voltage sensor output	load max 1 mA, @ 850 V	V _{DC} ana	8.3	8.5	8.7	V
Analog temperature sensor output inverter section (NTC)	load max 1 mA, @T _{NTC} = 81 °C	V _{Theta NTC2}		10		V
Over temperature shutdown inverter section	load max 1 mA, @T _{NTC} = 86 °C	V _{Error OT2}		10.9		V

System data				min.	typ.	max.	
EMC robustness	according to IEC-61800-3 at named	power	V _{Burst}		2		kV
	interfaces	control	V _{Burst}		1		kV
		aux (24V)	V _{surge}		1		kV
Storage temperature			T _{stor}	-40		80	°C
Operational ambient temperature	PCB, DC link capacitor, bus bar, excluding medium	cooling	T _{op amb}	-25		55	°C
Cooling air velocity	PCB, DC link capacitor, bus bar, standard	atmosphere	Vair	2			m/s
Humidity	no condensation		Rel. F	5		85	%
Protection degree					IP00		
Pollution degree					2		
Dimensions	width x depth x height			658	438	302	mm
Weight					50		kg

Heatsink air cooled			min.	typ.	max.	
Air flow	T _{air} = 20 °C, P _{air} = 1013 hPa, dry and dust free, measured at the side of the heat sink according to DIN 41882	ΔV/Δt	1500			m³/h
Air pressure drop	at min. air flow	Δр		200		Pa
Air inlet temperature		T _{inlet}	-40		55	°C

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Overview of optional components	Unit 1	Inverter Section	Unit 3
Parallel interface board			
Optical interface board			
Voltage sensor		×	
Current sensor		×	
Temperature sensor		×	
DC link capacitors		×	
Data cable for control signals		×	
Fan			
Collector-emitter Active Clamping		×	

Notes
Setting of Active Clamping TVS-Diodes: V_Z = 824 V

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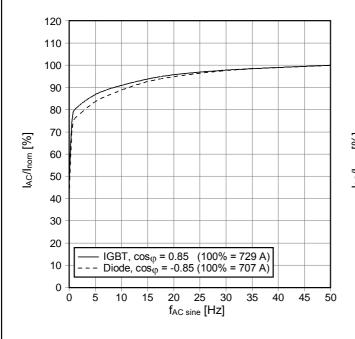
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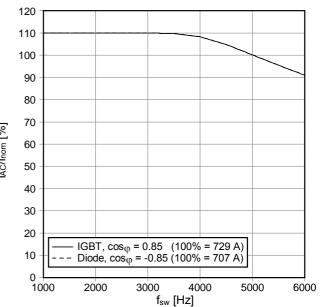


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 $\begin{array}{l} f_{AC\;\text{sine}} \text{ - derating curve IGBT (motor), Diode (generator)} \\ V_{DC} = 650\;V,\; V_{AC} = 400\;V_{RMS},\; f_{sw} = 5\;\text{kHz, } cos_{\phi} = \pm 0.85, \\ T_{inlet} = 40\;^{\circ}\text{C} \; \text{and nom. cooling conditions} \end{array}$

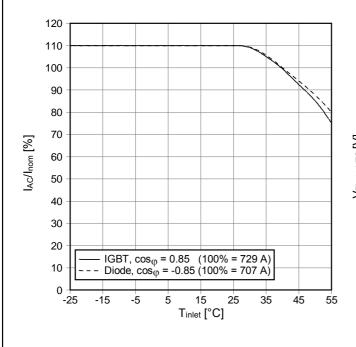
 f_{SW} - derating curve IGBT (motor), Diode (generator) V_{DC} = 650 V, V_{AC} = 400 $V_{\text{RMS}},\,f_{\text{AC sine}}$ = 50 Hz, \cos_ϕ = $\pm 0.85,\,$ T_{inlet} = 40 °C and nom. cooling conditions

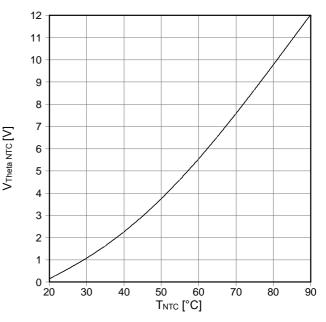




 T_{inlet} - derating curve IGBT (motor), Diode (generator) V_{DC} = 650 V, V_{AC} = 400 $V_{RMS},\,f_{sw}$ = 5 kHz, $f_{AC\,\,\text{sine}}$ = 50 Hz, cos_{\oplus} = ± 0.85 and nom. cooling conditions

Analog temperature sensor output $V_{\text{Theta NTC}}$ Sensing NTC of heatsink





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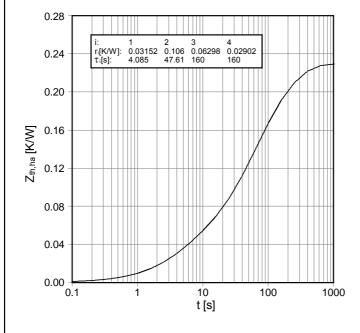
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 $Z_{\text{th,ha}} \text{ - thermal impedance heatsink to ambient per switch} \\ \text{nom. cooling conditions}$



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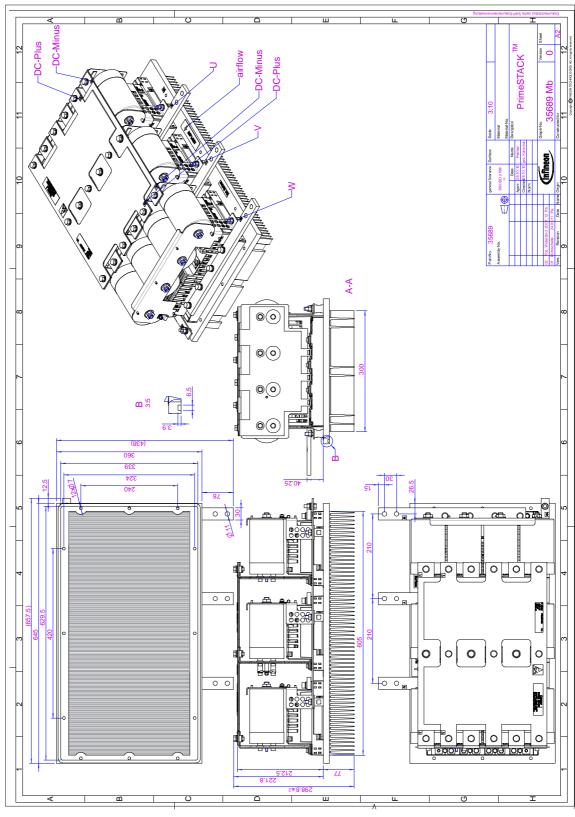
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Mechanical drawing



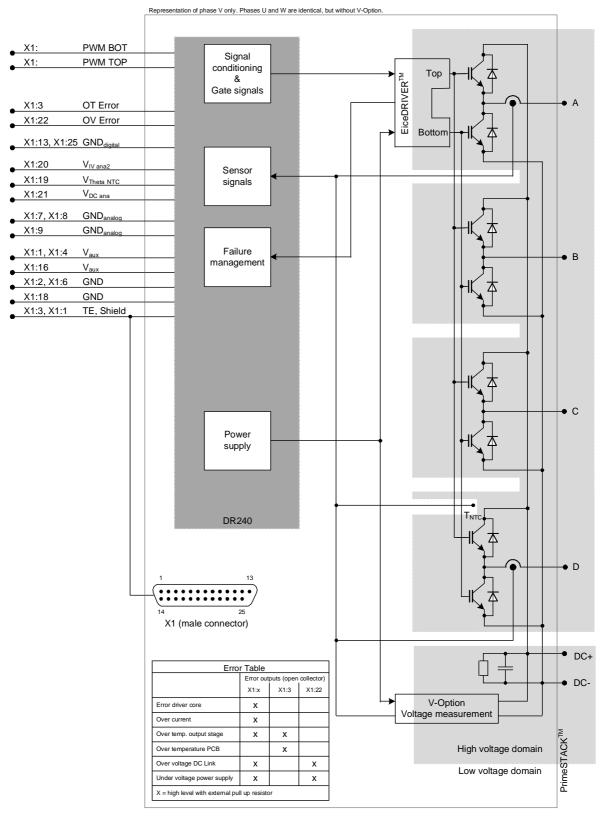
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Circuit diagram



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- to perform joint Risk and Quality Assessments;
- the conclusion of Quality Agreements;
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If and to the extent necessary, please forward equivalent notices to your customers.

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Safety Instructions

Prior to installation and operation, all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and that missing or damaged signs are replaced. To installation and operation, all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and that missing or damaged signs are replaced.

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