OptiMOS™-T2 Power-Transistor

AEC Qualified RoHS green Product

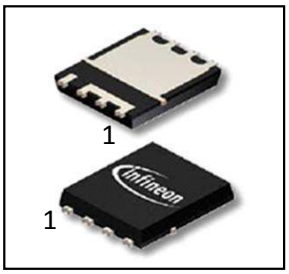
Product Summary

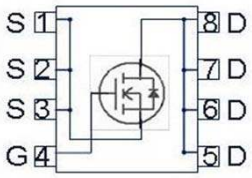
V_{DS}	40	V
$R_{DS(on)}$	5.6	mΩ
I_D	60	A

Features

- N-channel Logic Level - Enhancement mode
- AEC qualified
- MSL1 up to 260°C peak reflow
- Green product (RoHS compliant)
- 100% Avalanche tested
- Feasible for automatic optical inspection (AOI)

PG-TDSON-8-23





Type	Package	Marking
IPC60N04S4L-06	PG-TDSON-8-23	4N04L06

Maximum ratings, at T_j=25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	T_C=25°C, T_J =175°C, V_{GS}=10V	60[1)]	A
		T_C=100 °C, T_J =175°C, V_{GS}=10 V	58[1, 2)]	
Pulsed drain current[2)]	$I_{D,pulse}$	T_C=25 °C	240	
Avalanche energy, single pulse	E_{AS}	I_D=30 A	120	mJ
Avalanche current, single pulse	I_{AS}	-	60	A
Gate source voltage	V_{GS}	-	±16	V
Power dissipation	P_{tot}	T_C=25 °C, T_J =175°C	63	W
Operating and storage temperature	T_j, T_{stg}	-	-55 ... +175[3)]	°C

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	R_{thJC}	-	-	-	2.4	K/W
Electrical characteristics, at T_j=25 °C, unless otherwise specified						
Static characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS}=0 V, I_D= 1 mA	40	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D= 30µA	1.2	1.7	2.2	
Zero gate voltage drain current	I_{DSS}	V_{DS}=40 V, V_{GS}=0 V, T_j=25 °C	-	0.01	1	µA
		V_{DS}=18 V, V_{GS}=0 V, T_j=85 °C[2)]	-	1	20	
Gate-source leakage current	I_{GSS}	V_{GS}=16 V, V_{DS}=0 V	-	-	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=10 V, I_D= 30A	-	4.8	5.6	mΩ
		V_{GS}=4.5 V, I_D=30 A	-	6.5	7.9	

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Dynamic characteristics[2]						
Input capacitance	C_{iss}	V_{GS}=0 V, V_{DS}=25 V, f=1 MHz	-	2763	3600	pF
Output capacitance	C_{oss}		-	514	670	
Reverse transfer capacitance	C_{rss}		-	23	54	
Turn-on delay time	$t_{d(on)}$	V_{DD}=20 V, V_{GS}=10 V, I_D=60 A, R_G=3.5 Ω	-	4	-	ns
Rise time	t_r		-	5	-	
Turn-off delay time	$t_{d(off)}$		-	15	-	
Fall time	t_f		-	15	-	
Gate Charge Characteristics[2]						
Gate to source charge	Q_{gs}	V_{DD}=32 V, I_D=60 A, V_{GS}=0 to 10 V	-	9	12	nC
Gate to drain charge	Q_{gd}		-	4	9	
Gate charge total	Q_g		-	35	46	
Gate plateau voltage	$V_{plateau}$		-	3.2	-	V
Reverse Diode						
Diode continous forward current[2]	I_S	T_C=25 °C	-	-	60	A
Diode pulse current[2]	$I_{S,pulse}$		-	-	240	
Diode forward voltage	V_{SD}	V_{GS}=0 V, I_F=30 A, T_j=25 °C	-	0.9	1.3	V
Reverse recovery time[2]	t_{rr}	V_R=20 V, I_F=50A, di_F/dt=100 A/µs	-	37	-	ns
Reverse recovery charge[2]	Q_{rr}		-	33	-	nC

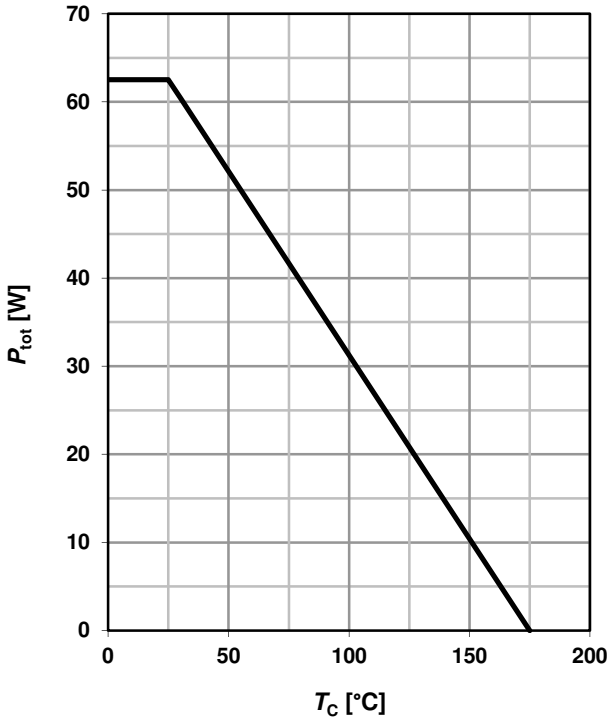
[1] Current is limited by package; with an R_{thJC} = 2.4 K/W the chip is able to carry 81 A at 25°C.

[2] Defined by design. Not subject to production test.

[3] T_J > 150°C is limited to 200h operation time over life time of the device

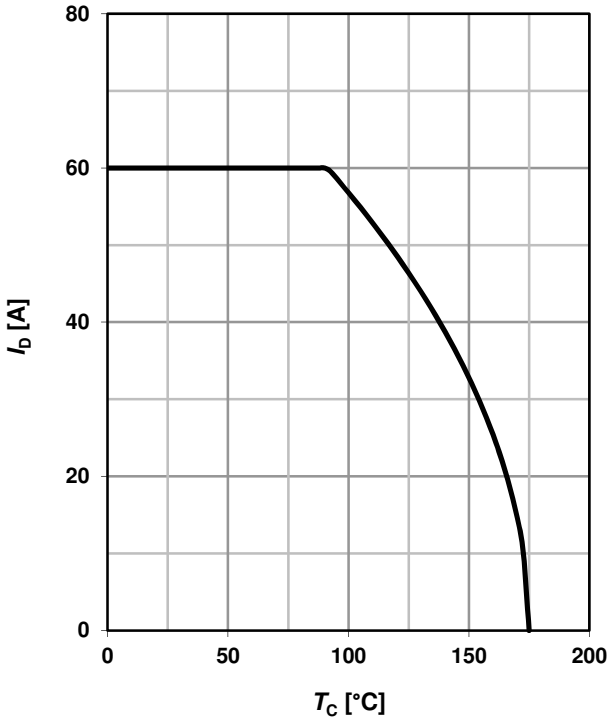
1 Power dissipation

$P_{tot} = f(T_C)$; $V_{GS} = 10$ V



2 Drain current

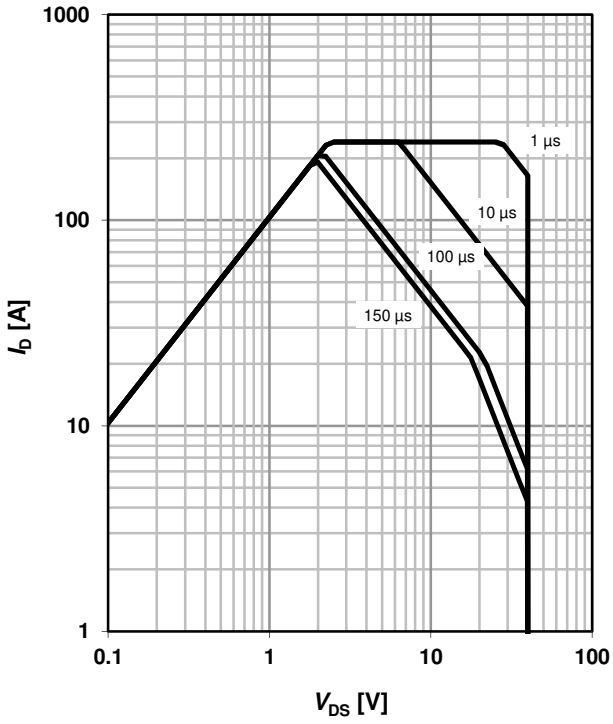
$I_D = f(T_C)$; $V_{GS} = 10$ V



3 Safe operating area

$I_D = f(V_{DS})$; $T_C = 25$ °C; $D = 0$

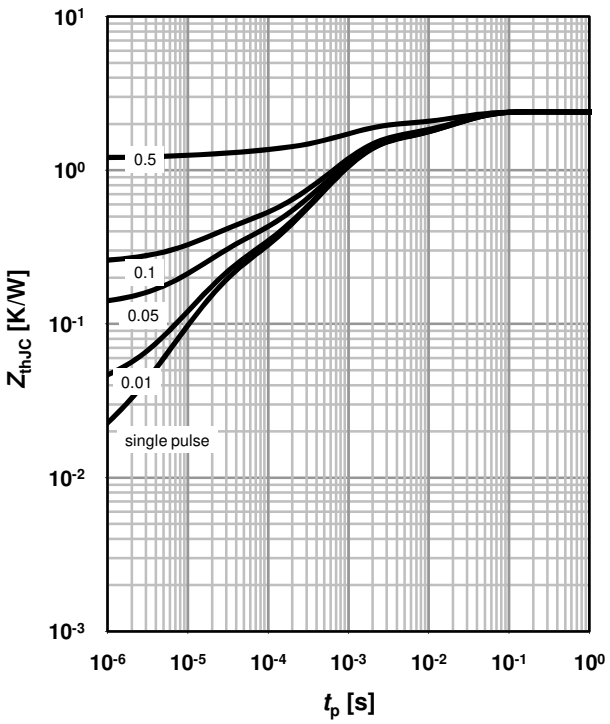
parameter: t_p



4 Max. transient thermal impedance

$Z_{thJC} = f(t_p)$

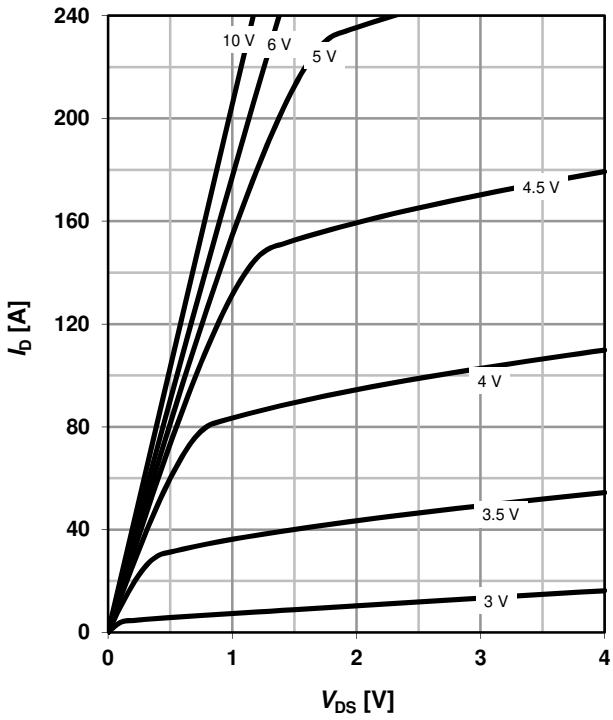
parameter: $D = t_p/T$



5 Typ. output characteristics

$I_D = f(V_{DS})$; $T_j = 25$ °C

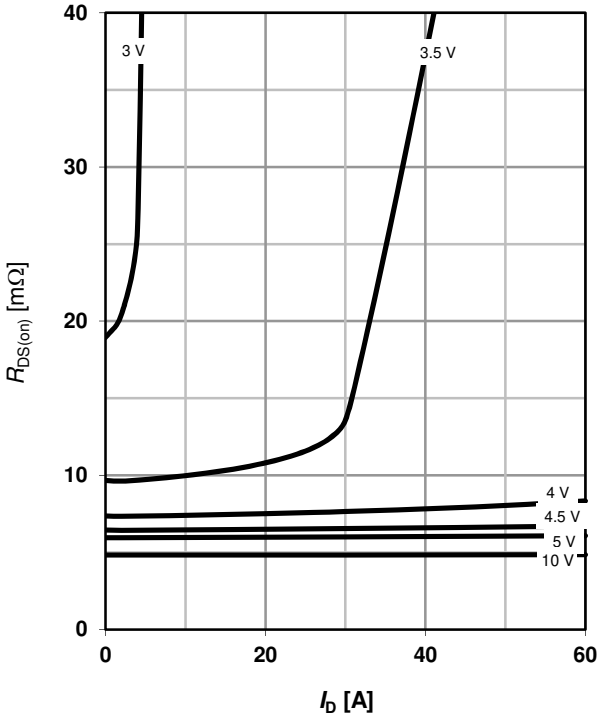
parameter: V_{GS}



6 Typ. drain-source on-state resistance

$R_{DS(on)} = (I_D)$; $T_j = 25$ °C

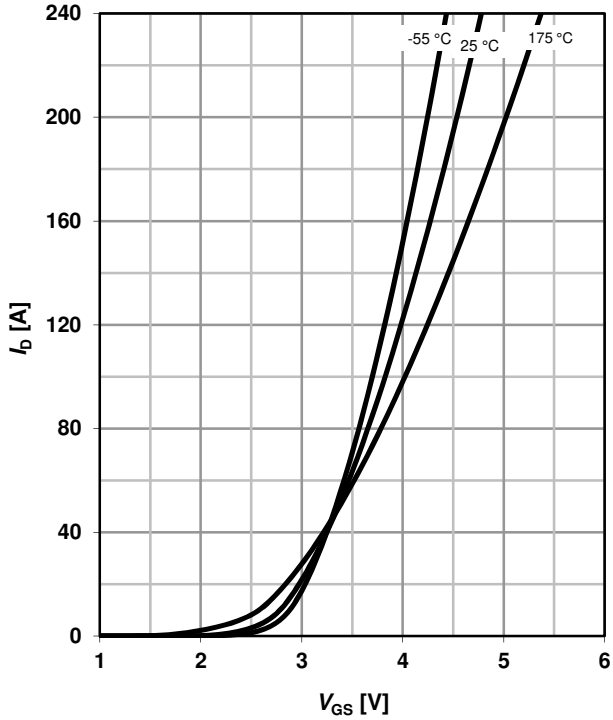
parameter: V_{GS}



7 Typ. transfer characteristics

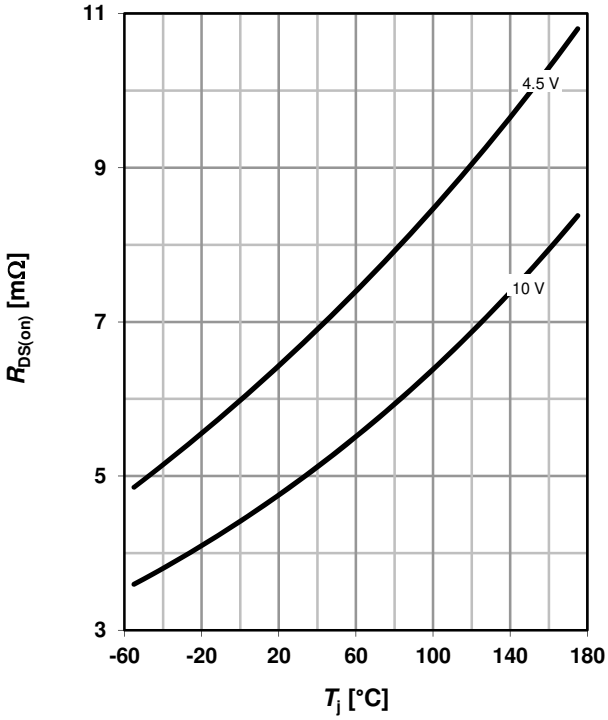
$I_D = f(V_{GS})$; $V_{DS} = 6$V

parameter: T_j



8 Typ. drain-source on-state resistance

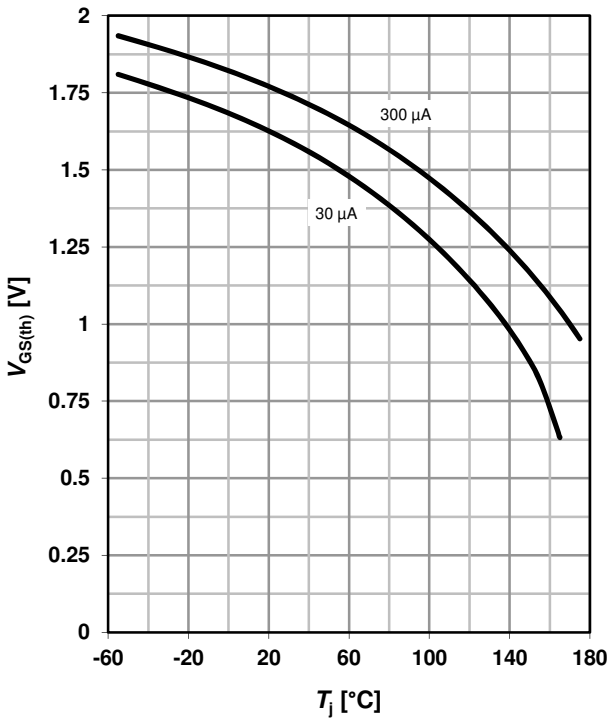
$R_{DS(on)} = f(T_j)$; $I_D = 100$ A; $V_{GS} = 10$ V



9 Typ. gate threshold voltage

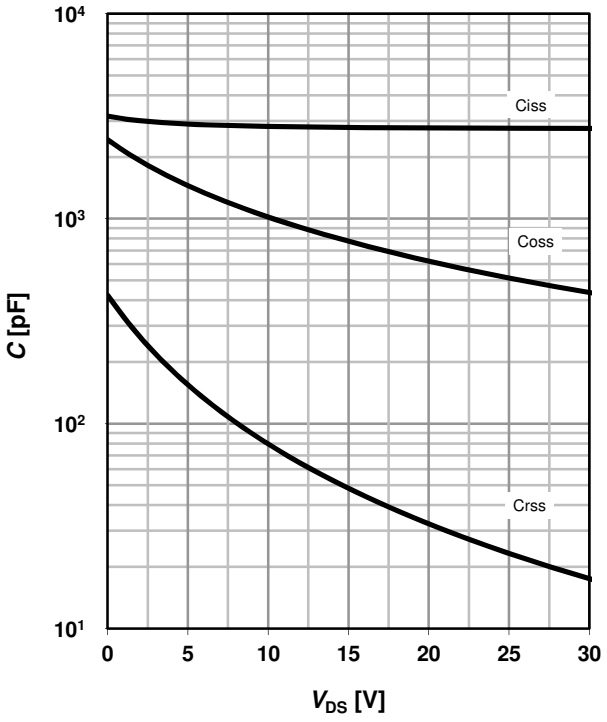
$V_{GS(th)} = f(T_j)$; $V_{GS} = V_{DS}$

parameter: I_D



10 Typ. capacitances

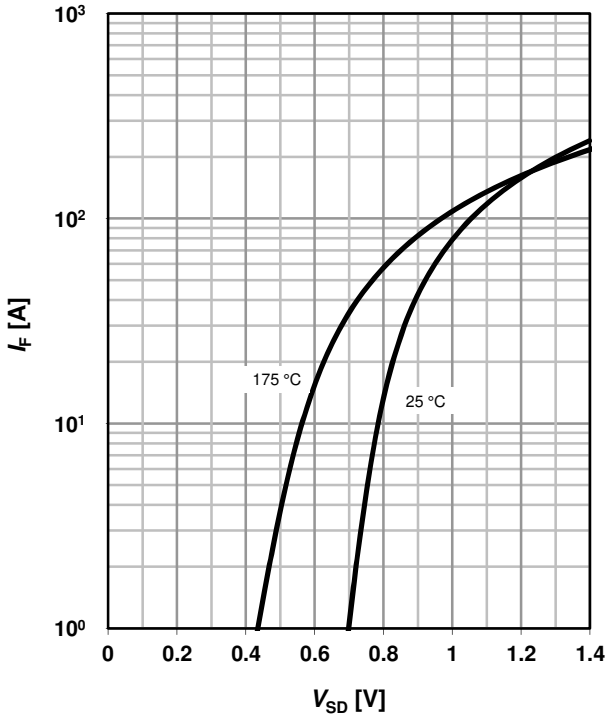
$C = f(V_{DS})$; $V_{GS} = 0$ V; $f = 1$ MHz



11 Typical forward diode characteristicis

IF = f(V_{SD})

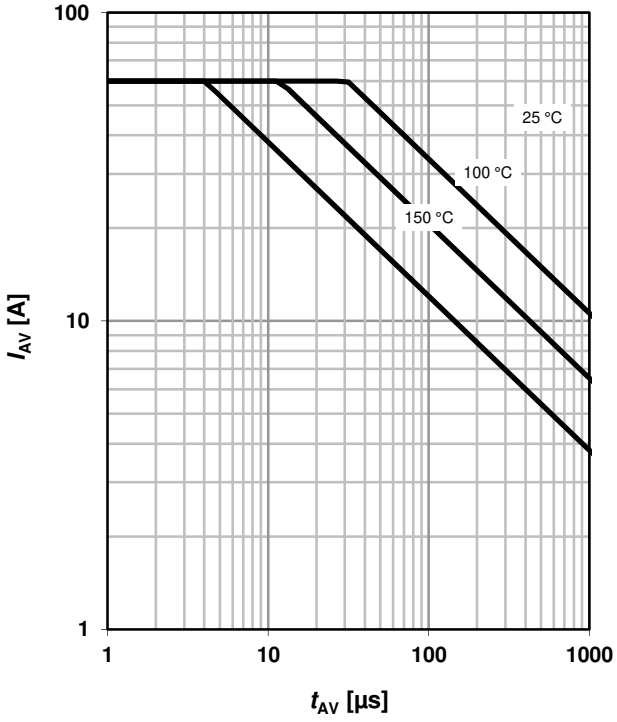
parameter: T_j



12 Typ. avalanche characteristics

$I_{AS} = f(t_{AV})$

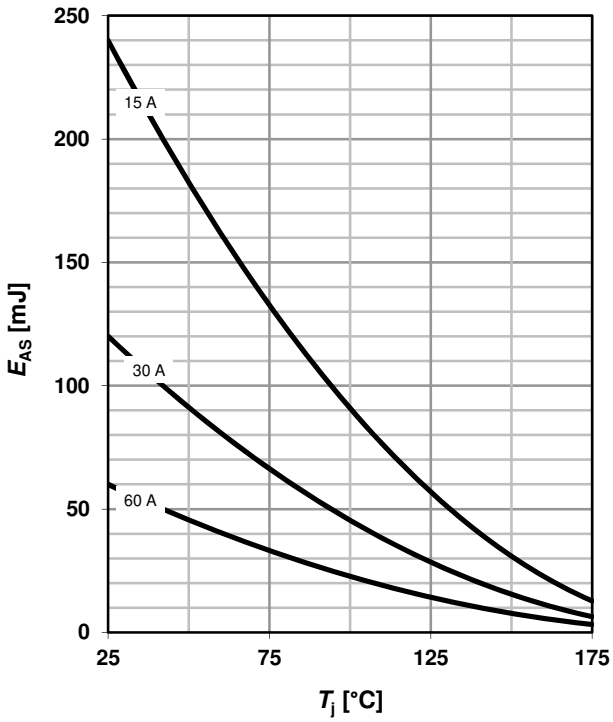
parameter: $T_{j(start)}$



13 Typical avalanche energy

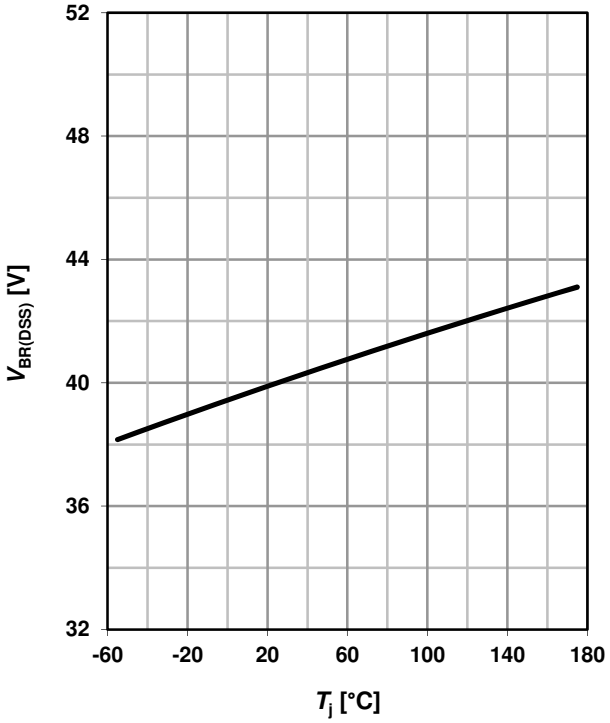
$E_{AS} = f(T_j)$

parameter: I_D



14 Drain-source breakdown voltage

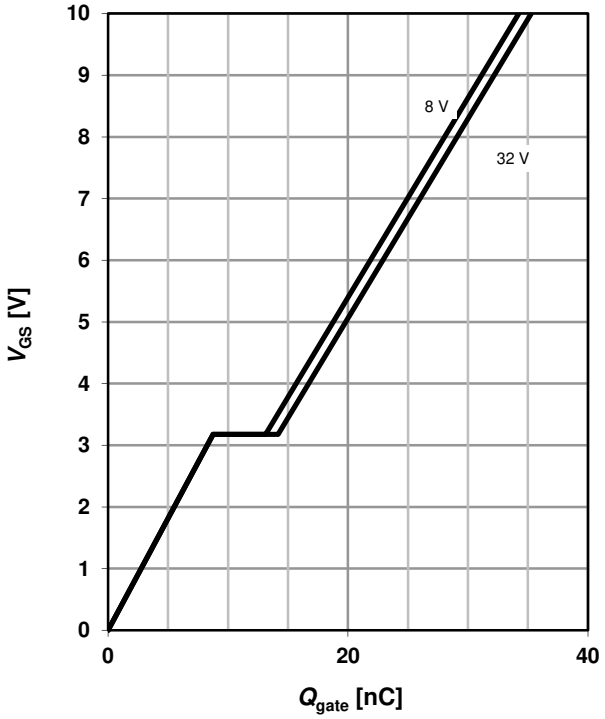
$V_{BR(DSS)} = f(T_j)$; $I_D = 1$ mA



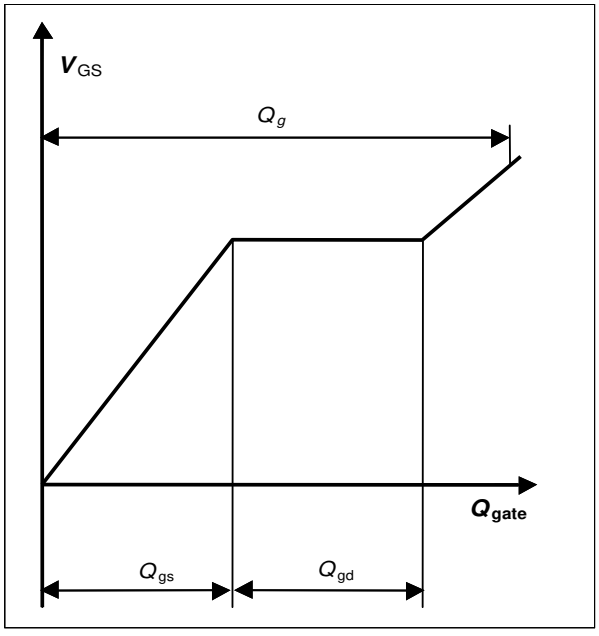
15 Typ. gate charge

$V_{GS} = f(Q_{gate})$; $I_D = 60$ A pulsed

parameter: V_{DD}



16 Gate charge waveforms



Published by
Infineon Technologies AG
81726 Munich, Germany

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Revision History

Version	Date	Changes
Revision 1.0	2015-05-22	Final Data Sheet