

QUICKSWITCH® PRODUCTS HIGH-PERFORMANCE CMOS ANALOG FOUR-CHANNEL SPDT MUX/DEMUX

FEATURES:

- Low ON resistance: rbs(on) = 5Ω
- Fast transition time: tTRAN = 6ns
- Wide bandwidth: 830MHz (-3dB point)
- Crosstalk: -115dB at 50KHz, -100dB at 5MHz, -66dB at 30MHz
- Off-isolation: -90dB at 50KHz, -60dB at 5MHz, -50dB at 30MHz
- Single 5V supply
- · Can be used as multiplexer or demultiplexer
- TTL-compatible control inputs
- Ultra-low quiescent current: 3µA
- · Available in QSOP package

APPLICATIONS:

- · High-speed video signal switching/routing
- · HDTV-quality video signal routing
- Audio signal switching/routing
- · Data acquisition
- ATE systems
- · Telecomm routing
- Switch between multiple video sources
- Token Ring transceivers
- · High-speed networking

FUNCTIONAL BLOCK DIAGRAM

DESCRIPTION:

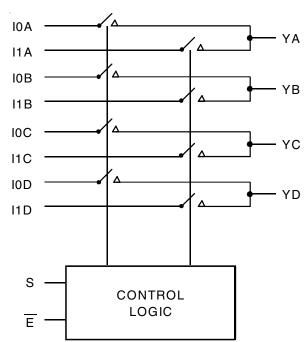
The QS4A205 is a high-performance CMOS analog Four-Channel SPDT multiplexer/demultiplexer with individual enables. The low Onresistance of the QS4A205 allows inputs to be connected to outputs with low insertion loss and high bandwidth. TTL-compatible control circuitry with "Break-Before-Make" feature prevents contention.

The QS4A205 with 830MHz bandwidth makes it ideal for high-performance video signal switching, audio signal switching, and telecomm routing applications. High performance and low power dissipation makes this device ideal for battery operated and remote instrumentation applications.

The QS4A205 is offered in the QSOP package and has several advantages over conventional packages such as PDIP and SOIC including:

- Reduced signal delays due to denser component packaging on circuit boards
- Reduced system noise due to less pin inductance resulting in lower ground bounce

The QS4A205 is characterized for operation at -40°C to +85°C.



IDT and the IDT logo are registered trademarks of Integrated Device Technology, Inc.

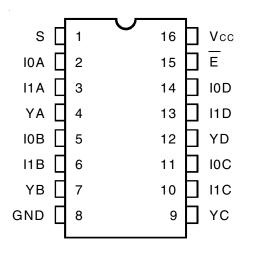
INDUSTRIAL TEMPERATURE RANGE

© 2019 Renesas Electronics Corporation

APRIL 2014

HIGH-PERFORMANCE CMOS ANALOG FOUR-CHANNEL SPDT MUX/DEMUX

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit	
VTERM ⁽²⁾	Supply Voltage to Ground	–0.5 to +7	V	
VTERM ⁽³⁾	DC Switch Voltage Vs	–0.5 to +7	V	
_	Analog Input Voltage	–0.5 to +7	V	
VTERM ⁽³⁾	DC Input Voltage VIN	–0.5 to +7	V	
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V	
Ιουτ	DC Output Current	120	mA	
Рмах	Maximum Power Dissipation	0.7	W	
TSTG	STG Storage Temperature		°C	

NOTES:

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

2. Vcc terminals.

3. All terminals except Vcc .

CAPACITANCE (TA = +25°C, f = 1MHz, VIN = 0V, VOUT = 0V)							
Symbol	Parameter ⁽¹⁾	Conditions	Тур.	Max.	Unit		
CMUX (OFF)	MUX Off Capacitance	\overline{E} = Vcc, Vin = Vout = 0V	5.6	-	рF		
CDEMUX(OFF)	DEMUX Off Capacitance	\overline{E} = Vcc, Vin = Vout = 0V	7.4	_	pF		
CMUX(ON)	MUX On Capacitance	\overline{E} = 0V, VIN = VOUT = 0V	12	_	рF		
CDEMUX(ON)	DEMUX On Capacitance	$\overline{E} = 0V$, Vin = Vout = 0V	15	_	pF		

NOTE:

1. As applicable to the device type.

PIN DESCRIPTION

Pin Names	I/O	Description	
IxA	I/O	Demux Port A	
IxB	I/O	Demux Port B	
IxC	I/O	Demux Port C	
IxD	I/O	Demux Port D	
Ē	I	Enable Input	
S	I	SelectInput	
YA-YD	I/O	Mux Port A-D	

FUNCTION TABLE(1)

Enable	Select	MUX/DEMUX Ports				
Ē	S	YA	YB	YC	YD	Function
Н	Х	Z	Z	Z	Z	Disable
L	L	10A	I0B	10C	I0D	Select 0
L	Н	I1A	I1B	I1C	I1D	Select 1

NOTE:

1. H = HIGH Voltage Level L = LOW Voltage Level

X = Don't Care

Z = High-Impedance

QSOP **TOP VIEW**

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified: Industrial: TA = -40° C to $+85^{\circ}$ C. Vcc = 5V ± 5%

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
Analog S	witch	•	•			
Vin	Analog Signal Range ⁽²⁾		-0.5	1	Vcc - 1	V
rds(on)	Drain-source ON resistance ^(2,3)	Vcc = Min., VIN = 0V, ION = 30mA	—	5	7	Ω
		Vcc = Min., VIN = 2.4V, ION = 15mA	—	13	17	
IC(OFF)	Channel Off Leakage Current	$I_N = V_{CC} \text{ or } 0V; Y_N = 0V \text{ or } V_{CC}; \overline{E} = V_{CC}$	—	2	_	nA
IC(ON)	Channel On Leakage Current	$I_N = Y_N = 0V$	—	2	_	nA
		(each channel is turned on sequentially)				
Digital Co	ntrol					
Vih	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	_	_	V
VIL	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	_	_	0.8	V
Dynamic (Characteristics			-		
t TRANS	Switching Time of MUX S to Y	RL = 1KΩ, CL = 100pF	0.5	_	6.6	ns
ton(ĒN)	Enable Turn-On Time, \overline{E} to Y	RL = 1KΩ, CL = 100pF	0.5	_	6	ns
$tOFF(\overline{EN})$	Enable Turn-Off Time, \overline{E} to Y	RL = 1KΩ, CL = 100pF	0.5	_	6	ns
t PD	Group Delay ^(2,4)	RL = 1KΩ, CL = 100pF	-	_	250	ps
f3dB	-3dB Bandwidth	VIN = 1Vp-p, RL = 75Ω	_	830	_	MHz
	Off-isolation	VIN = 1Vp-p, RL = 75Ω, f = 5MHz	_	-60	_	dB
Xtalk	Crosstalk	VIN = 1Vp-p, RL = 75Ω, f = 5MHz	_	-100	_	dB
QCI	Charge Injection		_	1.5	_	рC

NOTES:

1. Typical values are at Vcc = 5.0V, TA = 25°C.

2. Max value is guaranteed but not production tested.

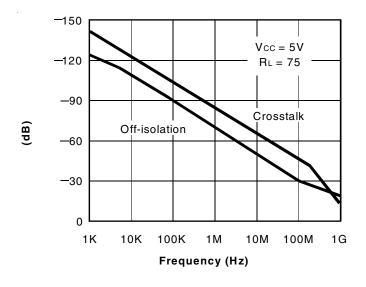
3. Measured by voltage drop between A and C pins or B and D pins at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, C, or B, D) pins.

4. The bus switch contributes no group delay other than the RC delay of the ON resistance of the switch and load capacitance. Group delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

POWER SUPPLY CHARACTERISTICS

Symbol Parameter		Test Conditions	Max.	Unit
lccq	QuiescentPower	Vcc = Max., VIN = GND or Vcc, f = 0	3	μA

TYPICAL CHARACTERISTICS

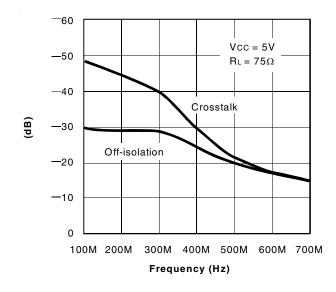




NOTES:

1. Crosstalk = 20 log |Vo/Vs|

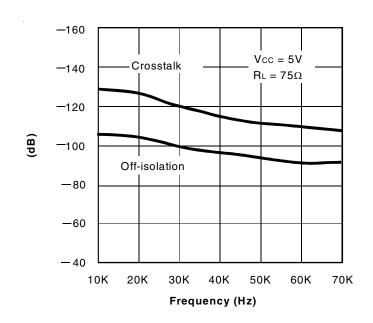
2. Off-isolation = 20 log |Vo/Vs|

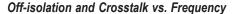


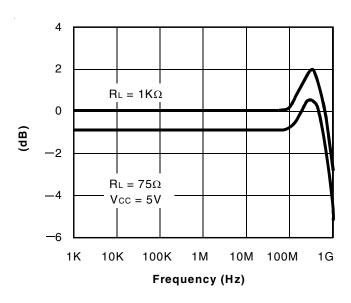
Off-isolation and Crosstalk vs. Frequency



1. Crosstalk = 20 log |Vo/Vs| 2. Off-isolation = 20 log |Vo/Vs|









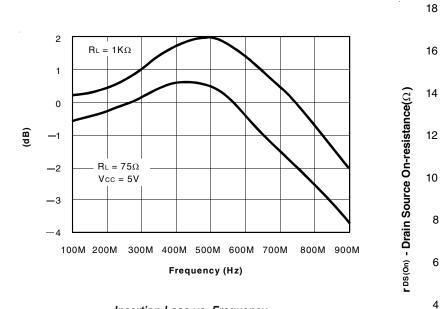
NOTE: 1. Insertion Loss = 20 log |Vo/Vs|

1. Crosstalk = 20 log |Vo/Vs|

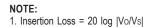
2. Off-isolation = 20 log |Vo/Vs|

NOTES:

TYPICAL CHARACTERISTICS (CONTINUED)



Insertion Loss vs. Frequency





3.0

2.5

On-Resistance vs. VIN

1.5

VIN (Volts)

2.0

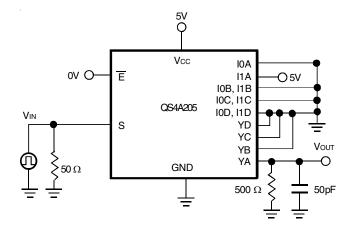
1.0

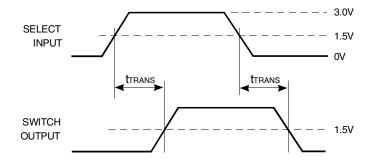
0.0

0.5

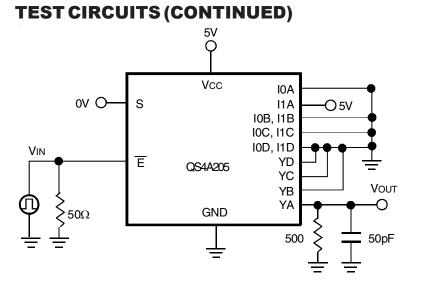
Vcc = 4.75V

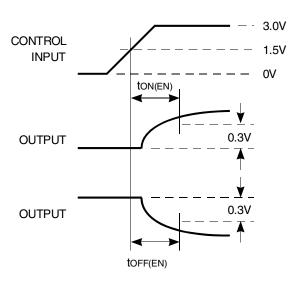
TEST CIRCUITS



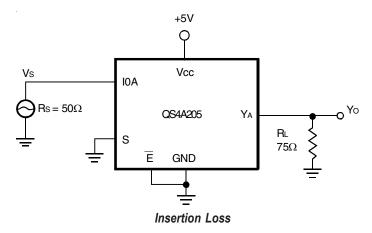


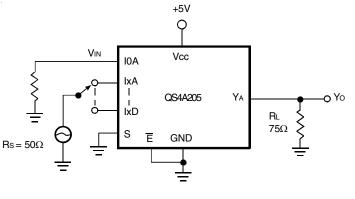
Transition Time





Enable Switching Time

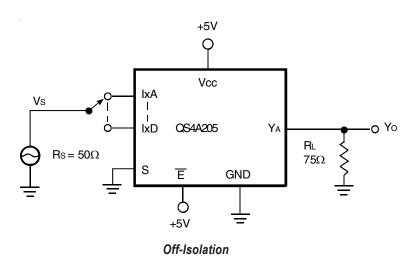




Crosstalk

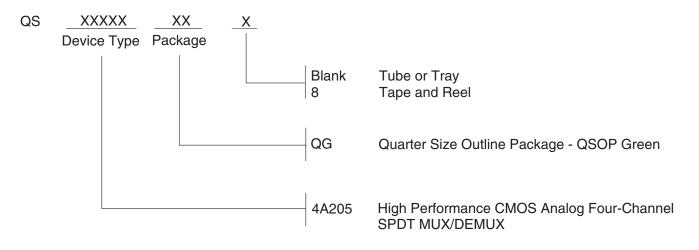
NOTE: 1. Crosstalk = 20 log |Vo/Vs|

NOTE: 1. Insertion Loss = 20 log |Vo/Vs|



NOTE: 1. Off-isolation = 20 log |Vo/Vs|

ORDERING INFORMATION



IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers skilled in the art designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only for development of an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising out of your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use o any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.0 Mar 2020)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners. **Contact Information**

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit: <u>www.renesas.com/contact/</u>