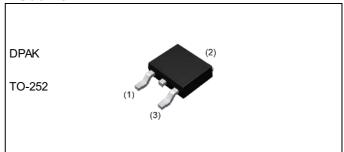


RD3U041AAFRA

Nch 250V 4A Power MOSFET

V_{DSS}	250V
R _{DS(on)} (Max.)	1.3Ω
I _D	±4.0A
P _D	29W

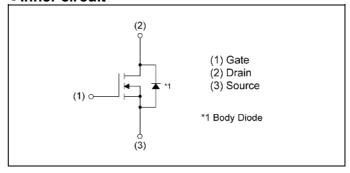
●Outline



Features

- 1) Low on-resistance
- 2) Fast switching
- 3) Drive circuits can be simple
- 4) Pb-free plating; RoHS compliant
- 5) AEC-Q101 Qualified

Inner circuit



Packaging specifications

or ackaging specifications							
Packing	Embossed Tape						
Reel size (mm)	330						
Tape width (mm)	16						
Quantity (pcs)	2500						
Taping code	TL						
Marking	RD3U041AA						
	Packing Reel size (mm) Tape width (mm) Quantity (pcs) Taping code						

Application

Switching Power Supply

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	250	V
Continuous drain current (T _c = 25°C)	I _D *1	±4.0	А
Pulsed drain current	I _{DP} *2	±16	А
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	2.0	А
Avalanche energy, single pulse	E _{AS} *3	1.61	mJ
Power dissipation (T _c = 25°C)	P _D *4	29	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Downwortow	Cymah al	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *4	-	-	4.3	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	100	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

● Electrical characteristics (T_a = 25°C)

Darameter	Symbol Conditions -		Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		250	-	-	V	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 250V, V _{GS} = 0V	-	-	10	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	-	-	±100	nA	
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	3.5	-	5.5	V	
Static drain - source on - state resistance	R _{DS(on)} *6	V _{GS} = 10V, I _D = 2.0A	-	0.93	1.3	Ω	
Gate resistance	R_{G}	f = 1MHz, open drain	-	4.0	-	Ω	

● Electrical characteristics (T_a = 25°C)

Darramatar	Cymah al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	350	-	
Output capacitance C _{oss}		V _{DS} = 25V	-	30	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	15	-	
Turn - on delay time	t _{d(on)} *6	V _{DD} ≈ 125V, V _{GS} = 10V	-	15	-	
Rise time	t _r *6	I _D = 2.0A	-	14	-	
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 62\Omega$	-	18	-	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	15	-	

● Gate charge characteristics (T_a = 25°C)

Darameter	Symbol Conditions		Values			Unit	
Parameter			Min.	Тур.	Max.	Offic	
Total gate charge	Q_g^{*6}	V _{DD} ≃ 125V	-	8.5	-		
Gate - Source charge	Q _{gs} *6	I _D = 4.0A	-	3.5	-	nC	
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	3.5	-		
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 125V, I _D = 4.0A	-	7.8	-	V	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \simeq 500 μ H, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C Fig.3-1,3-2

^{*4} T_c=25°C

^{*5} Mounted on an epoxy PCB FR4 (20×20×0.8mm)

^{*6} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Offic	
Source current	I _S *1	T _C = 25°C	1	-	4.0	Α	
Pulsed source current	I _{SP} *2	1C - 23 C	1	-	16	Α	
Source-Drain voltage	V _{SD} *6	$V_{GS} = 0V, I_{S} = 4.0A$	-	-	1.5	V	
Reverse recovery time	t _{rr} *6		-	95	-	ns	
Reverse recovery charge	arge $Q_{rr}^{*6} \qquad I_{S} = 4.0A$ $di/dt = 100A/\mu s$		-	0.3	-	μC	
Peak reverse recovery current	_{rr} *6		-	6.3	-	А	

Fig.1 Power Dissipation Derating Curve

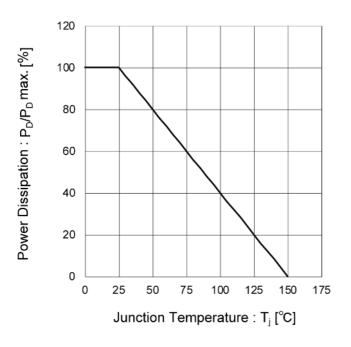


Fig.2 Drain Current Derating Curve

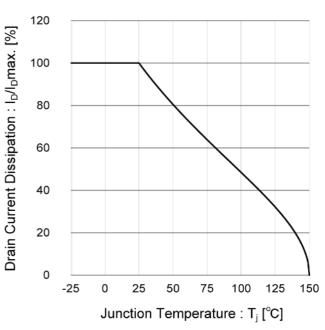


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

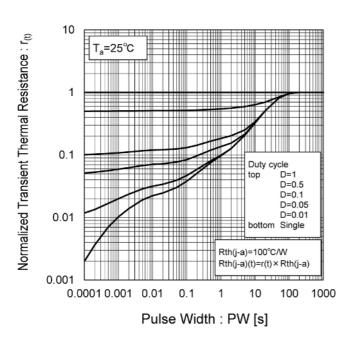
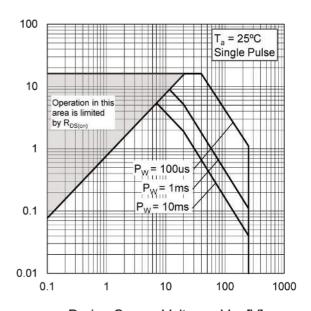


Fig.4 Maximum Safe Operating Area



Drain Current : Ip [A]

Fig.5 Avalanche Energy Derating Curve

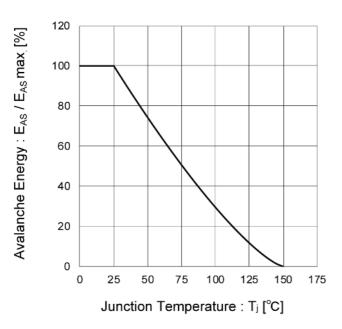


Fig.6 Normalized Breakdown Voltage vs.
Junction Temperature

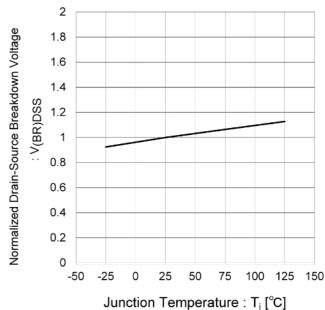


Fig.7 Output Characteristics(I)

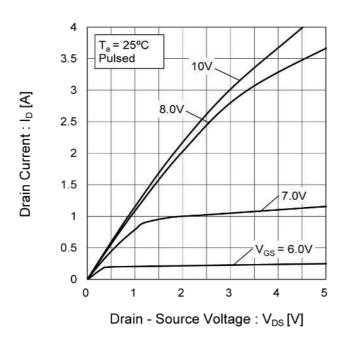
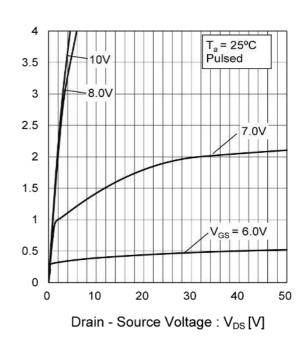


Fig.8 Output Characteristics(II)



Drain Current: Ip [A]

Fig.9 Gate Threshold Voltage vs. Drain Current

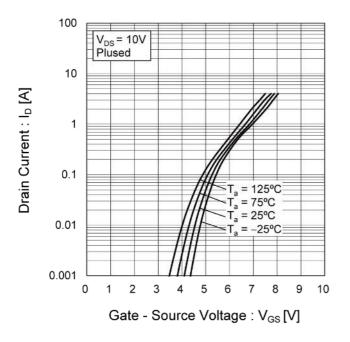


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

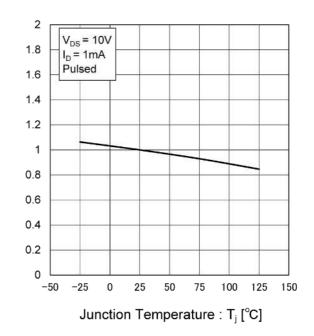
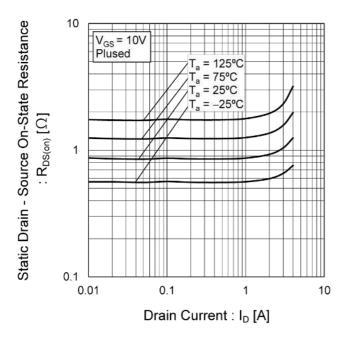


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current



Static Drain - Source On-State Resistance : $R_{\text{DS}(\text{on})}\left[\Omega\right]$

Normalized Gate Threshold Voltage: V_{GS(th)}

Fig.12 Static Drain - Source On - State
Resistance vs. Gate Source Voltage

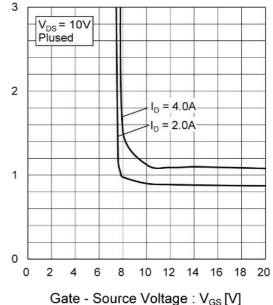


Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

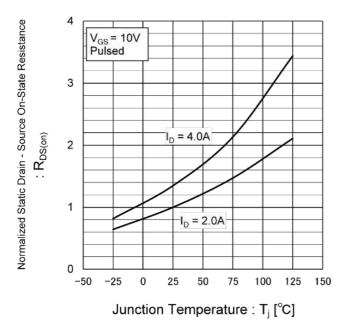


Fig.14 Capacitances

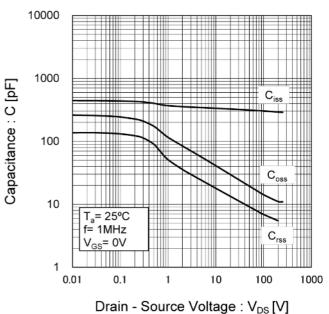


Fig.15 Switching Times

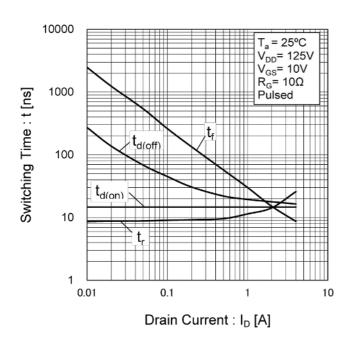
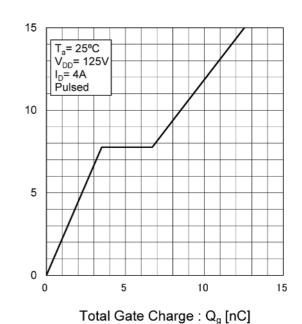


Fig.16 Gate Charge



Gate - Source Voltage : V_{GS} [V]

Fig.17 Source Current vs. Source - Drain Voltage

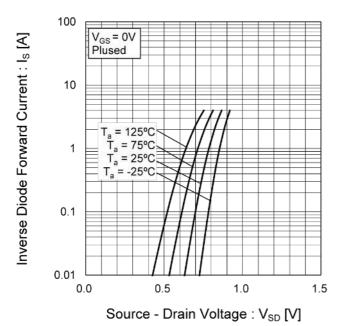
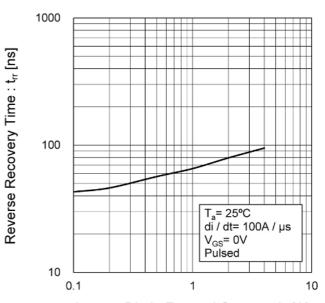


Fig.18 Reverse Recovery Time vs. Source Current



Inverse Diode Forward Current: Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

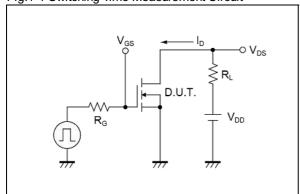


Fig.2-1 Gate Charge Measurement Circuit

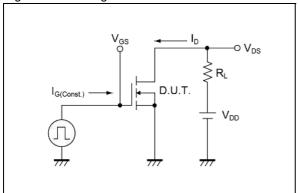


Fig.3-1 Avalanche Measurement Circuit

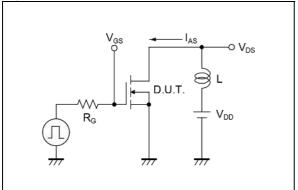


Fig.4-1 trr Measurement Circuit

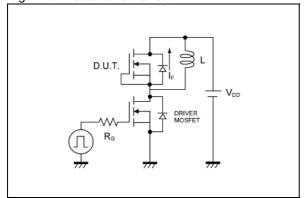


Fig.1-2 Switching Waveforms

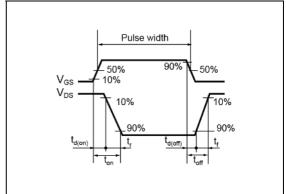


Fig.2-2 Gate Charge Waveform

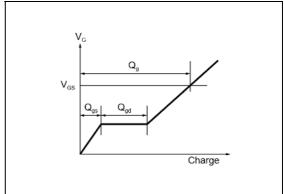


Fig.3-2 Avalanche Waveform

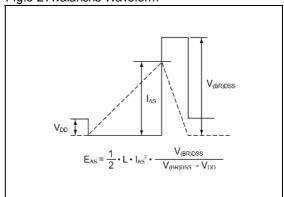
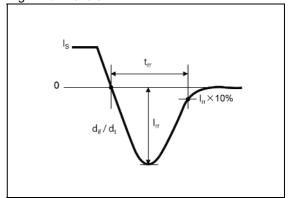
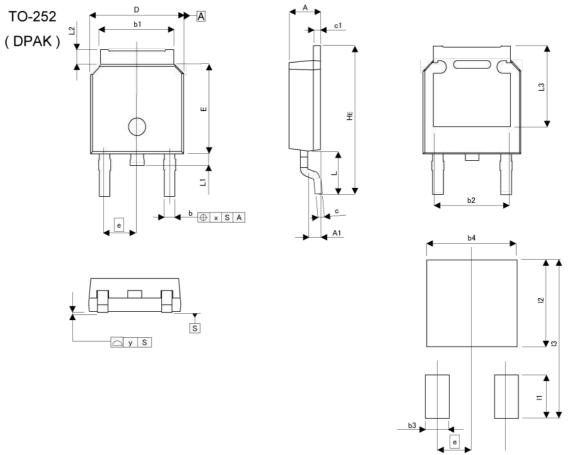


Fig.4-2 trr Waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	2.10	2.30	0.083	0.091
A1	0.70	1.10	0.028	0.043
b	0.65	0.85	0.026	0.033
b1	5.10	5.40	0.201	0.213
b2	5.	10	0.2	201
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.0	91
E	6.00	6.40	0.236	0.252
HE	9.50	10.50	0.374	0.413
L	2.	90	0.1	14
L1	0.70	0.90	0.028	0.035
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.2	209
Х	-	0.10	10 - 0	0.004
у	-	0.10		0.004

DIM	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
b3	25	1.10	7/27	0.043
b4	+	5.40	3,53	0.213
11	20	2.90	Nay	0.114
12	*	5.50	0.50	0.217
13	<u>~</u>	10.50	1524	0.413

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSIIb	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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