



BS107P

200V N-CHANNEL ENHANCEMENT MODE VERTICAL DMOSFET

Features

- BV_{DSS} > 200V
- $R_{DS(ON)} \le 23\Omega @ V_{GS} = 2.6V$
- I_D = 120mA Maximum Continuous Drain Current
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q101, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

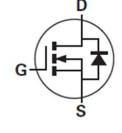
https://www.diodes.com/products/automotive/automotive-products/.

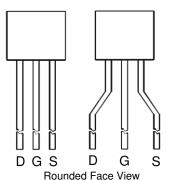
- This part is qualified to JEDEC standards (as references in AEC-Q101) for High Reliability.
- https://www.diodes.com/quality/product-definitions/

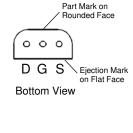
Mechanical Data

- Case: E-Line (TO-92 Compatible)
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Rating 94V-0
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.159 grams (Approximate)









Ordering Information (Note 4)

Product	Marking	Package	Leads	Quantity
BS107P	BS107	E-Line	Straight	4,000 Loose in a Box
BS107PSTZ	BS107	E-Line	Joggled	2,000 Taped per Ammo Box

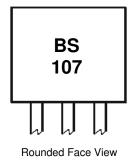
Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

Device Symbol

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



BS107 = Product Type Marking Code



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	200	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	I _D	120	mA
Pulsed Drain Current	I _{DM}	2	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P_{D}	500	mW
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Leads	(Note 6)	$R_{ heta JL}$	71	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes:

- 5. For a through-hole device mounted on the minimum recommended pad layout with 12mm lead length from the bottom of package to the single-sided FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.

 6. Thermal resistance from junction to solder-point at the seating plane (2.5mm from the bottom of package along the drain lead).

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

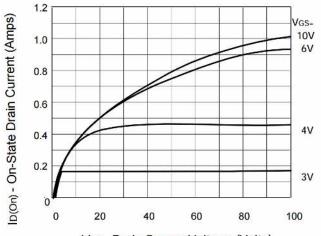
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	200	230	_	V	$I_D = 100 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	30	nA	$V_{DS} = 130V, V_{GS} = 0V$	
Drain Cut-Off Current	I _{DSX}	_	_	1	μΑ	$V_{DS} = 70V, V_{GS} = 0.2V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	nA	$V_{GS} = \pm 15V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	3.0	V	$I_D = 1mA$, $V_{DS} = V_{GS}$	
Static Drain-Source On-Resistance (Note 7)		R _{DS(ON)} —	15	23	Ω	$V_{GS} = 2.6V, I_D = 25mA$	
Static Drain-Source On-Nesistance (Note 7)	HDS(ON)		_	30		$V_{GS} = 5V, I_D = 100mA$	
Forward Transconductance (Notes 7 & 9)	9 _{fs}	100	_	_	mS	$V_{DS} = 25V, I_D = 250mA$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	_	85		V 05V V 0V	
Output Capacitance	Coss	_	_	20	рF	$V_{DS} = 25V$, $V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	_	7			
Turn-On Delay Time (Note 8)	t _{D(ON)}	_	_	7		$V_{DD} = 25V, I_D = 250mA$	
Turn-On Rise Time (Note 8)	t _R	_	_	8			
Turn-Off Delay Time (Note 8)	t _{D(OFF)}	_	_	16	ns		
Turn-Off Fall Time (Note 8)	t _F	_	_	8			

Notes:

- 7. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$. Duty cycle $\leq 2\%$.
- 8. Switching characteristics are independent of operating junction temperature. Switching times are measured with 50Ω source impedance and <5ns rise time on a pulse generator.
- 9. For design aid only, not subject to production testing.

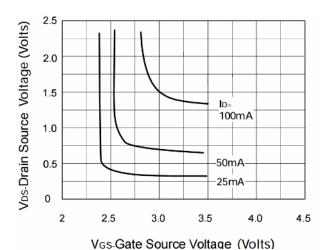


Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

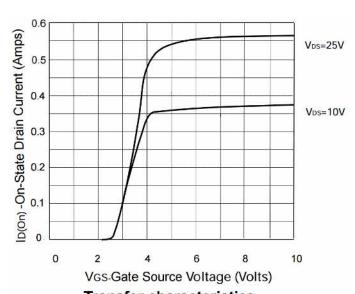


V_{DS} - Drain Source Voltage (Volts)

Output Characteristics



Voltage Saturation Characteristics

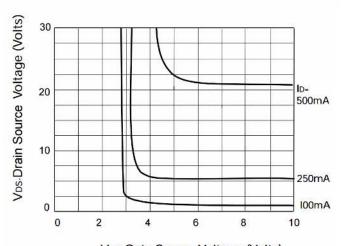


Transfer characteristics



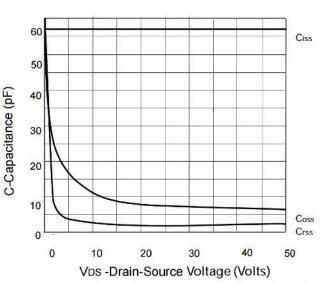
V_{DS} - Drain Source Voltage (Volts)

Saturation Characteristics



VGS-Gate Source Voltage (Volts)

Voltage Saturation Characteristics

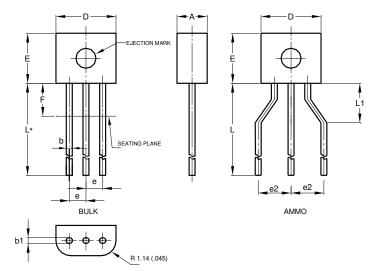


Capacitance v drain-source voltage



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



E-Line					
Dim	Min	Max	Тур		
Α	2.16	2.41	_		
b	0.41	0.495	_		
b1	0.41	0.495	_		
D	4.37	4.77	-		
Е	3.61	4.01	_		
е	_	_	1.27		
e2	_	_	2.54		
F	_	2.50	-		
L	13.00	13.97	_		
L1	2.50	3.50	_		
All Dimensions in mm					



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