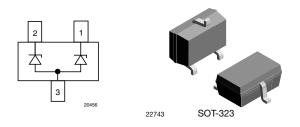
# VESD01A2-03G to VESD33A2-03G

**Vishay Semiconductors** 

## **Dual-Line ESD-Protection Diode Array in SOT-323**



www.vishay.com

#### **MARKING** (example only)



22744

ABC = type code (see table below) WW = date code working week VY = date code year

#### LINKS TO ADDITIONAL RESOURCES



### iede Arrentie CO

#### **FEATURES**

- Compact SOT-323 package
- 2-line unidirectional ESD-protection
- AEC-Q101 qualified available
- Working range 1 V to 33 V
- ESD immunity acc. IEC 61000-4-2 ±15 kV to ±30 kV contact discharge ±15 kV to ±30 kV air discharge
- Lead plating: Sn (e3)
  soldering can be checked by standard vision
  - inspectionAOI = Automated Optical Inspection
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

ORDERING INFORMATION							
		ENVIRONM	ENVIRONMENTAL AND QUALITY CODE				
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	RoHS COMPLIANT + LEAD (Pb)-FREE TERMINATIONS			ORDERING CODE (EXAMPLE)		
		GREEN		15K = MOQ			
VESD05A2-03G	-	G	3	-08	VESD05A2-03G-G3-08		
VESD05A2-03G	Н	G	3	-08	VESD05A2-03GHG3-08		

PACKAGE DAT	Ά					
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VESD01A2-03G-G3	SOT-323	D01				
VESD03A2-03G-G3	SOT-323	D03				
VESD05A2-03G-G3	SOT-323	D05				
VESD08A2-03G-G3	SOT-323	D08	E 0 mm	UL 94 V-0	MSL level 1	Deals temperature may 060 °C
VESD12A2-03G-G3	SOT-323	D12	5.2 mg	UL 94 V-0	(according J-STD-020)	Peak temperature max. 260 °C
VESD16A2-03G-G3	SOT-323	D16				
VESD26A2-03G-G3	SOT-323	D26				
VESD33A2-03G-G3	SOT-323	D33				

Rev. 1.3, 05-Oct-2022

1 contact: ESDor







ABSOLUTE MAXIMUM RATINGS VESD01A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	11	А		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	70	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	VESD	kV			
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD03A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	11.6	А		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD05A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	8.7	A		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD08A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	6.60	A		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	N/	30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		



ABSOLUTE MAXIMUM RATINGS VESD12A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	4.4	А		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	e acc. IEC 61000-4-2; 10 pulses	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD16A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	3.6	А		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD26A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	2.1	A		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	20	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	20	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD33A2-03G T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	1.6	A		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	15	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	15	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		



<b>ELECTRICAL CHARACTERISTICS</b> VESD01A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	1	V	
Reverse voltage	at I <sub>R</sub> = 100 μA	V <sub>R</sub>	1	1.2	-	V	
Reverse current	at V <sub>R</sub> = 1 V	I <sub>R</sub>	-	20	100	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 20 mA	V <sub>BR</sub>	2.5	2.65	2.8	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 11 \text{ A}$ , $t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	5.6	6.4	V	
Forward elemping voltage	at I <sub>PP</sub> = 1 A, t <sub>p</sub> = 300 μs	V <sub>F</sub>	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 11 \text{ A}$ , $t_p = 8/20 \mu\text{s}$	V <sub>F</sub>	-	2.5	3.2	V	
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	0.13	-	Ω	
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	153	192	230	pF	

ELECTRICAL CHARACTERISTICS VESD03A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	3	V	
Reverse voltage	at I <sub>R</sub> = 20 µA	V <sub>R</sub>	3	-	-	V	
Reverse current	at $V_R = 3 V$	I <sub>R</sub>	-	8	20	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	4.4	4.65	4.9	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 11.6 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>C</sub>	-	7.8	8.70	V	
Forward clamping voltage	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	V <sub>F</sub>	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 11.6 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>F</sub>	-	2.6	3.32	V	
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	0.19	-	Ω	
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	89	112	135	pF	

ELECTRICAL	CHARACTERISTICS VESD05A2-03G	
ELEVINIVAL	CHARACIERISIICS VESDUSAZ-USG	

 $(T_{amb} = 25 \text{ °C}, \text{ between pin } 1 - 3 \text{ or } 2 - 3, \text{ unless otherwise specified})$ 

(T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	5	V	
Reverse voltage	at I <sub>R</sub> = 1 μA	V <sub>R</sub>	5	-	-	V	
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	0.01	0.1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6.85	7.26	7.65	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 8.7 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>C</sub>	-	10.3	11.5	V	
Forward clamping voltage	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	V <sub>F</sub>	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 8.7 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>F</sub>	-	2.2	2.74	V	
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	0.2	-	Ω	
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	53	67	81	pF	



<b>ELECTRICAL CHARACTERISTICS</b> VESD08A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	8	V		
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	8	-	-	V		
Reverse current	at V <sub>R</sub> = 8 V	I <sub>R</sub>	-	0.01	0.1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	9.5	10	10.5	V		
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 6.6 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>C</sub>	-	13.7	15.3	V		
Forward alamaing valtage	at I <sub>PP</sub> = 1 A, t <sub>p</sub> = 300 μs	V <sub>F</sub>	0.9	1.1	1.2	V		
Forward clamping voltage	at $I_{PP} = I_{PPM} = 6.6 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>F</sub>	-	1.9	2.32	V		
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	0.23	-	Ω		
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	37	47	57	pF		

<b>ELECTRICAL CHARACTERISTICS</b> VESD12A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	12	V	
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	12	-	-	V	
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	-	0.01	0.1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	13.9	14.7	15.5	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 4.4 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>C</sub>	-	20.5	22.7	V	
Forward elemping voltage	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	V <sub>F</sub>	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 4.4 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>F</sub>	-	1.6	1.88	V	
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	0.4	-	Ω	
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	26	33	40	pF	

$(T_{amb} = 25 \text{ °C}, between pin 1 - 3 or 2 - 3, unless otherwise specified)$							
PARAMETER TEST CONDITIONS/REMARKS SYMBOL MIN. TY							
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-			
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-			
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	16	-			
Reverse current	at V <sub>R</sub> = 16 V	I <sub>R</sub>	-	0.01			

Reverse current	at V <sub>R</sub> = 16 V	I <sub>R</sub>	-	0.01	0.1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	17	17.9	18.8	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 3.6 \text{ A}, t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	25.3	28	V
	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	V <sub>F</sub>	0.9	1.1	1.2	V
Forward clamping voltage	at $I_{PP} = I_{PPM} = 3.6 \text{ A}, t_p = 8/20 \ \mu\text{s}$	V <sub>F</sub>	-	1.5	1.72	V
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	0.53	-	Ω
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	CD	21	27	33	pF

UNIT

lines

۷

V

**MAX.** 

16

-

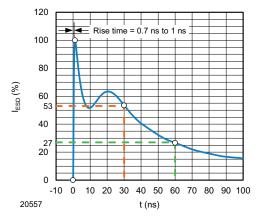


<b>ELECTRICAL CHARACTERISTICS</b> VESD26A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	26	V		
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	26	-	-	V		
Reverse current	at V <sub>R</sub> = 26 V	I <sub>R</sub>	-	< 0.01	0.1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	27.6	29.1	30.6	V		
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 2.1 \text{ A}, t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	43	48	V		
Forward alamping voltage	at I <sub>PP</sub> = 1 A, t <sub>p</sub> = 300 μs	V <sub>F</sub>	0.9	1.1	1.2	V		
Forward clamping voltage	at $I_{PP} = I_{PPM} = 2.1 \text{ A}, t_p = 8/20 \mu\text{s}$	V <sub>F</sub>	-	1.3	1.42	V		
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	1.9	-	Ω		
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	14	17.5	21	pF		

<b>ELECTRICAL CHARACTERISTICS</b> VESD33A2-03G (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	33	V	
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	33	-	-	V	
Reverse current	at V <sub>R</sub> = 33 V	I <sub>R</sub>	-	< 0.01	0.1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	35.5	37.4	39.3	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 1.6 \text{ A}, t_p = 8/20 \ \mu \text{s}$	V <sub>C</sub>	-	56	62.5	V	
Forward elemping voltage	at $I_{PP} = 1 \text{ A}, t_p = 300 \mu\text{s}$	V <sub>F</sub>	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 1.6 \text{ A}, t_p = 8/20 \mu\text{s}$	V <sub>F</sub>	-	1.22	1.32	V	
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; reverse direction)	r <sub>dyn</sub>	-	3.6	-	Ω	
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	12	15	18	pF	

VESD01A2-03G to VESD33A2-03G

**Vishay Semiconductors** 



www.vishay.com

Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$  / 150 pF)

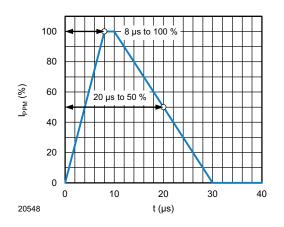


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

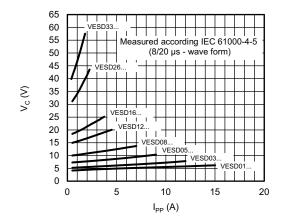


Fig. 3 - Typical Peak Clamping Voltage vs. Peak Pulse Current

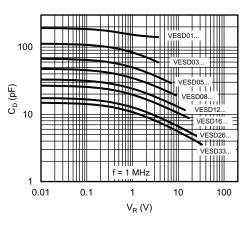


Fig. 4 - Typical Capacitance vs. Reverse Voltage

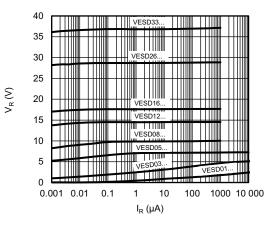


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

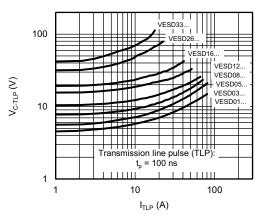


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current

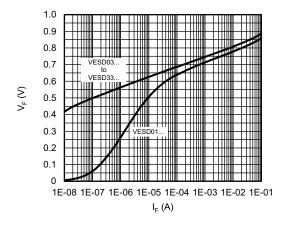
Rev. 1.3, 05-Oct-2022

7

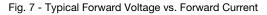
Document Number: 86150

For technical questions, contact: <u>ESDprotection@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>





www.vishay.com



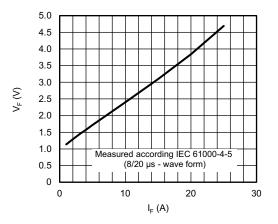
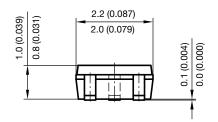
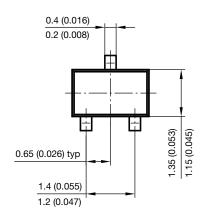


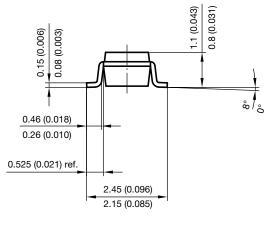
Fig. 8 - Typical Forward Voltage vs. Forward Current

#### PACKAGE DIMENSIONS in millimeters (inches): SOT-323

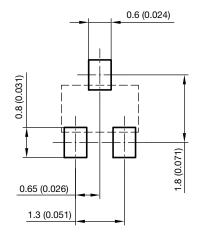




Document no.: 6.541-5040.02-4 Rev. 1 - Date: 06. April 2010 21113



foot print recommendation:



Rev. 1.3, 05-Oct-2022

8 For technical questions, contact: <u>ESDprotection@vishav.com</u> Document Number: 86150

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

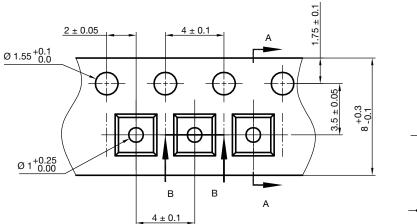


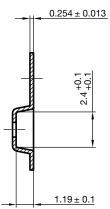
### VESD01A2-03G to VESD33A2-03G

A-A Section

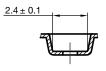
### **Vishay Semiconductors**

#### **CARRIER TAPE SOT-323**



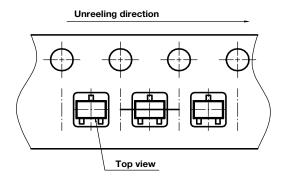


**B-B Section** 



Document no.: S8-V-3717.08-002 (4) Created - Date: 09. Feb. 2010 22762

#### **ORIENTATION IN CARRIER TAPE SOT-323**



Document no.: S8-V-3717.08-002 (4) Created - Date: 09. Feb. 2010 22761

9



Vishay

### Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.