

High Performance Schottky Rectifier, 120 A





HALF-PAK (D-67)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	120 A			
V_{R}	30 V			
Package	HALF-PAK (D-67)			
Circuit configuration	Single diode			

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-122NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	120	Α		
V _{RRM}		30	V		
I _{FSM}	t _p = 5 μs sine	$t_p = 5 \mu s \text{ sine}$ 18 000			
V _F	120 A _{pk} , T _J = 125 °C	120 A _{pk} , T _J = 125 °C 0.47			
T _J	Range	Range -55 to +150			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-122NQ030PbF	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 115 °C, rectangular waveform		120	Α
Maximum peak one cycle non-repetitive surge current I _{ESN}	l=a	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	18 000	А
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	2000	^
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 11 A, L = 1 mH		54	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		12	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	120 A	- T _{.1} = 25 °C	0.57	
Maximum forward voltage drop per leg		240 A	1j=25 C	0.75	V
See fig. 1		120 A	T _{.1} = 125 °C	0.47	
		240 A	1J = 125 C	0.67	
Maximum reverse leakage current per leg			$V_{\rm B}$ = Rated $V_{\rm B}$	10	mA
See fig. 2		T _J = 125 °C	VR = nateu VR	560	I IIIA
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		7400	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage ter	mperature range	T _J , T _{Stg}		-55 to 150	°C	
Maximum thermal resistance, junc	tion to case	R _{thJC}	DC operation See fig. 4	0.38	°C/W	
Typical thermal resistance, case to	heatsink	R _{thCS}	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	oz.	
Mounting toyour	minimum			3 (26.5)		
Mounting torque	maximum		Name to the signature of the second of	4 (35.4)	N⋅m	
Terminal torque ——	minimum		Non-lubricated threads 3.4 (30)	3.4 (30)	(lbf · in)	
	maximum			5 (44.2)		
Case style				HALF-PAK module		

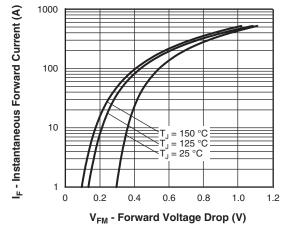


Fig. 1 - Maximum Forward Voltage Drop Characteristics

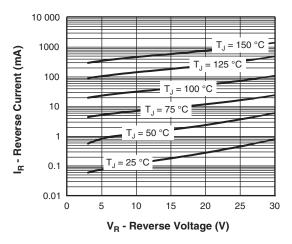


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



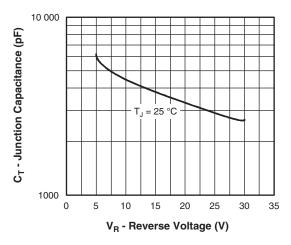


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

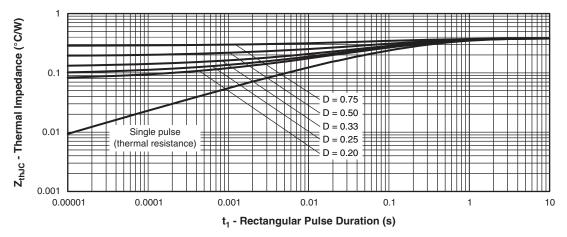


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

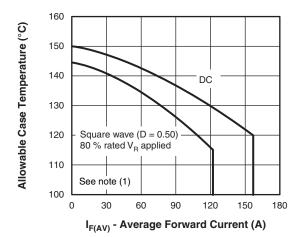


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

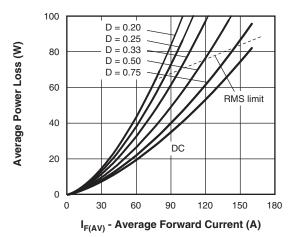
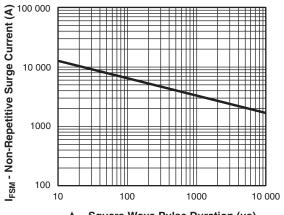


Fig. 6 - Forward Power Loss Characteristics



 t_{p} - Square Wave Pulse Duration ($\mu s)$

Fig. 7 - Maximum Non-Repetitive Surge Current

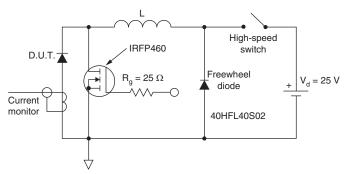


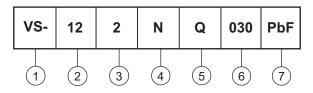
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



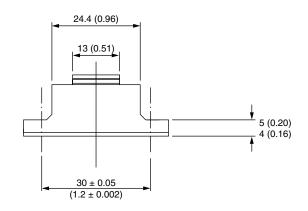
- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- 3 Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (030 = 30 V)
- 7 Lead (Pb)-free

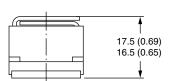
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95020		

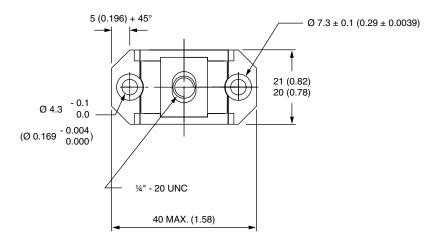


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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