

Single Value Wirebondable Thin Film Chip Resistors



LINKS TO ADDITIONAL RESOURCES



FEATURES

- Small size 20 mil x 20 mil
- Very high ohmic value up to 10 M Ω
- Aluminum terminations
- Good stability 0.1 % (2000 h, rated power at +70 °C)



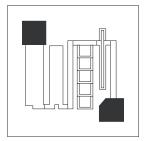


 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Chromium silicon thin film is very well suited to produce high density and high ohmic value resistor chips. These high ohmic value chip resistors are available with improved performances and size when compared to thick film counterparts.

SCHEMATIC AND PATTERN





STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	SIZE	$\begin{array}{c} \textbf{RESISTANCE} \\ \textbf{RANGE} \\ \Omega \end{array}$	RATED POWER P _{70°C} W	LIMITING ELEMENT VOLTAGE V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C		
CS22	0202	10K to 10M	0.05	100 ⁽¹⁾	0.5, 1, 2	50 ⁽²⁾ , 100		

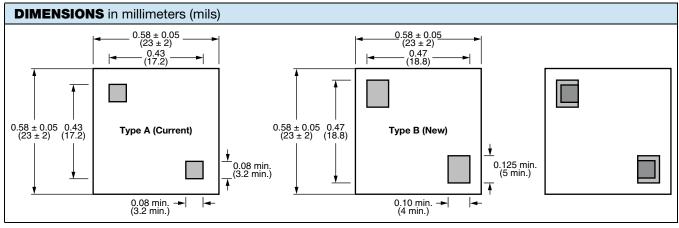
Notes

(1) Higher on Al₂O₃

(2) On request

CLIMATIC SPECIFICATIONS				
Operating temperature range	-55 °C to +155 °C			
Storage temperature range	-55 °C to +155 °C			

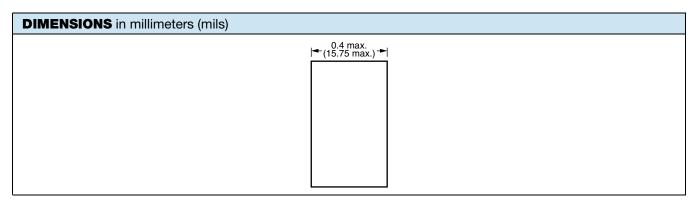
MECHANICAL SPECIFICATIONS					
Resistive element	Chromium silicon				
Passivation	Silicon nitride				
Substrate material	Silicon (consult Vishay for Al ₂ O ₃)				
Bonding pads	Aluminum				



Note

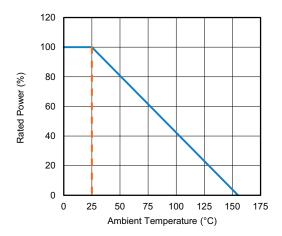
· Customer can get one or the other part, but positions of pads are similar

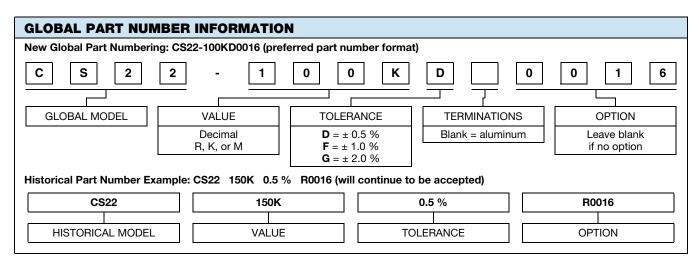




TECHNICAL SPECIFICATIONS						
TEST	SPECIFICATIONS	CONDITIONS				
Stability	± 0.1 % typical, ± 0.2 maximum	2000 h at +70 °C at Pn				
Noise	< -20 dB typical	MIL-STD-202 method 308				
Thermal EMF	< 0.01 μV/°C					
Shelf life stability	200 ppm	1 year at +25 °C				

DERATING







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