



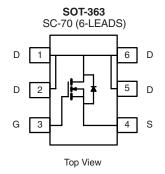
# N-Channel 20 V (D-S) MOSFET

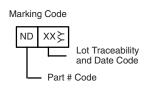
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
- 20	0.150 at V <sub>GS</sub> = 4.5 V	1.7		
	0.235 at V <sub>GS</sub> = 2.5 V	1.3		

### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET: 2.5 V Rated
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si1400DL-T1-E3 (Lead (Pb)-free)

Si1400DL-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unles	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	20		V
tte-Source Voltage		V <sub>GS</sub>	± 12		
Continuous Drain Current /T 150 °C\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	1.7	1.6	А
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		1.2	1.0	
Pulsed Drain Current		I <sub>DM</sub>	5		A
Continuous Source Current (Diode Conduction) $^{\rm a}$		I <sub>S</sub>	0.8	0.8	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.625	0.568	W
	T <sub>A</sub> = 85 °C		0.40	0.295	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	- R <sub>thJA</sub>	165	200	°C/W
Maximum Junction-to-Ambient	Steady State		180	220	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	105	130	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

# Vishay Siliconix



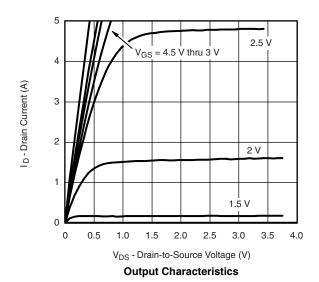
<b>SPECIFICATIONS</b> $T_J = 25$ °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min. Typ		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.6			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA		
Zoro Coto Voltago Droin Current		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			1	μΑ		
Zero Gate Voltage Drain Current	DSS				5			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	2			Α		
Drain-Source On-State Resistance <sup>a</sup>	В	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.7 A		0.123	0.150	0		
Diain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 1.3 A 0.19		0.195	0.235	Ω		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.7 A		5		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 0.8 A, V <sub>GS</sub> = 0 V		0.78	1.1	V		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			2.1	4.0			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 1.7 \text{ A}$		0.3		nC		
Gate-Drain Charge	$Q_{gd}$			0.4		1		
Turn-On Delay Time	t <sub>d(on)</sub>			10	17			
Rise Time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, R_{I} = 20 \Omega$		30	50			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		14	25	ns		
Fall Time	t <sub>f</sub>			8	15			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 0.8 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		30	50			

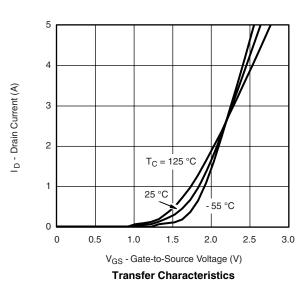
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



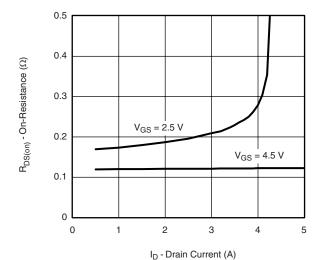




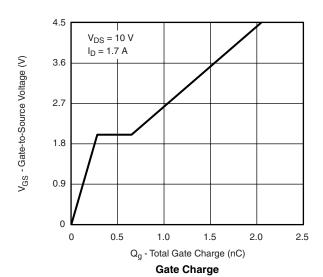




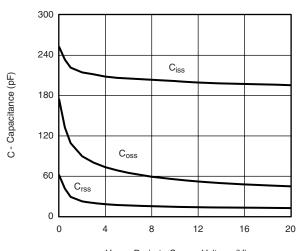
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



**On-Resistance vs. Drain Current** 

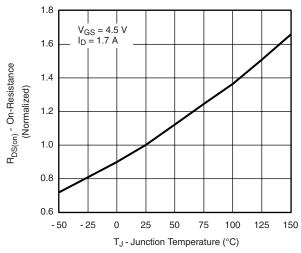


(v) the total of t

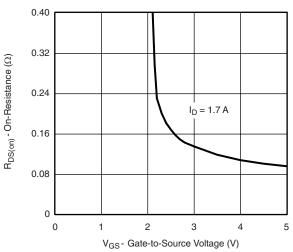


V<sub>DS</sub> - Drain-to-Source Voltage (V)





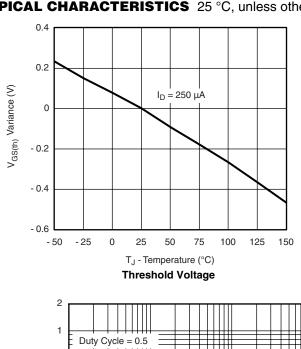
On-Resistance vs. Junction Temperature

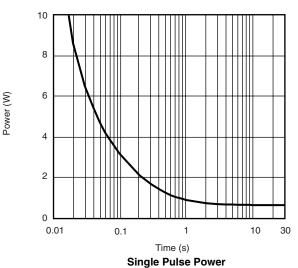


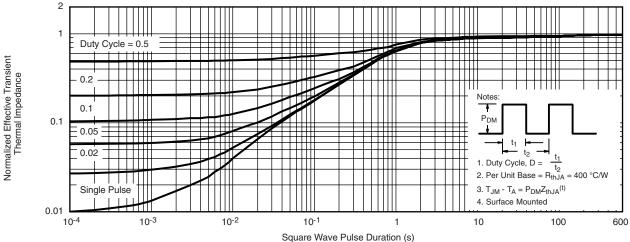
On-Resistance vs. Gate-to-Source Voltage

## Vishay Siliconix

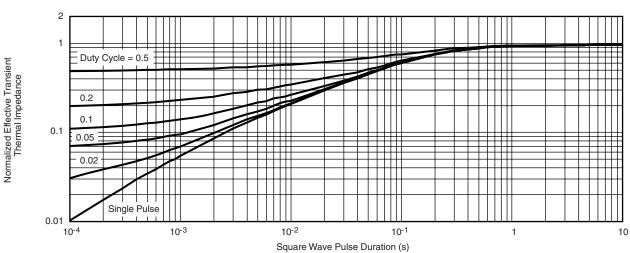
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







## Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71179



Vishay

## **Disclaimer**

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

Document Number: 91000 Revision: 18-Jul-08