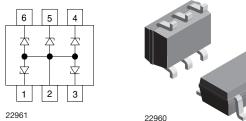


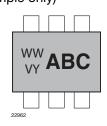
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# Five-Line ESD Protection Diode Array in SOT-363



### **MARKING** (example only)



Bar = cathode marking

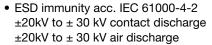
X = date code





#### **FEATURES**

- Compact SOT-363 package
- 5-line unidirectional ESD-protection
- Working range 5V to 26 V



- AEC-Q101 qualified available
- Lead plating: Sn (e3)
  - soldering can be checked by standard vision inspection
  - (AOI = automated optical inspection)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912







RoHS HALOGEN

FREE **GREEN** 

(5-2008)

' = type code (see table below)		
DESIGN SUPPORT TOOLS AVAILABLE		

ORDERING II	ORDERING INFORMATION									
		ENVIRON	MENTAL AND QUALITY	CODE						
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	RoHS COMPLIANT + LEAD (Pb)-FREE TERMINATIONS	EAD (Pb)-FREE 3K PER 7" REEL (8 mm TAPE)		EAD (Pb)-FREE 3K PER 7" REEL (8 mm TAPE)		ORDERING CODE (EXAMPLE)			
		GREEN		MOQ = 15K/BOX						
VESD05A5-06G	-	G	3	-08	VESD05A5-06G-G3-08					
VESD05A5-06G	Н	G	3	-08	VESD05A5-06GHG3-08					

PACKAGE DA	PACKAGE DATA										
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS					
VESD05A5-06G		D05									
VESD12A5-06G	SOT-363	D12	7 00 ma	UL 94 V-0 MSL level 1 Peak to	Peak temperature						
VESD16A5-06G	501-303	D16	7.22 mg	OL 94 V-0	(according J-STD-020)	max. 260°C					
VESD26A5-06G		D26									





ABSOLUTE MAXIMUM RATINGS VESD05A5-06G								
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT				
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	8.7	Α				
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W				
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	\/	30	kV				
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	30	kV				
Operating temperature	Junction temperature	T <sub>J</sub>	-55 to +150	°C				
Storage temperature		T <sub>stg</sub>	-55 to +150	°C				

ABSOLUTE MAXIMUM	ABSOLUTE MAXIMUM RATINGS VESD12A5-06G								
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT					
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I <sub>PPM</sub>	4.4	Α					
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P <sub>PP</sub>	100	W					
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	30	kV					
L3D infiniting	Air discharge acc. IEC 61000-4-2; 10 pulses	V ESD	30	kV					
Operating temperature	Junction temperature	T <sub>J</sub>	-55 to +150	°C					
Storage temperature		T <sub>stg</sub>	-55 to +150	°C					

ABSOLUTE MAXIMUM	ABSOLUTE MAXIMUM RATINGS VESD16A5-06G								
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT					
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I <sub>PPM</sub>	3.6	Α					
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	$P_PP$	100	W					
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	30	kV					
E3D IIIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	30	kV					
Operating temperature	Junction temperature	$T_J$	-55 to +150	°C					
Storage temperature		T <sub>stg</sub>	-55 to +150	°C					

ABSOLUTE MAXIMUM RATINGS VESD26A5-06G								
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT				
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	2.1	Α				
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W				
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	W	20	kV				
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	20	kV				
Operating temperature	Junction temperature	TJ	-55 to +150	°C				
Storage temperature		T <sub>stg</sub>	-55 to +150	°C				



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ELECTRICAL CHARAC (T <sub>amb</sub> = 25 °C, unless oth	CTERISTICS VESD05A5-06G nerwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	5	V
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	5	-	-	V
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	0.01	0.1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6.85	7.26	7.65	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 8.7 \text{ A}$ , $t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	10.3	11.5	V
Forward clamping voltage	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	$V_{F}$	0.9	1.1	1.2	V
Torward clamping voltage	at $I_{PP} = I_{PPM} = 8.7 \text{ A}$ , $t_p = 8/20 \mu\text{s}$	$V_{F}$	-	2.2	2.74	V
Dynamic resistance	$t_p = 100 \text{ ns (TLP; pin 2-1)}$	r <sub>dyn</sub>	-	0.2	-	Ω
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	53	67	81	pF

ELECTRICAL CHARAC (T <sub>amb</sub> = 25 °C, unless oth	CTERISTICS VESD12A5-06G perwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	12	V
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	12	-	-	V
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	-	0.01	0.1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	13.9	14.7	15.5	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 4.4 \text{ A}$ , $t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	20.5	22.7	V
Converd elemning veltage	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	$V_{F}$	0.9	1.1	1.2	V
Forward clamping voltage	at $I_{PP} = I_{PPM} = 4.4 \text{ A}, t_p = 8/20 \mu \text{s}$	V <sub>F</sub>	-	1.6	1.88	V
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP); pin 2-1	r <sub>dyn</sub>	-	0.4	-	Ω
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	26	33	40	pF

ELECTRICAL CHARAC (T <sub>amb</sub> = 25 °C, unless oth	CTERISTICS VESD16A5-06G nerwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	16	V
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	16	-	-	V
Reverse current	at V <sub>R</sub> = 16 V	I <sub>R</sub>	-	0.01	0.1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	17	17.9	18.8	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 3.6 \text{ A}, t_p = 8/20 \mu\text{s}$	V <sub>C</sub>	-	25.3	28	V
Commend alamanian maltana	at I <sub>PP</sub> = 1 A, t <sub>p</sub> = 300 μs	V <sub>F</sub>	0.9	1.1	1.2	V
Forward clamping voltage	at $I_{PP} = I_{PPM} = 3.6 \text{ A}, t_p = 8/20 \mu \text{s}$	V <sub>F</sub>	-	1.5	1.72	V
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP); pin 2-1	r <sub>dyn</sub>	-	0.53	-	Ω
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	$C_D$	21	27	33	pF



ELECTRICAL CHARACTERISTICS VESD26A5-06G (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	26	V		
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	26	-	-	V		
Reverse current	at V <sub>R</sub> = 26 V	I <sub>R</sub>	-	< 0.01	0.1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	27.6	29.1	30.6	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 2.1 A, t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	43	48	V		
Converd elemping veltage	at $I_{PP} = 1 \text{ A}$ , $t_p = 300 \mu\text{s}$	$V_{F}$	0.9	1.1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 2.1 A, t <sub>p</sub> = 8/20 μs	V <sub>F</sub>	-	1.3	1.42	V		
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP); pin 2-1	r <sub>dyn</sub>	-	1.9	-	Ω		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	$C_D$	14	17.5	21	pF		

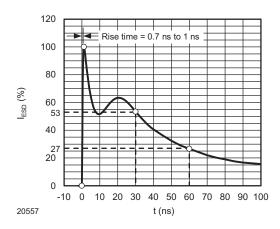


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$  / 150 pF)

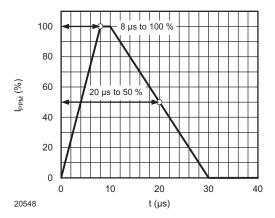


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

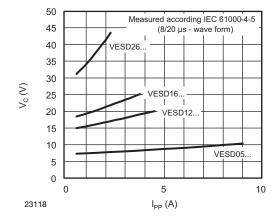


Fig. 3 - Typical Peak Clamping Voltage vs. Peak Pulse Current

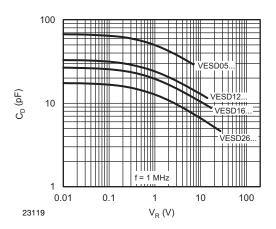


Fig. 4 - Typical Capacitance vs. Reverse Voltage

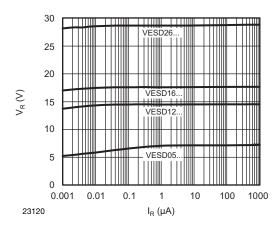


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

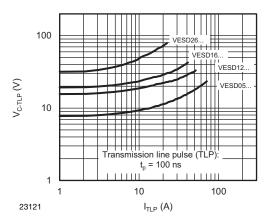


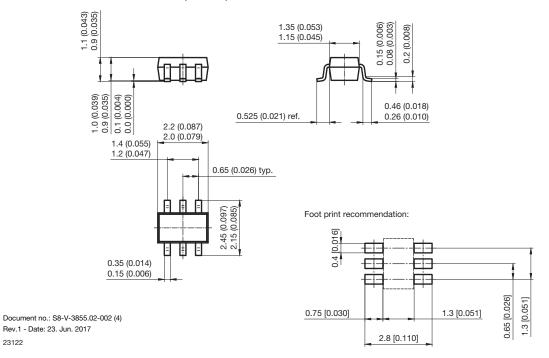
Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current



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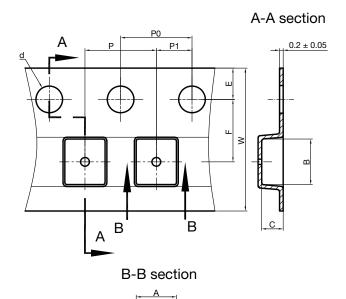
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#### PACKAGE DIMENSIONS in millimeters (Inches): SOT-363



#### **CARRIER TAPE SOT-363**

23122



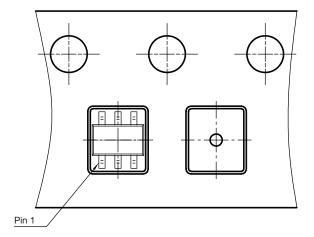
Cummulative tolerances of 10 sprocket holes is  $\pm$  0.2 mm

Dimensions in millimeters										
Packaging type	Α	В	С	d	Е	F	P0	Р	P1	W
SOT-363 2.25 2.55 1.20 Ø 1.5 1.75 3.50 4.00 4.00 2.00 8.00										
(Tolerance)	± 0.1	± 0.1	± 0.1	+0.1/-0	± 0.1	± 0.1	± 0.05	± 0.1	± 0.05	+0.3/-0.1

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#### **ORIENTATION IN CARRIER TAPE SOT-363**



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