



20V N-Channel Enhancement Mode MOSFET - ESD Protected

Voltage 20 V Current 0.7A

Features

- RDS(ON), VGS@4,5V, ID@0.7A<150mΩ
- RDS(ON) , VGS@2.5V, ID@0.5A<220mΩ
- RDS(ON), VGS@1.8V, ID@0.2A<400mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

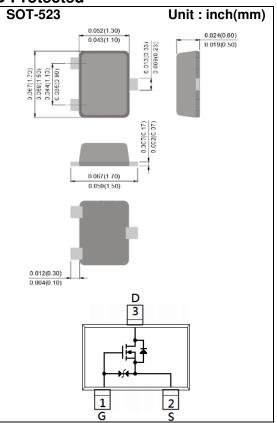
Mechanical Data

• Case: SOT-523 Package

Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.002 grams

Marking : E02



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		ID	0.7	Α
Pulsed Drain Current		I _{DM}	2.8	Α
Power Dissipation	T _a =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient ^(Note 3)		ReJA	417	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.5	0.78	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =0.7A	-	129	150	mΩ
		V _{GS} =2.5V, I _D =0.5A	-	167	220	
		V _{GS} =1.8V, I _D =0.2A	-	260	400	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	Igss	$V_{GS}=\underline{+}8V, V_{DS}=0V$	-	<u>+</u> 2	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Q_g	V 40V I 0.7A	-	1.6	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} =10V, I _D =0.7A, V _{GS} =4.5V ^(Note 1,2)	-	0.3	-	
Gate-Drain Charge	Q_{gd}		-	0.4	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	92	-	pF
Output Capacitance	Coss		-	25	-	
Reverse Transfer Capacitance	Crss	I=I.UIVIMZ	-	9	-	
Switching						
Turn-On Delay Time	td _(on)	\/ 10\/ 0.7A	-	6	-	
Turn-On Rise Time	tr	V_{DD} =10V, I_{D} =0.7A, V_{GS} =4.5V, R_{G} =6 $\Omega^{(Note 1,2)}$	-	26	-	ns
Turn-Off Delay Time	td _(off)		-	41	-	
Turn-Off Fall Time	tf	MG=012(Note 1,2)	-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	0.4	Α
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V	-	0.89	1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

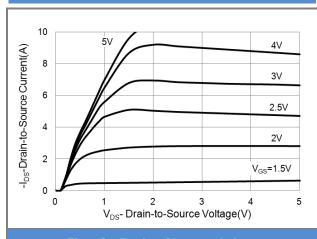


Fig.1 On-Region Characteristics

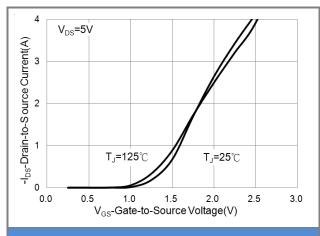


Fig.2 Transfer Characteristics

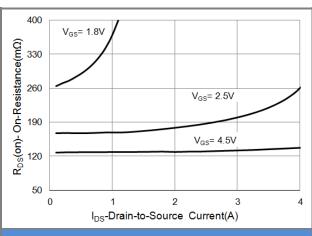


Fig.3 On-Resistance vs. Drain Current

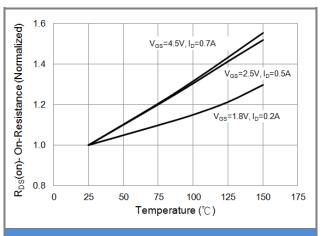
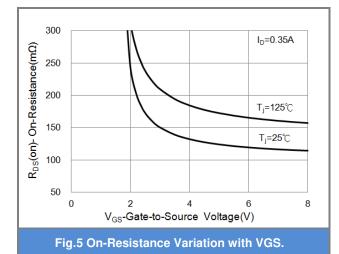
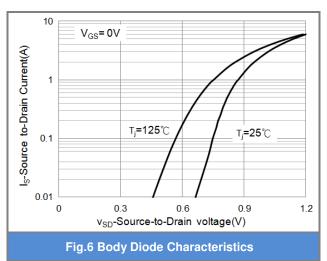


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

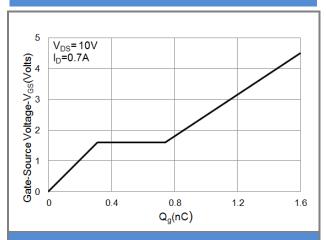


Fig.7 Gate-Charge Characteristics

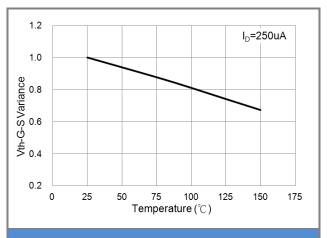


Fig.8 Threshold Voltage Variation with Temperature.

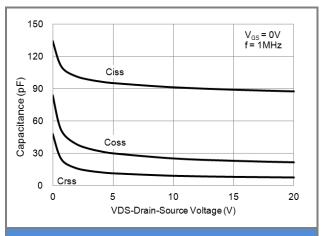


Fig.9 Capacitance vs. Drain-Source Voltage.

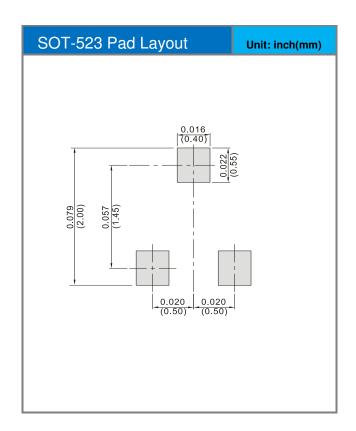




PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE8402_R1_00001	SOT-523	4K pcs / 7" reel	E02	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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