



Product Summary

Device	BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
N-	۹- 30V	0.4Ω @ V _{GS} = 10V	0.8A
Channel	30 v	$0.7\Omega @ V_{GS} = 4.5V$	0.57A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- Power Management Functions
- DC-DC Converters

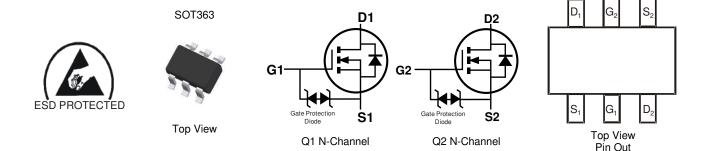
DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- The DMN3401LDWQ is suitable for automotive applications requiring specific change control and is AEC-Q101 qualified, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.027 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3401LDWQ-7	SOT363	3000/Tape & Reel
DMN3401LDWQ-13	SOT363	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Date Code Key			 401 ₩ <u>⋏</u> 	MY 401	7 7	M = Date C = Year (ex	ct Type Ma Code Markir :: H = 2020) ex: 9 = Sep	ng)	2			
Year	2020		2021 2022 2023 2024 2025 2026 2027					2027				
Code	Н			J		K L M N O				0		
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 6) $V_{GS} = 10V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			ID	0.8 0.6	A
Maximum Continuous Body Diode Forward Current (Note	Is	0.4	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	4	А		

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.29	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	433	°C/W
Total Power Dissipation (Note 6)		PD	0.35	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	360	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}		—	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		—	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.8	—	1.6	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	D		0.2	0.4	Ω	$V_{GS} = 10V, I_D = 0.59A$
	R _{DS(ON)}		0.3	0.7	Ω	$V_{GS} = 4.5V, I_D = 0.2A$
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 10mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		50	_	pF	
Output Capacitance	Coss		12		pF	−V _{DS} = 15V, V _{GS} = 0V, −f = 1.0MHz
Reverse Transfer Capacitance	Crss		10		pF	
Gate Resistance	Rg		58	-	Ω	$V_{DS} = V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg		0.5		nC	
Total Gate Charge (V _{GS} = 10V)	Qg		1.2	_	nC	$V_{DS} = 10V, V_{GS} = 10V$
Gate-Source Charge	Q _{gs}		0.2		nC	$I_D = 250 \text{mA}$
Gate-Drain Charge	Q _{gd}		0.1	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	3.5	_	ns	
Turn-On Rise Time	t _R		3.3		ns	$V_{GS} = 10V, V_{DS} = 30V,$
Turn-Off Delay Time	t _{D(OFF)}		16.8	_	ns	$I_D = 100 mA, R_G = 25 \Omega$
Turn-Off Fall Time	t _F	_	13.8	_	ns	

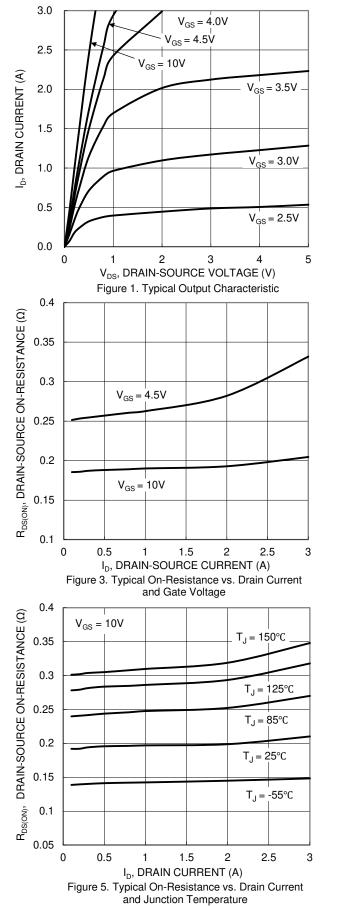
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

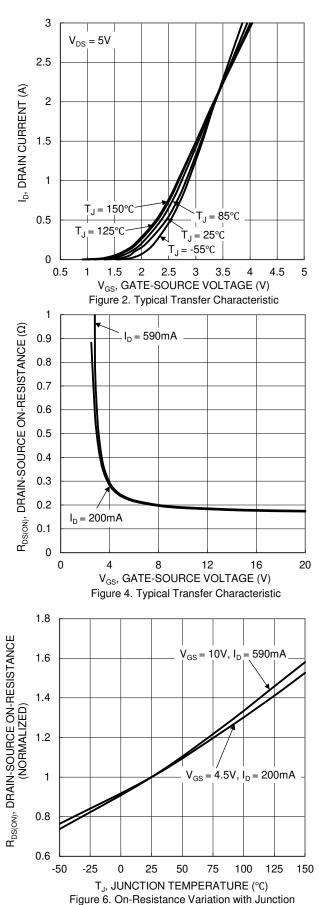
Device mounted on FR-4 substrate PC board, 22 copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



DMN3401LDWQ

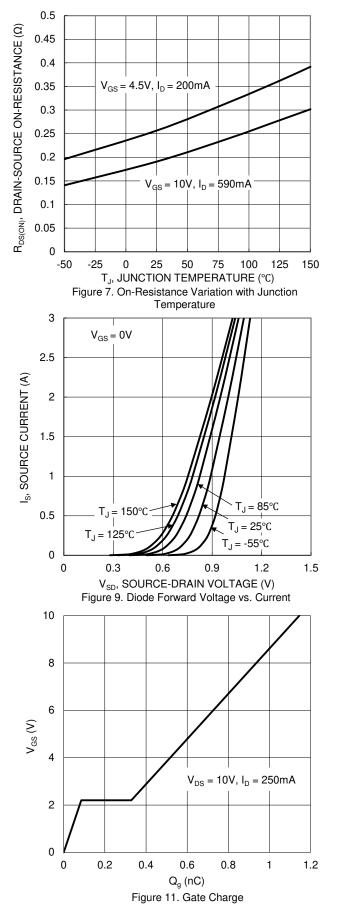


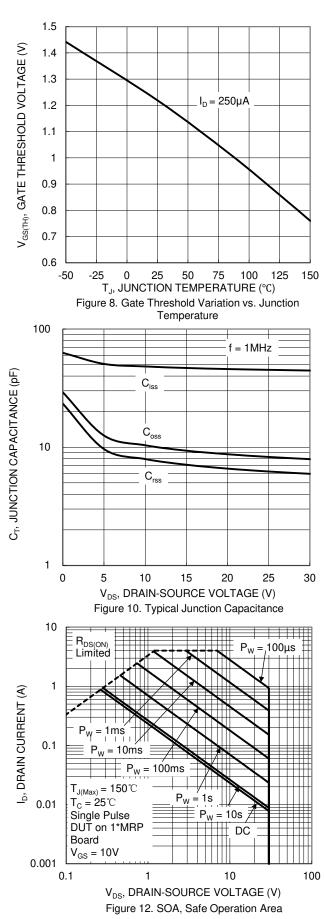


Temperature

DMN3401LDWQ Document number: DS41997 Rev. 2 - 2

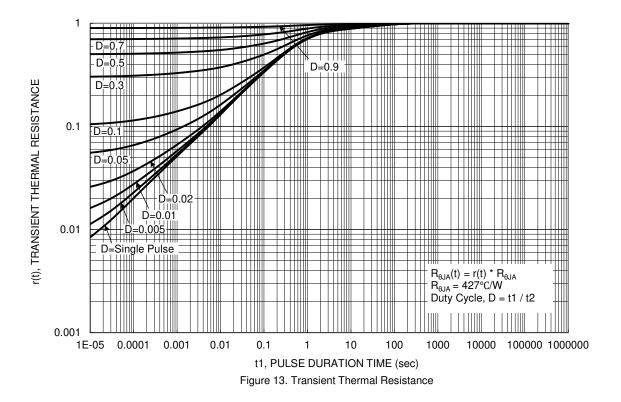






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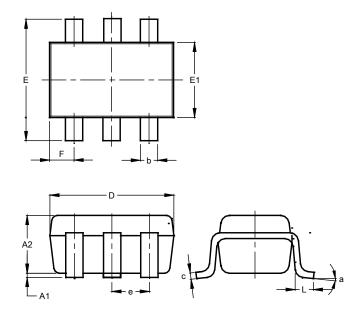






Package Outline Dimensions

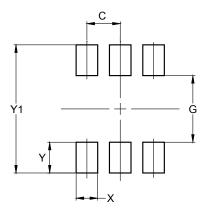
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT363								
Dim	Min	Min Max Typ						
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.10	0.30	0.25					
С	0.10	0.22	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C	.650 E	SC					
F	0.40	0.45	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)			
С	0.650			
G	1.300			
Х	0.420			
Y	0.600			
Y1	2.500			



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