High Voltage 3-Phase Motor Driver IC SLA6845MZ



General Description

The SLA6845MZ provides a highly-integrated solution by incorporating key components into one package - IGBTs in a 3-phase full-bridge configuration, built-in protection functions such as UVLO (undervoltage lockout) and TD (thermal detection) circuits, and pre-driver ICs with 7.5 V regulator output. The SLA6845MZ employs three LS terminals to configure a 3-shunt current detection system. The product is supplied in a SIP package with Al heatsink.

Applications

Include motor control for:

- Air conditioner fan
- Air purifier fan
- · Washer-dryer fan
- Dishwasher pump

Features and Benefits

- CMOS-compatible input (3.3 or 5 V)
- Built-in protection circuit for controlling power supply voltage drop (UVLO)
- Built-in overheat detection circuit (TD)
- Regulator output: 7.5 V, 35 mA
- Small SIP (SLA, 24 pins)
- 3-shunt current detection

Product Specifications

	Aller.
cot	Leadform 2175

SLA

1.27 mm

 $31 \times 16 \times 4.8$ mm

adform 2171

Not to scale

uilt egu nal shu	age drop (UVLO) -in overheat detection co lator output: 7.5 V, 35 n l SIP (SLA, 24 pins) int current detection	nA	ndedfo	Leadform	Not
	Part Number	IGBT Breakdown Voltage, V _{CES} (V)	Output Current (Continuous), I _O (A)	IGBT Saturation Voltage, V _{CE(sat)} (V Typ.)	Package
	SLA6845MZ	600	3.0	1.75	Al heatsink

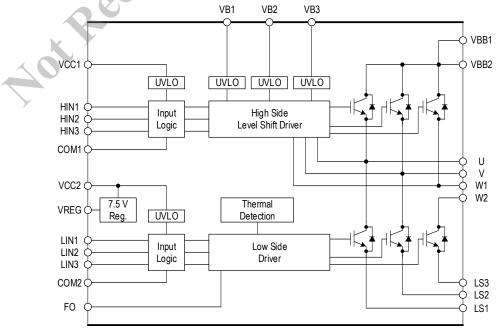
Package

• Pin Pitch:

• Package Name:

• External Size:

Functional Block Diagram





1. Scope

The specifications described in this document shall apply to the SLA6845MZ, a high-voltage 3-phase motor driver IC.

2. Absolute Maximum Ratings, valid at $T_A = 25^{\circ}C$

Characteristics	Symbol	Remarks	Ratings	Unit
IGBT Breakdown Voltage	V _{CES}	$V_{CC} = 15 \text{ V}, I_C = 1 \text{ mA}, V_{IN} = 0 \text{ V}$	600	V
Logic Supply Voltage	V _{CC}	Between VCC and COM	20	v
Bootstrap Voltage	V _{BS}	Between VB and phase U, V, or W	20	V
Output Current (Continuous)	Io	$T_{\rm C} = 25^{\circ}{\rm C}$	3.0	А
Output Current (Pulsed)	I _{OP}	$T_{\rm C} = 25^{\circ}{\rm C}, {\rm P}_{\rm W} \le 100 \mu{\rm s}$	4.5	А
Output Current for Regulator	I _{REG}	10	35	mA
Input Voltage	V _{IN}	HIN and LIN pins	-0.5 to 7	V
Allowable Power Dissipation	P _D	$T_{C} = 25^{\circ}C$	32.8	W
Thermal Resistance	R _{(j-c),Q}	All elements operating (IGBT)	3.8	°C/W
(Junction-to-Case)	R _{(j=c) F}	All elements operating (FWD)	4.2	°C/W
Thermal Resistance (Junction-to-Ambient)	R _{j-a}	All elements operating (IGBT and FWD)	25	°C/W
Case Operating Temperature	T _{C(OP)}		-20 to 100	°C
Junction Temperature	T_j		150	°C
Storage Temperature	T _{stg}		-40 to 150	°C

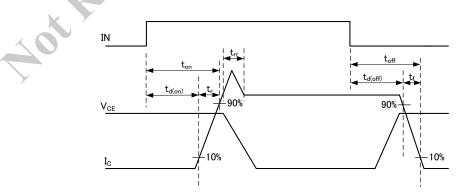


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3. Electrical Characteristics

3-1. Electrical Characteristics, valid at $T_a = 25^{\circ}C$, $V_{cc} = 15 V$

Characteristics		ol Remarks		Ratings			
Characteristics Symbol		Remarks	Min.	Тур.	Max.	Unit	
Logic Supply Current	I _{CC}	$I_{REG} = 0 A$		4	6	mA	
Bootstrap Supply Current	I _{BS}	$V_{BS} = 15 \text{ V}, \text{HIN} = 5 \text{ V}$	_	135	380	μΑ	
	V _{IH}	Output ON	_	2.0	2.5	V	
Input Voltage	V _{IL}	Output OFF	1.0	1.5		V	
	V _{HYS}	Hysteresis		0.5	4	V	
Input Current	I _{IH}	$V_{IN} = 5 V$	_	50	100	μΑ	
	V _{UVHL}	Between VB and U, V, or W	9.0	10.0	11.0	V	
Undervoltage Lockout (Bootstrap)	V _{UVHH}	Between VB and U, V, or W	9.5	10.5	11.5	V	
(Dootstrap)	V _{UVhys}	Between VB and U, V, or W; hysteresis	Í	0.5		V	
	V _{UVLL}	Between VCC and COM	10.0	11.0	12.0	V	
Undervoltage Lockout (Logic Supply)	V _{UVLH}	Between VCC and COM	10.5	11.5	12.5	V	
(Logie Supply)	V _{UVhys}	Between VCC and COM; hysteresis	_	0.5		V	
FO Terminal Output Voltage	V _{FOL}	$V_{CC} = 15 \text{ V}, I_{FO} = -1 \text{ mA}$	0		1.0	V	
10 Terminal Output Voltage	$V_{\rm FOH}$	$V_{\rm CC} = 15$ V, $I_{\rm FO} = 1.6$ mA	4.0	—	5.5	V	
IGBT Leakage Current	I _{CES}	$V_{CE} = 600 \text{ V}, V_{IN} = 0 \text{ V}, V_{CC} = 15 \text{ V}$	_	—	1	mA	
IGBT Saturation Voltage	V _{CE(sat)}	$V_{CC} = 15 \text{ V}, I_{CE} = 3 \text{ A}, V_{IN} = 5 \text{ V}$		1.75	2.1	V	
Diode Forward Voltage	V _F	$V_{CC} = 15 \text{ V}, I_F = 3 \text{ A}, V_{IN} = 0 \text{ V}$	_	1.65	2.0	V	
	t _{d(on)}			315			
	t _r	$V_{BB} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_C = 3 \text{ A},$	_	50			
Switching Time, High Side	t _{rr}	$HIN = 0 \rightarrow 5 V \text{ or } 5 \rightarrow 0 V,$		80		ns	
	$t_{d(off)}$	inductive load	_	375			
	t _f		_	165			
	t _{d(on)}			395			
	t _r	$V_{BB} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_C = 3 \text{ A},$		60			
Switching Time, Low Side	t _{rr}	$LIN = 0 \rightarrow 5 V \text{ or } 5 \rightarrow 0 V,$		75		ns	
	t _{d(off)}	inductive load		395			
	t _f			170			



Switching Characteristics Definitions



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3-2. Recommended Operating Conditions

Characteristics	Symbol		Remarks			Ratings			Unit
Characteristics	Symbol		Remarks			Min.	Тур.	Max.	Unit
Main Supply Voltage	V _{DC}	Bety	ween VB	B and LS			300	450	V
Logic Supply Voltage	V _{CC}	Bety	ween VC	C and COM		13.5		16.5	V
Dead Time	t _{dead}					1.5			μs
Minimum Input Pulse Width	t _{INmin(on)}					0.5			μs
Winning input Pulse widen	t _{INmin(off)}					0.5		ł	μs
3-3. Tru <u>th Table</u>							51	02	
Mode		HIN	I IN	High-Side	Low-	Side	0.		

3-3. Truth Table

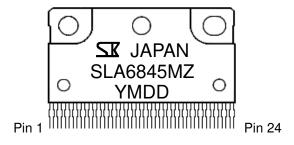
Mode	HIN	LIN	High-Side IGBT	Low-Side IGBT
	L	L	OFF	OFF
Nr 11)	Н	L	ON	OFF
Normal ¹⁾	L	Н	OFF	ON
	Н	Н	ON	ON
	L	L	OFF	OFF
OCD.	Н	L	ON	OFF
OCP	L	Н	OFF	OFF
	Н	Н	ON	OFF
	L	L	OFF	OFF
UVLO (VCC) ²⁾	Н	L	OFF	OFF
UVLO (VCC)	L	Н	OFF	OFF
	Н	Н	OFF	OFF
	Ĺ	L	OFF	OFF
$IIII O (III)^{3}$	Н	L	OFF	OFF
UVLO (VB) ³⁾	L	Н	OFF	ON
	Н	Н	OFF	ON

¹⁾ An arm short-circuit may occur when inputs on the HIN and LIN pins for the same phase are all logic high. Therefore, extra attention should be paid to prevent a condition in which the pins for the same phase are fully ON at once.

²⁾ When returning to the Normal operation mode from a V_{CC} UVLO state, high-side and low-side IGBTs resume switching on the rising edge of an HIN input (positive edge triggering).

3) When returning to the Normal operation mode from a V_B UVLO state, a high-side IGBT resumes switching on the rising edge of an HIN input (positive edge triggering).

4. Pin-out Diagram



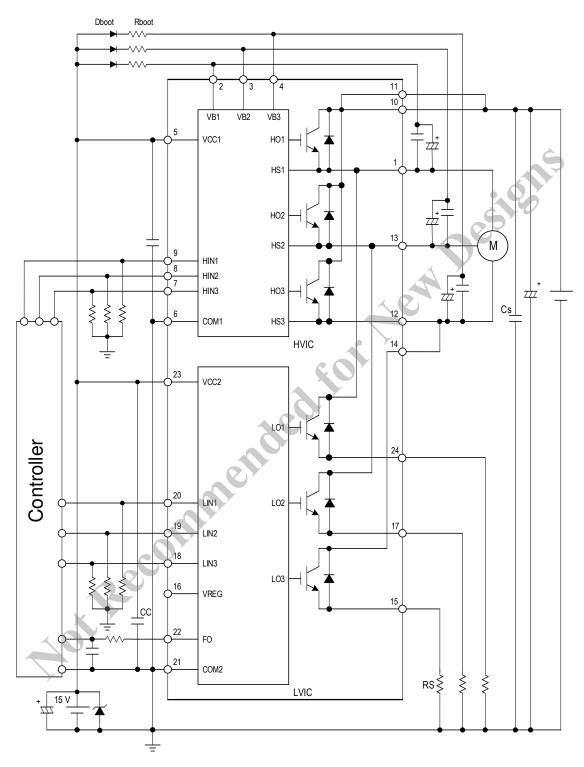
Pin Number	Pin Name	Functions	I/O
1	U	Phase U output	Output
2	VB1	High-side bootstrap (phase U)	
3	VB2	High-side bootstrap (phase V)	
4	VB3	High-side bootstrap (phase W)	
5	VCC1	High-side logic supply voltage	
6	COM1	High-side logic GND	
7	HIN3	High-side input (phase W)	Input
8	HIN2	High-side input (phase V)	Input
9	HIN1	High-side input (phase U)	Input
10	VBB1	Main supply voltage 1 (connected to VBB2 externally)	—
11	VBB2	Main supply voltage 2 (connected to VBB1 externally)	_
12	W1	Phase W output (connected to W2 externally)	Output
13	V	Phase V output	Output
14	W2	Phase W output (connected to W1 externally)	Output
15	LS3	Low-side emitter (phase W)	
16	VREG	Internal regulator output	Output
17	LS2	Low-side emitter (phase V)	
18	LIN3	Low-side input (phase W)	Input
19	LIN2	Low-side input (phase V)	Input
20	LIN1	Low-side input (phase U)	Input
21	COM2	Low-side logic GND	
22	FO	Overheat detection and UVLO protection fault-signal output	Output
23	VCC2	Low-side logic supply voltage	
24	LS1	Low-side emitter (phase U)	



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SLA6845MZ

5. Application Example



NOTES:

- All of the input pins are connected to GND with internal pull-down resistors rated at 100 k Ω . However, an external pull-down resistor may be required to secure stable condition of the inputs if high impedance conditions are applied to them.
- The external electrolytic capacitors should be placed as close to the IC as possible, in order to avoid malfunctions from external noise interference. Put a ceramic capacitor in parallel with the electrolytic capacitor if further reduction of noise susceptibility is necessary.



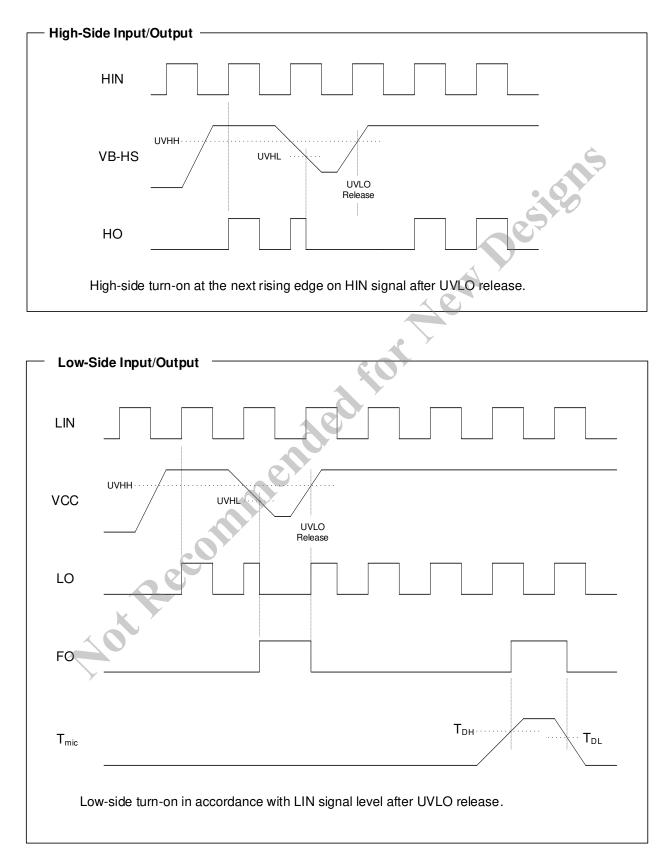
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6. Timing Diagrams for Protection Operations

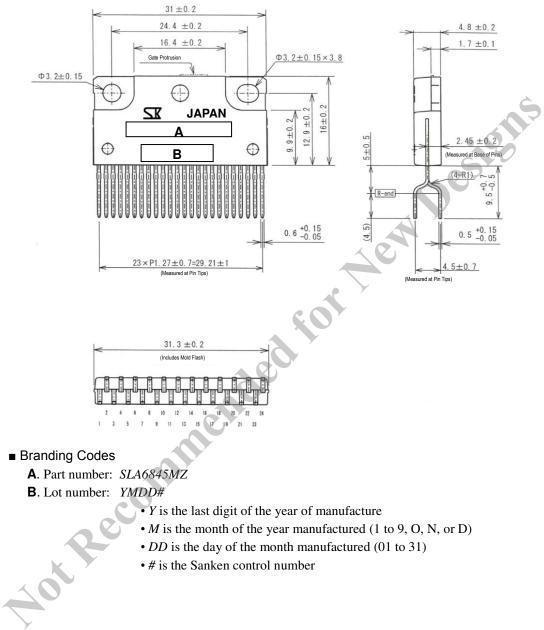


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7. Package Outline Drawing

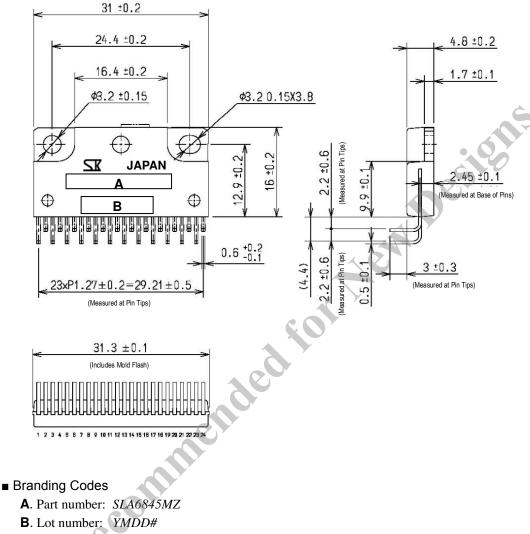
7-1. Leadform 2171 (Dimensions in Millimeters)



- *DD* is the day of the month manufactured (01 to 31)
- # is the Sanken control number



7-2. Leadform 2175 (Dimensions in Millimeters)



- *Y* is the last digit of the year of manufacture
- *M* is the month of the year manufactured (1 to 9, O, N, or D)
- *DD* is the day of the month manufactured (01 to 31)
- # is the Sanken control number

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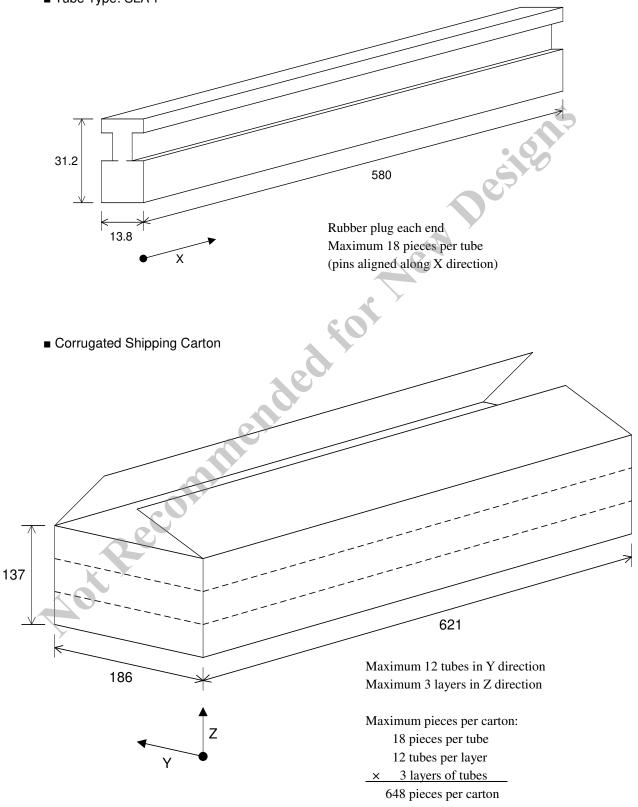
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8. Packing Specifications



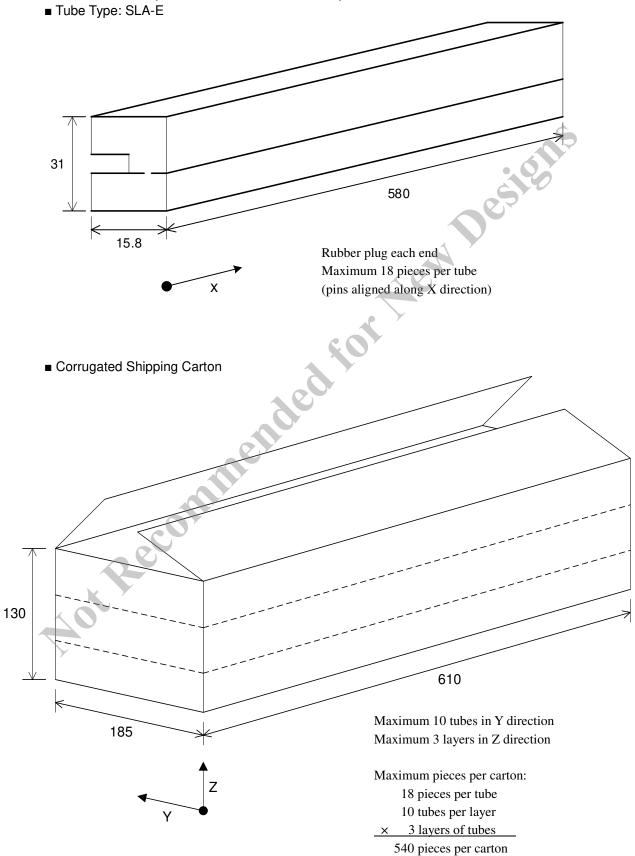
■ Tube Type: SLA-F





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8-2. Leadform 2175 (Dimensions in Millimeters)



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