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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET



MOS FIELD EFFECT TRANSISTOR μ PA1912

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μ PA1912 is a switching device which can be driven directly by a 2.5-V power source.

The μ PA1912 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- Can be driven by a 2.5-V power source
- Low on-state resistance $R_{DS(on)1} = 50 \text{ m}\Omega \text{ MAX.}$ (Vgs = -4.5 V, ID = -2.5 A) $R_{DS(on)2} = 52 \text{ m}\Omega \text{ MAX.}$ (Vgs = -4.0 V, ID = -2.5 A) $R_{DS(on)3} = 70 \text{ m}\Omega \text{ MAX.}$ (Vgs = -2.5 V, ID = -2.5 A)

ORDERING INFORMATION

PART NUMBER	PACKAGE	
μΡΑ1912TE	SC-95 (Mini Mold Thin Type)	

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage	Vdss	-12	V
Gate to Source Voltage	Vgss	±10	V
Drain Current (DC)	D(DC)	±4.5	Α
Drain Current (pulse) ^{Note1}	D(pulse)	±18	Α
Total Power Dissipation	P T1	0.2	W
Total Power Dissipation Note2	P T2	2	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1 %

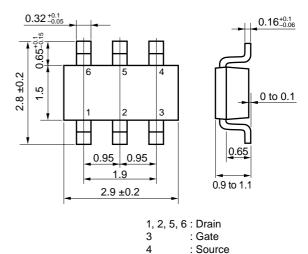
- **2.** Mounted on FR-4 board, $t \le 5$ sec.
- **Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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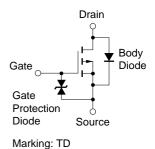
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The mark **★** shows major revised points.

PACKAGE DRAWING (Unit : mm)



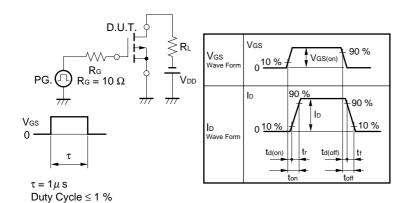
EQUIVALENT CIRCUIT



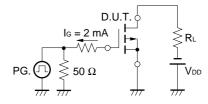
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -12 V, V_{GS} = 0 V$			-10	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 10 \text{ V}, \text{ Vds} = 0 \text{ V}$			±10	μA
Gate to Source Cut-off Voltage	V _{GS(off)}	$V_{DS} = -10 V$, $I_{D} = -1 mA$	-0.5	-0.90	-1.5	V
Forward Transfer Admittance	y _{fs}	$V_{DS} = -10 V$, $I_D = -2.5 A$	3	9.3		S
Drain to Source On-state Resistance	RDS(on)1	$V_{GS} = -4.5 \text{ V}, \text{ Id} = -2.5 \text{ A}$		39	50	mΩ
	RDS(on)2	$V_{GS} = -4.0 \text{ V}, \text{ Id} = -2.5 \text{ A}$		40	52	mΩ
	RDS(on)3	Vgs = -2.5 V, Id = -2.5 A		53	70	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V		810		pF
Output Capacitance	Coss	Vgs = 0 V		241		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		122		pF
Turn-on Delay Time	td(on)	$V_{DD} = -6 V$		304		ns
Rise Time	tr	I□ = −2.5 A		532		ns
Turn-off Delay Time	td(off)	VGS(on) = -4.0 V		406		ns
Fall Time	tr	Rg = 10 Ω		796		ns
Total Gate Charge	QG	$V_{DD} = -10 V$		5.6		nC
Gate to Source Charge	QGS	ID = -4.5 A		2.2		nC
Gate to Drain Charge	Qgd	Vgs = -4.0 V		2.6		nC
Diode Forward Voltage	VF(S-D)	IF = 4.5 A, VGS = 0 V		0.86		V
Reverse Recovery Time	trr	IF = 4.5 A, VGS = 0 V		1.1		μs
Reverse Recovery Charge	Qrr	di/dt = 10 A/ μ s		4.3		μC

TEST CIRCUIT 1 SWITCHING TIME

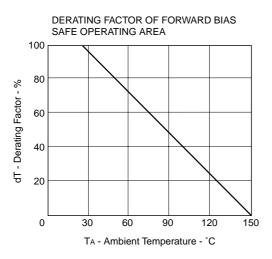


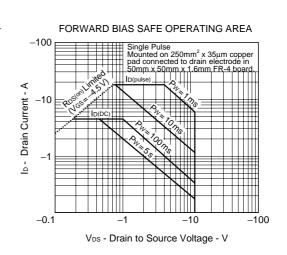
TEST CIRCUIT 2 GATE CHARGE



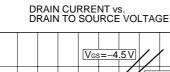
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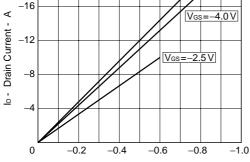




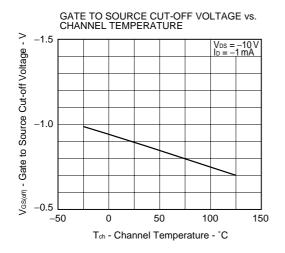


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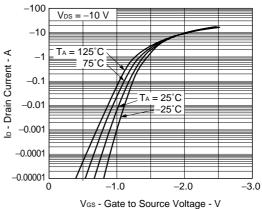




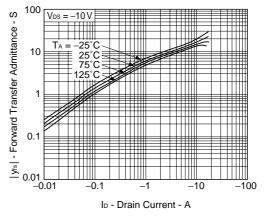
VDs - Drain to Source Voltage - V

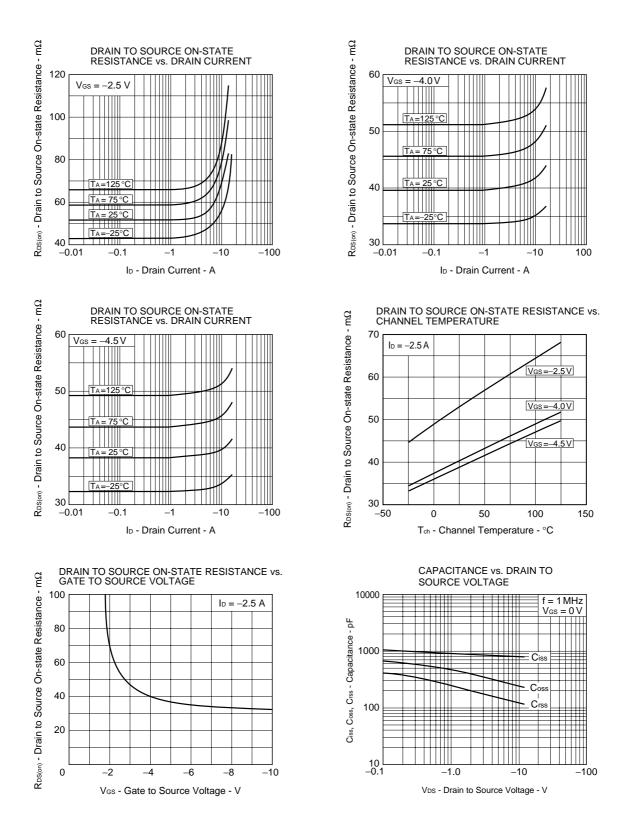




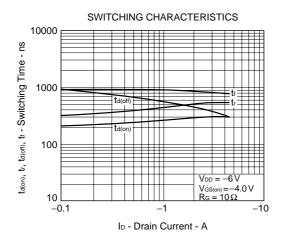


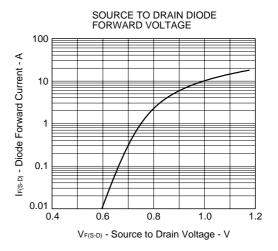
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

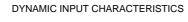


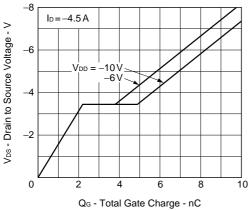


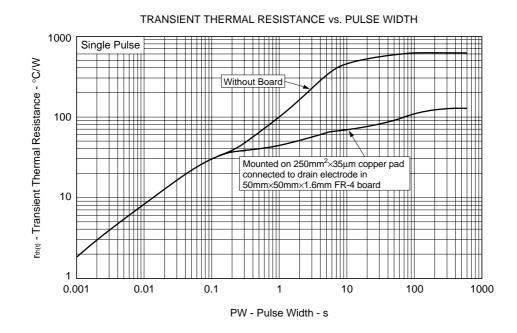
Data Sheet D13806EJ3V0DS











Data Sheet D13806EJ3V0DS

[MEMO]

[MEMO]

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