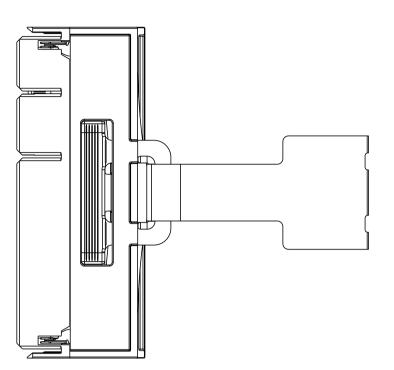


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A48       GH2       SA3       A46         A48       9       PCI=7 X       DCIFF FA12       FCI=7 X       9       A43         A48       9       PCI=7 X       DCIFF FA12       FCI=7 X       9       A43         A49       6H2       SA2       A48       9       PCI=7 X       DCIFF FA12       FCI=7 X       9       A43         A40       6H2       SA2       A48       9       PCI=7 X       DCIFF FA12       FCI=7 X       12       A55         A50       12       PCI=7 X       DCIFF FA12       FCI=7 X       12       A55         A52       13       PCI=7 X       DCIFF FA12       FCI=1 X       17       A54         A53       13       PCI=7 X       DCIFF FA12       FCI=1 X       17       A56         A53       14       PCI=7 X       DCIFF FA12       FCI=1 X       13       A55         A54       13       PCI=7 X       DCIFF FA12       FCI=1 X       14       A54         A54       13       PCI=7 X       DCIFF FA12       FCI=1 X       13       A55         A54       13       PCI=7 X       DCIFF FA12       FCI=1 X       14       A54         <												
A     447     9     PC[PTX     D)FF PA13     PCTP V     9     A47       A48     9     PCTP PA13     PCTP VX     9     A46       A48     9     PCTP PA13     PCTP VX     9     A46       A48     9     PCTP VX     9     A46       A49     6.52     010     11     A11     PCTP VX     9     A46       A49     10     PCTP VX     11     A11     PCTP VX     12     A50       A49     10     PCTP VX     DTF PA13     PCTP VX     11     A51       A49     10     PCTP VX     DTF PA13     PCTP VX     11     A51       A40     0     PCTP VX     DTF PA13     PCTP VX     11     A51       A40     10     PCTP VX     DTF PA13     PCTP VX     11     A54       A50     017     PCTP VX     DTF PA13     PCTP VX     12     A56       A52     017     PCTP VX     DTF PA13     PCTP VX     12     A56       A52     017     PCTP VX     DTF PA13     PCTP VX     13     A54       A55     017     PCTP VX     DTF PA13     PCTP VX     13     A56       A54     017     PCTP VX <td< td=""><td>В</td><td></td><td></td><td></td><td>ð</td><td></td><td>DIFF PAIR</td><td></td><td>8</td><td></td><td></td><td></td></td<>	В				ð		DIFF PAIR		8			
A43       CVD       SXD       4.49         A50       10       CC+X       D1F       PAIR       PCL+X       10       A50         A51       10       CC+X       D1F       PAIR       PCL+X       10       A51         A52       CAD       CAD       CAD       A52       A53       A54       11       A53         A54       11       CC+X       D1F       PAIR       PCL+X       11       A53         A55       SAS       11       CC+X       D1F       PAIR       PCL+X       11       A54         A55       SAS       12       -CC+X       D1F       PAIR       PCL+X       12       A36         A55       12       -CC+X       D1F       PAIR       PCL+X       12       A36         A56       12       -CC+X       D1F       PAIR       PCL+X       12       A36         A53       CVD       SAV       A38       CVD       A37       A36         A53       12       -CL+X       D1F       PAIR       PCL+X       13       A60         A54       CAV       CAV       CAV       A12       A36       A61       A00				A 4 7		РСІеТХ		РСІеТХ		A 4 7		
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A         ChD         GUD         AS2           AS3         11         PCIBIX         DITPATE         PCIBIX         11         AS3           AS5         11         PCIBIX         DITPATE         PCIBIX         11         AS4           A55         ShD         GUD         AS3         GUD         AS5           A56         ShD         GUD         AS5           A56         ShD         GUD         AS5           A56         ShD         GUD         AS5           A57         PCIBIX         DIFPATE         PCIBIX         12         AS5           A58         ShD         GUD         AS6         AS6         AS6         AS6           A58         ShD         GUD         AS6         AS6         AS6         AS6           A56         ShD         GUD         AS6         AS6         AS6         AS6         AS6           A56         14         PCIBIX         DIFF 2AI8         PCIBIX         14         A62           A66         A66         FCIBIX         DIFF 2AI8         PCIBIX         14         A66           A66         FCIBIX         DIFF 2AI8         PCIBIX <t< td=""><td></td><td></td><td></td><td></td><td>1 0</td><td></td><td>DIFF PAIR</td><td></td><td>1 0</td><td></td><td></td><td></td></t<>					1 0		DIFF PAIR		1 0			
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A63       14       PCIe TX       DIFF PAIR       PCIe TX       14       A63         A64       GND       GND       A64         A65       15       PCIeTX       DIFF PAIR       PCIeTX       15       A65         A66       15       PCIe TX       DIFF PAIR       PCIe TX       15       A66         A67       GND       GND       A67       Imm       Imm </td <td></td> <td></td> <td></td> <td></td> <td>4 4</td> <td>GND</td> <td></td> <td>GND</td> <td></td> <td></td> <td></td> <td></td>					4 4	GND		GND				
AAGNDAG4AG4A6515PCIeTXDIFF PAIRPCIeTX15A65A6615PCIeTXDIFF PAIRPCIeTX15A66A67GNDGNDA67A67A68RESERVEDDISCRETERESERVEDA68A69RESERVEDDISCRETERESERVEDA69A70RESERVEDDISCRETERESERVEDA70												
A6615PCIe TXDIFF PAIRPCIe TX15A66A67GNDGNDA67A68RESERVEDDISCRETERESERVEDA68A69RESERVEDDISCRETERESERVEDA69A70RESERVEDDISCRETERESERVEDA67A70RESERVEDDISCRETERESERVEDA70	A			A 6 4		GND		GND		A 6 4		
A 6 7G N DG N DA 6 7A 6 8RESERVEDDISCRETERESERVEDA 6 8A 6 9RESERVEDDISCRETERESERVEDA 6 9A 7 0RESERVEDDISCRETERESERVEDA 7 0											THIS DRAWING IS A CON	ICHK
A68RESERVEDDISCRETERESERVEDA68A69RESERVEDDISCRETERESERVEDA69A70RESERVEDDISCRETERESERVEDA70									1.3		mm	OTHERWISE SPECIFIED: APVD
ACCORESERVEDDISCRETERESERVEDACCOA70RESERVEDDISCRETERESERVEDA70				A 6 8		RESERVED		RESERVED		A 6 8		PLC ±0.5 PLC ±0.13
											' 4 A N	PLC ±0.0001 NGLES ±
											-	-

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TE Connectivity TOLERANCES UNLESS OTHERWISE SPECIFIED: APVD SLIVER 2.0 4C 30AWG STRAIGHT TO RIGHT ANGLE 0 PLC ±-1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES ±-FINISH PRODUCT SPEC -APPLICATION SPEC size cage code drawing no A 1 0 0 7 7 9 C - 2 3 6 1 3 5 5 RESTRICTED TO WEIGHT --SCALE 1:1 SHEET OF 3 REV B CUSTOMER DRAWING -

	3			4		5		6			8	8
									20	ED FOR PUBLICATION ALL RIGHTS RESERVED.		THIS DRAWING IS UNPUBLISHED.
		<b>P2</b>		WIRE TYPE		P1						2
	PIN NO.	PAIR NO.	DESIGNATION		DESIGNATION	PAIR NO.	PIN NO.					
	<u>B1</u>		12V POWER	DISCRETE	12V POWER		B 1					
	B2		12V POWER	DISCRETE	12V POWER		B2					
СОМ	B 3 -		12V POWER	DISCRETE	12V POWER		<u> </u>	Commoned -				
			<u>12V POWER</u>	DISCRETE	12V POWER		<u> </u>					
	B 5		<u>12V POWER</u>	DISCRETE	12V POWER		B 5					
			<u>12V POWER</u>	DISCRETE	12V POWER		<u>B6</u>					
	B 7		SIDE BAND	DISCRETE	SIDE BAND		B 7					
	B 8		SIDE BAND	DISCRETE	SIDE BAND		<u> </u>					
	B 9		SIDE BAND	DISCRETE	SIDE BAND		B 9					
	B 1 0		SIDE BAND	DISCRETE	SIDE BAND		B 1 0					
	B 1 1		3.3 AUX POWER	DISCRETE	3.3 AUX POWER		B 1 1					
	B12		SIDE BAND	DISCRETE	SIDE BAND		B12					
	B13		GND		GND		B 1 3					
	B 1 4		C L K	DIFF PAIR	CLK		B 1 4					
	B 1 5			DIFF PAIR	CLK		B 1 5					
	B16		GND		<u>GND</u>		B 1 6					
	B17	0	PCIe RX	DIFF PAIR	PCIe RX	U	B17					
	B18	0	PCIe RX	DIFF PAIR	PCIe RX	0	B18					
	B19 B20	1	GND PCIA PV		GND PCIA RY	1	B19					
	B 2 0	1	PCIe RX	DIFF PAIR	PCIe RX	1	B 2 0					
	B21 B22		PCIe RX	DIFF PAIR	PCIe RX		B21					
	B 2 2 B 2 3	2	GND PCIe RX	DIFF PAIR	GND PCIe RX	2	B 2 2 B 2 3					
	B 2 3 B 2 4	2	PCIe RX PCIe RX	DIFF PAIR DIFF PAIR	PCIe RX PCIe RX	2	B 2 3					
	B25		GND	THI FAIR	GND	Ĺ	B 2 5					
	B26	3	PCIe RX	DIFF PAIR	PCIe RX	3	B26					
	B27	3	PCIe RX	DIFF PAIR	PCIe RX	3	B 2 7					
	B 2 8		GND		GND		B 2 8					
			K E Y		KEY							
	B 2 9		GND		GND		B 2 9					
	B 3 0	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 0					
	B 3 1	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 1					
	B 3 2		GND		GND		B 3 2					
	B 3 3	5	PCIe RX	DIFF PAIR	PCIe RX	5	B 3 3					
	B 3 4	5	PCIe RX	DIFF PAIR	PCIe RX	5	B 3 4					
	B 3 5		GND		GND		B 3 5					
	B 3 6	6	PCIe RX	DIFF PAIR	PCIe RX	6	B 3 6					
	B 3 7	6	PCIe RX	DIFF PAIR	PCIe RX	6	B 3 7					
	B 3 8	· · ·	GND		GND		B 3 8					
	B 3 9	7	PCIe RX	DIFF PAIR	PCIe RX	7	B 3 9					
	B 4 0	7	PCIe RX	DIFF PAIR	PCIe RX	7	B 4 0					
	B 4 1		GND		GND		B 4 1					
	B 4 2		PRSNT DETECT	DISCRETE	PRSNT DETECT		B 4 2					
			KEY		KEY							
	B 4 3		GND		GND		B 4 3					
	B 4 4	8	PCIe RX	DIFF PAIR	PCIe RX	8	B 4 4					
	B 4 5	8	PCIe RX	DIFF PAIR	PCIe RX	8	B 4 5					
	B 4 6		GND		GND		B 4 6					
	B 4 7	9	PCIe RX	DIFF PAIR	PCIe RX	9	B 4 7					
	B 4 8	9	PCIe RX	DIFF PAIR	PCIe RX	9	B 4 8					
	B 4 9		GND		GND		B 4 9					
	B 5 0	1 0	PCIe RX	DIFF PAIR	PCIe RX	1 0	B 5 0					
	B 5 1	1 0	PCIe RX	DIFF PAIR	PCIe RX	1 0	B 5 1					
	B 5 2		GND		GND		B 5 2					
	B 5 3	1 1	PCIe RX	DIFF PAIR	PCIe RX	1 1	B 5 3					
	B 5 4	1 1	PCIe RX	DIFF PAIR	PCIe RX	1 1	B 5 4					
	B 5 5		GND		GND		B 5 5					
	B 5 6	1 2	PCIe RX	DIFF PAIR	PCIe RX	1 2	B 5 6					
	B 5 7	1 2	PCIe RX	DIFF PAIR	PCIe RX	1 2	B 5 7					
	B 5 8		GND		GND		B 5 8					
	B 5 9	1 3	PCIe RX	DIFF PAIR	PCIe RX	1 3	B 5 9					
	B60	1 3	PCIe RX	DIFF PAIR	PCIe RX	1 3	B60					
	B 6 1		GND		GND		B 6 1					
	B 6 2	1 4	PCIe RX	DIFF PAIR	PCIe RX	1 4	B 6 2					
	B 6 3	1 4	PCIe RX	DIFF PAIR	PCIe RX	1 4	B 6 3					
	B 6 4		GND		GND		B 6 4					
TULC	B 6 5	1 5	PCIe RX	DIFF PAIR	PCIe RX	15	B 6 5					
I THIS D	B66	1 5	PCIe RX	DIFF PAIR	PCIe RX	1 5	B 6 6					
		1					B 6 7					
	B 6 7		GND		GND							
DI			GND RESERVED	DISCRETE	G N D R E S E R V E D		B68					
THIS DR DIM MATERIAL	B 6 7			DISCRETE DISCRETE								

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DWN 30AUG2019 B. MATTHEWS CHK 30AUG2019 ING IS A CONTROLLED DOCUMENT. **TE** TE Connectivity TOLERANCES UNLESS OTHERWISE SPECIFIED: APVD SLIVER 2.0 4C 30AWG STRAIGHT TO RIGHT ANGLE 0 PLC ±-1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES ±-FINISH PRODUCT SPEC -APPLICATION SPEC size cage code drawing no A 1 0 0 7 7 9 C - 2 3 6 1 3 5 5 RESTRICTED TO WEIGHT --SCALE 1:1 SHEET OF 3 REV B CUSTOMER DRAWING -