

40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI1012-8 (TOLL)

Product Summary

BVDSS	R _{DS(ON)} Max	I _D Max Tc = +25°C	
40V	0.85mΩ @ V _{GS} = 10V	300A	

Description and Applications

This new generation N-channel enhancement mode MOSFET is designed to minimize R_{DS(ON)} yet maintain superior switching performance. This device is ideal for use in power management and load switch.

- Motor controls
- DC-DC converters
- · Power managements

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On State Losses
- Wettable Flank for Improved Optical Inspection
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

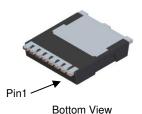
Mechanical Data

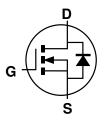
- Package: POWERDI[®]1012-8 (TOLL)
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.388 grams (Approximate)



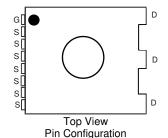








Internal Schematic



Ordering Information (Note 4)

Part Number	Paakana	Packing		
Part Number	Package	Qty.	Carrier	
DMTH4001STLW-13	POWERDI1012-8	1500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



);; = Manufacturer's Marking
 TH4001STL = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 22 = 2022)
 WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	40	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current, V _{GS} = 10V (Notes 6 and 9) $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$		ID	300 210	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	Ірм	1200	Α	
Continuous Body Diode Forward Current (Note 6)	Is	300	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	lsм	1200	Α	
Avalanche Current, L = 0.3mH	las	76.4	Α	
Avalanche Energy, L = 0.3mH	Eas	876.5	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	6	W
Thermal Resistance, Junction to Ambient (Note 5)		Reja	25	°C/W
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		PD	300	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	0.5	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C	

Electrical Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 32V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	•	•		•	•	•	
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	V _{DS} = V _{GS} , I _D = 250μA	
Static Drain-Source On-Resistance	RDS(ON)	_	0.55	0.85	mΩ	V _G S = 10V, I _D = 30A	
Diode Forward Voltage	VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)	•	•		•	•	•	
Input Capacitance	Ciss	_	13185	_		V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	7151	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	220	_			
Gate Resistance	Rg	_	2.0	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	150	_			
Gate-Source Charge	Qgs	_	59.4	_	nC	$V_{DS} = 20V, I_D = 30A, V_{GS} = 10V$	
Gate-Drain Charge	Qgd	_	22.4	_			
Turn-On Delay Time	tD(ON)	_	22.1	_			
Turn-On Rise Time	tR	_	35.9	_	20	$V_{DD} = 20V, V_{GS} = 10V,$ $I_{D} = 30A, R_{g} = 2.5\Omega$	
Turn-Off Delay Time	tD(OFF)	_	88.1	_	ns		
Turn-Off Fall Time	t _F		57.3	_			
Body Diode Reverse Recovery Time	trr	_	93	_	ns	la 154 di/dt 1004/up	
Body Diode Reverse Recovery Charge	Q _{RR}	_	194	_	- Is = 15A, di/dt = 100A/μs		

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate. Notes:

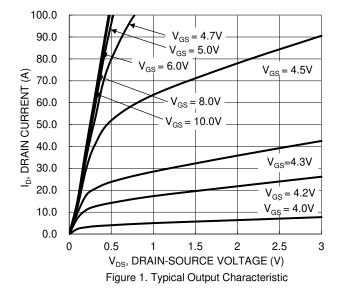
6. Thermal resistance from junction to soldering point (on the exposed drain pad).

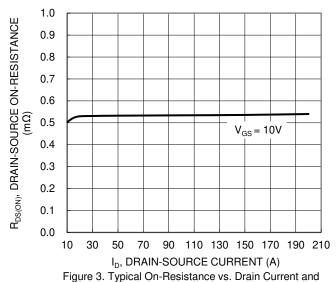
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

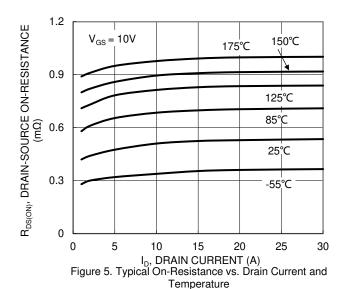
9. Limited by Package.

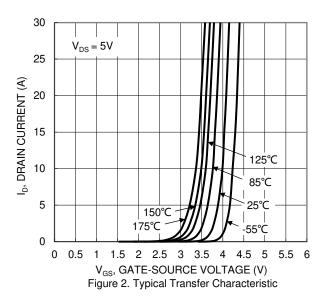


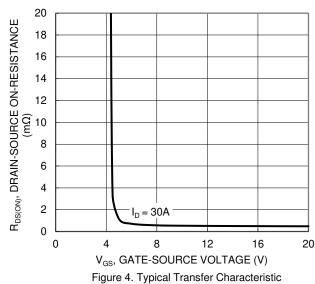




Gate Voltage







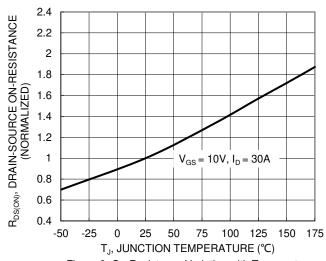


Figure 6. On-Resistance Variation with Temperature





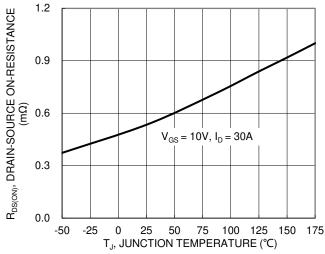


Figure 7. On-Resistance Variation with Temperature

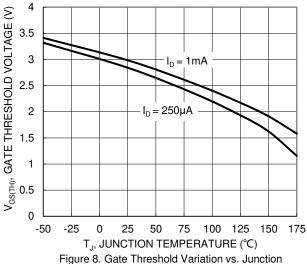


Figure 8. Gate Threshold Variation vs. Junction Temperature

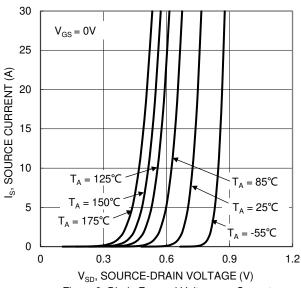
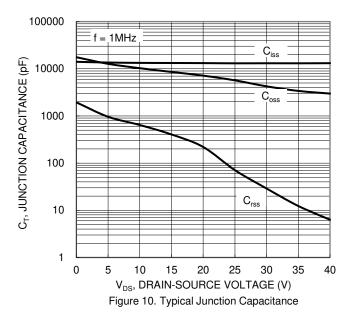
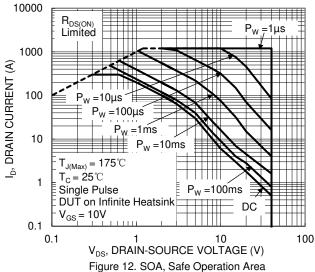


Figure 9. Diode Forward Voltage vs. Current



10 8 6 E V_{DS} = 20V, I_D = 30A 2 0 0 0 30 60 90 120 150 Qg (nC) Figure 11. Gate Charge





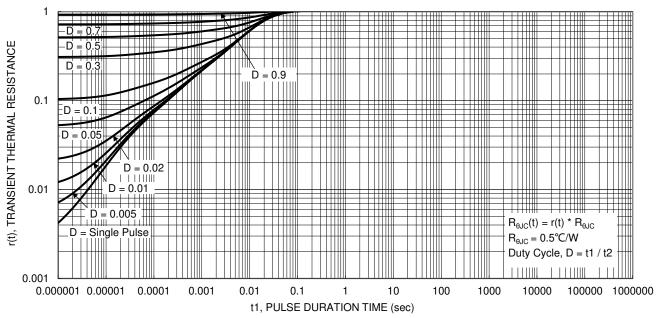


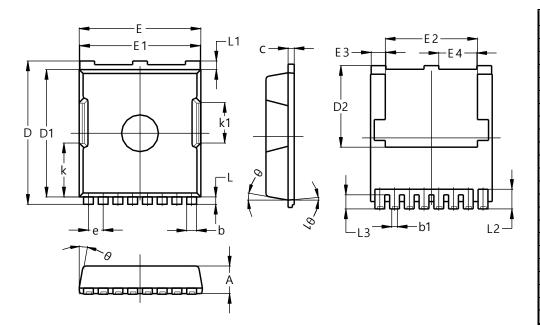
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

POWERDI1012-8

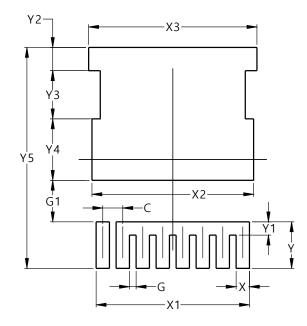


POWERDI1012-8						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
b	0.70	0.90	0.80			
b1	0.42	0.50	0.45			
С	0.40	0.60	0.50			
D	11.48	11.88	11.68			
D1	10.23	10.53	10.38			
D2	6.45	6.85	6.65			
Е	9.70	10.10	9.90			
E1	9.70	9.90	9.80			
E2	7.00	8.00	7.50			
E3	1.10	1.30	1.20			
E4	3.00	3.20	3.10			
е		1.20 BSC				
k	4.39 REF					
k1	3.30 REF					
L	0.50	0.70	0.60			
L1	0.50	0.90	0.70			
L2	1.40	1.80	1.60			
L3	1.00	1.30	1.15			
θ	0₀	15º	10⁰			
θ1	0∘	10⁰	5º			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

POWERDI1012-8



Dimensions	Value (in mm)			
С	1.200			
G	0.400			
G1	2.500			
Х	0.800			
X1	9.200			
X2	9.700			
Х3	10.100			
Υ	2.800			
Y1	0.800			
Y2	1.400			
Y3	2.900			
Y4	3.700			
Y5	13.300			



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