

## Product Summary

<b>BV<sub>DSS</sub></b>	<b>R<sub>DS(on)</sub> max</b>	<b>I<sub>D</sub></b> T <sub>A</sub> = +25°C
-50V	10Ω @ V <sub>GS</sub> = -5V	-130mA

## Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([BSS84Q](#))**

## Description and Applications

This MOSFET has been designed to minimize on-state resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

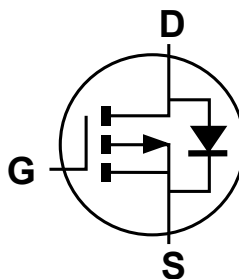
## Mechanical Data

- Case: SOT23 (Standard)
- Case Material: UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish (Lead Free Plating) Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.009 grams (Approximate)

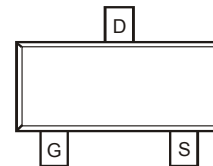
SOT23



Top View



Equivalent Circuit



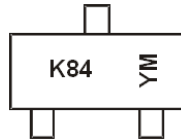
Top View

## Ordering Information (Note 4)

Part Number	Case	Packaging
BSS84-7-F	SOT23 (Standard)	3000/Tape & Reel
BSS84-13-F	SOT23 (Standard)	10000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



K84 = Product Type Marking Code  
 YM = Date Code Marking  
 Y or  $\bar{Y}$  = Year (ex: I = 2021)  
 M or  $\bar{M}$  = Month (ex: 9 = September)

### Date Code Key

Year	1998	.....	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	J	.....	I	J	K	L	M	N	O	P	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Maximum Ratings (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	-50	V
Drain-Gate Voltage $R_{GS} \leq 20\text{k}\Omega$	$V_{DGR}$	-50	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current (Note 5)	$I_D$	-130	mA
Pulsed Drain Current	$I_{DM}$	-1.2	A

## Thermal Characteristics (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	300	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

## Electrical Characteristics (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 6)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	-50	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	-1	$\mu\text{A}$	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = +25^\circ\text{C}$
		—	—	-100	nA	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = +125^\circ\text{C}$
Gate-Body Leakage	$I_{GSS}$	—	—	$\pm 10$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS (Note 6)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	-0.8	—	-2.0	V	$V_{DS} = V_{GS}, I_D = -1\text{mA}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	3.2	10	$\Omega$	$V_{GS} = -5\text{V}, I_D = -0.100\text{A}$
Forward Transconductance	$g_{FS}$	0.05	—	—	S	$V_{DS} = -25\text{V}, I_D = -0.1\text{A}$
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	$C_{iss}$	—	24.6	45	pF	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{oss}$	—	4.7	25	pF	
Reverse Transfer Capacitance	$C_{rss}$	—	2.8	12	pF	
Gate Resistance	$R_g$	—	916	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ( $V_{GS} = -4.5\text{V}$ )	$Q_g$	—	0.28	—	nC	$V_{DS} = -10\text{V}, I_D = -0.1\text{A}$
Total Gate Charge ( $V_{GS} = -10\text{V}$ )	$Q_{g1}$	—	0.59	—	nC	
Gate-Source Charge	$Q_{gs}$	—	0.09	—	nC	
Gate-Drain Charge	$Q_{gd}$	—	0.08	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	10	—	ns	$V_{DD} = -30\text{V}, I_D = -0.27\text{A}, R_{GEN} = 50\Omega, V_{GS} = -10\text{V}$
Turn-Off Delay Time	$t_{D(off)}$	—	18	—	ns	

- Notes:
- Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

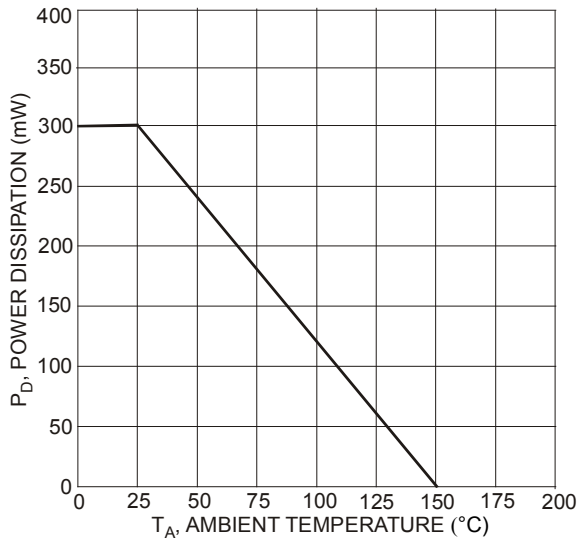


Fig. 1 Max Power Dissipation vs. Ambient Temperature

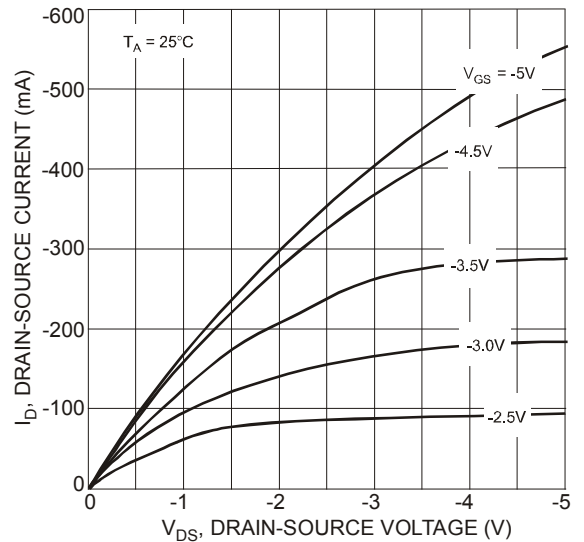


Fig. 2 Drain-Source Current vs. Drain-Source Voltage

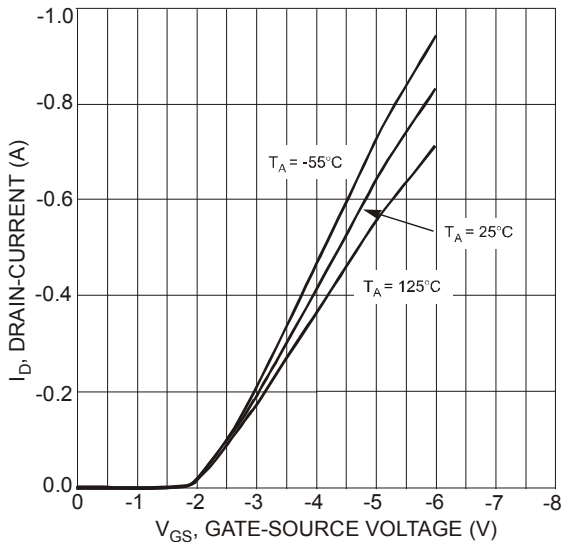


Fig. 3 Drain-Current vs. Gate-Source Voltage

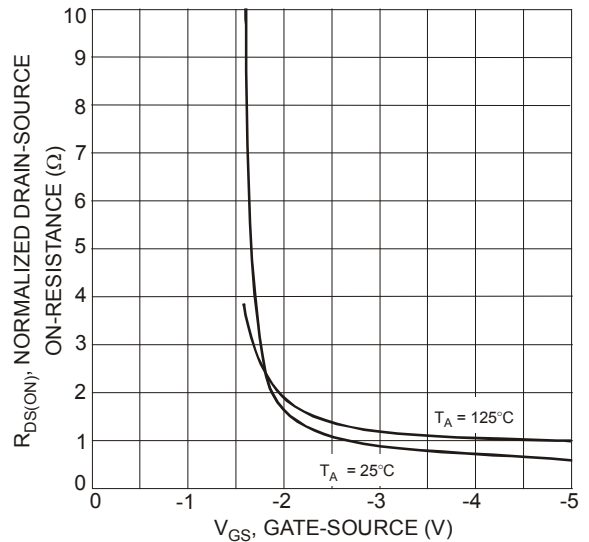


Fig. 4 On-Resistance vs. Gate-Source Voltage

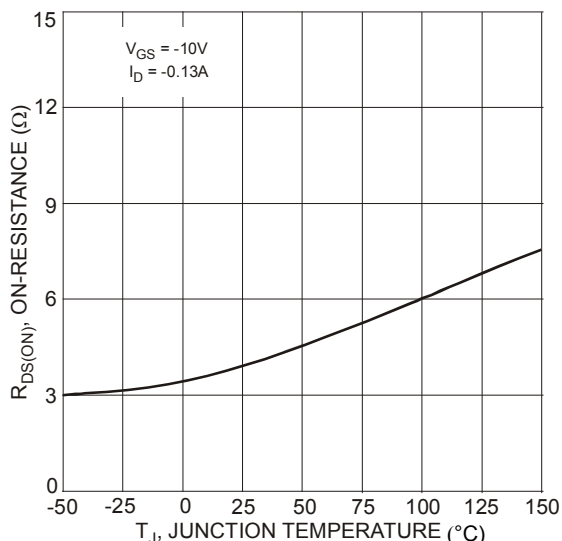


Fig. 5 On-Resistance vs. Junction Temperature

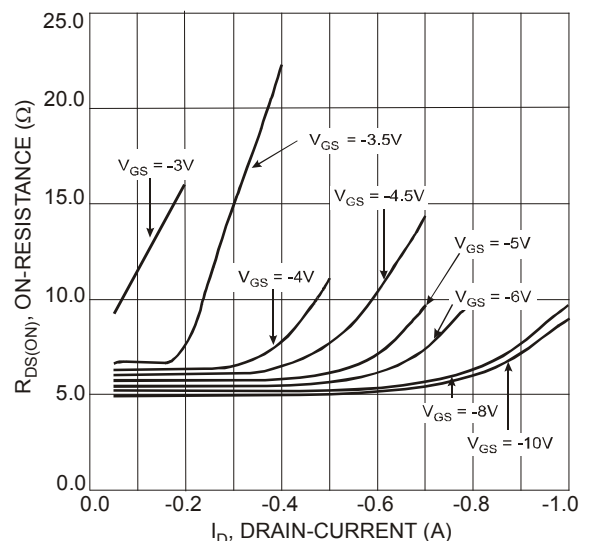


Fig. 6 On-Resistance vs. Drain-Current

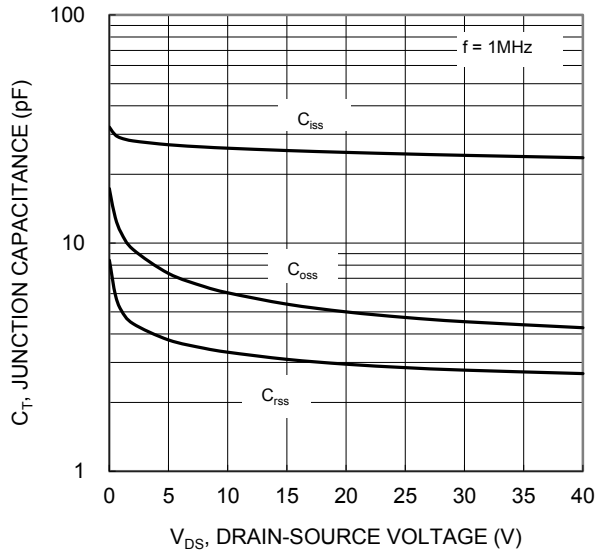


Fig. 7 Typical Junction Capacitance

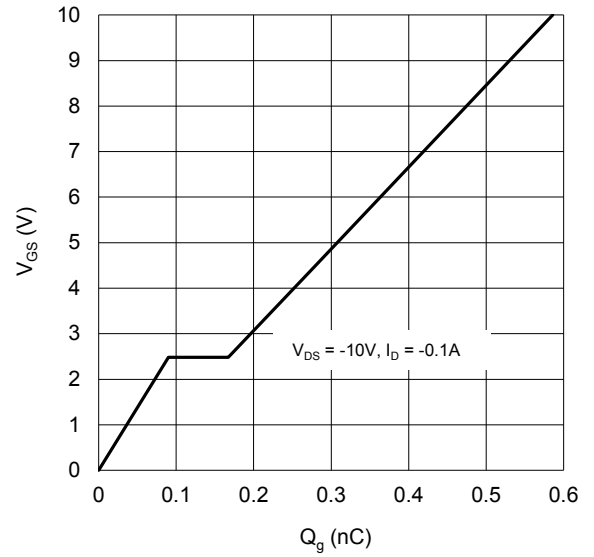


Fig. 8 Gate Charge

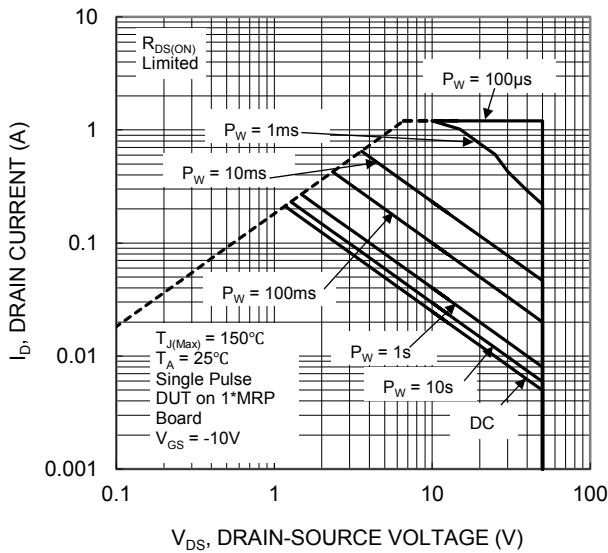
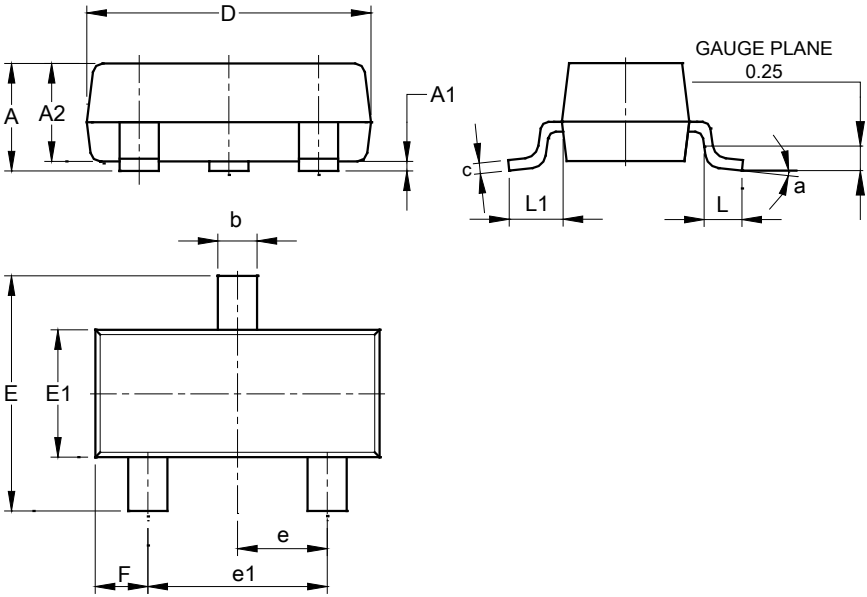


Fig. 9 SOA, Safe Operation Area

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23 (Standard)**

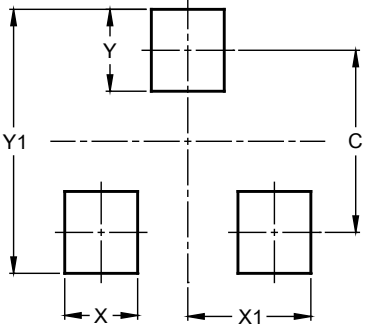


SOT23 (Standard)			
Dim	Min	Max	Typ
A	0.90	1.15	1.025
A1	0.00	0.10	0.05
A2	0.85	1.10	0.975
b	0.30	0.51	0.40
c	0.080	0.202	0.11
D	2.80	3.00	2.90
E	2.25	2.55	2.40
E1	1.20	1.40	1.30
e	0.89	1.03	0.915
e1	1.78	2.05	1.83
F	0.40	0.60	0.535
L1	0.45	0.61	0.55
L	0.25	0.55	0.40
a	0°	8°	--
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23 (Standard)**



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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