

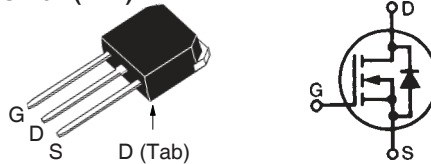
# TrenchT2™ Power MOSFET

N-Channel Enhancement Mode  
Avalanche Rated

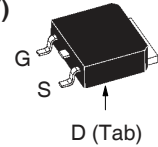
**IXTI90N055T2**  
**IXTY90N055T2**  
**IXTA90N055T2**  
**IXTP90N055T2**

$V_{DSS} = 55V$   
 $I_{D25} = 90A$   
 $R_{DS(on)} \leq 8.4m\Omega$

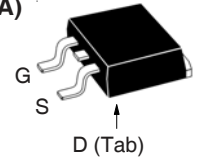
TO-262 (IXTI)



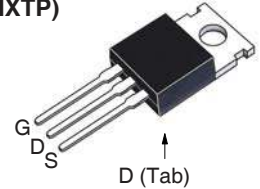
TO-252 (IXTY)



TO-263 (IXTA)



TO-220 (IXTP)



G = Gate      D = Drain  
S = Source    Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ C$ to $175^\circ C$	55	V
$V_{DGR}$	$T_J = 25^\circ C$ to $175^\circ C$ , $R_{GS} = 1M\Omega$	55	V
$V_{GSM}$	Transient	$\pm 20$	V
$I_{D25}$	$T_C = 25^\circ C$	90	A
$I_{DM}$	$T_C = 25^\circ C$ , Pulse Width Limited by $T_{JM}$	230	A
$I_A$	$T_C = 25^\circ C$	50	A
$E_{AS}$	$T_C = 25^\circ C$	300	mJ
$P_D$	$T_C = 25^\circ C$	150	W
$T_J$		-55 ... +175	$^\circ C$
$T_{JM}$		175	$^\circ C$
$T_{stg}$		-55 ... +175	$^\circ C$
$T_L$	Maximum Lead Temperature for Soldering	300	$^\circ C$
$T_{SOLD}$	1.6 mm (0.062in.) from Case for 10s	260	$^\circ C$
$F_C$	Mounting Force (TO-263)	10..65 / 2.2..14.6	N/lb
$M_d$	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in
<b>Weight</b>	TO-252	0.35	g
	TO-262 (Lead)	0.40	g
	TO-263	2.50	g
	TO-220	3.00	g

## Features

- International Standard Packages
- Avalanche Rated
- Low Package Inductance
- Fast Intrinsic Rectifier
- 175°C Operating Temperature
- High Current Handling Capability
- ROHS Compliant
- High Performance Trench Technology for extremely low  $R_{DS(on)}$

## Advantages

- High Power Density
- Easy to Mount
- Space Savings

## Applications

- Automotive Engine Control
- Synchronous Buck Converter (for Notebook System Power & General Purpose Point & Load)
- DC/DC Converters
- High Current Switching Applications
- Power Train Management
- Distributed Power Architecture

Symbol	Test Conditions ( $T_J = 25^\circ C$ Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0V$ , $I_D = 250\mu A$	55		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	2.0		V
$I_{GSS}$	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			$\pm 200$ nA
$I_{DSS}$	$V_{DS} = V_{DSS}$ , $V_{GS} = 0V$ $T_J = 150^\circ C$			2 $\mu A$
				200 $\mu A$
$R_{DS(on)}$	$V_{GS} = 10V$ , $I_D = 25A$ , Notes 1 & 2	7.0	8.4	m $\Omega$

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 10\text{V}, I_D = 45\text{A}$ , Note 1	25	43	S
$C_{iss}$ $C_{oss}$ $C_{rss}$	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$		2770	pF
			420	pF
			102	pF
$t_{d(on)}$ $t_r$ $t_{d(off)}$ $t_f$	<b>Resistive Switching Times</b> $V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 25\text{A}$ $R_G = 5\Omega$ (External)		19	ns
			21	ns
			39	ns
			19	ns
$Q_{g(on)}$ $Q_{gs}$ $Q_{gd}$	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 25\text{A}$		42	nC
			14	nC
			8.5	nC
$R_{thJC}$ $R_{thCS}$	TO-220			1.00 $^\circ\text{C/W}$
			0.50	$^\circ\text{C/W}$

#### Source-Drain Diode

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$I_S$	$V_{GS} = 0\text{V}$			90 A
$I_{SM}$	Repetitive, Pulse Width Limited by $T_{JM}$			360 A
$V_{SD}$	$I_F = 25\text{A}, V_{GS} = 0\text{V}$ , Note 1		0.85	1.00 V
$t_{rr}$ $I_{RM}$ $Q_{RM}$	$I_F = 45\text{A}, V_{GS} = 0\text{V}$ , $-di/dt = 100\text{A}/\mu\text{s}, V_R = 27\text{V}$		37	ns
			2.2	A
			40	nC

- Notes: 1. Pulse test,  $t \leq 300\mu\text{s}$ ; duty cycle,  $d \leq 2\%$ .  
 2. On through-hole packages,  $R_{DS(on)}$  Kelvin test contact location must be 5mm or less from the package body.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065B1	6,683,344	6,727,585	7,005,734B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123B1	6,534,343	6,710,405B2	6,759,692	7,063,975B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728B1	6,583,505	6,710,463	6,771,478B2	7,071,537	

Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$

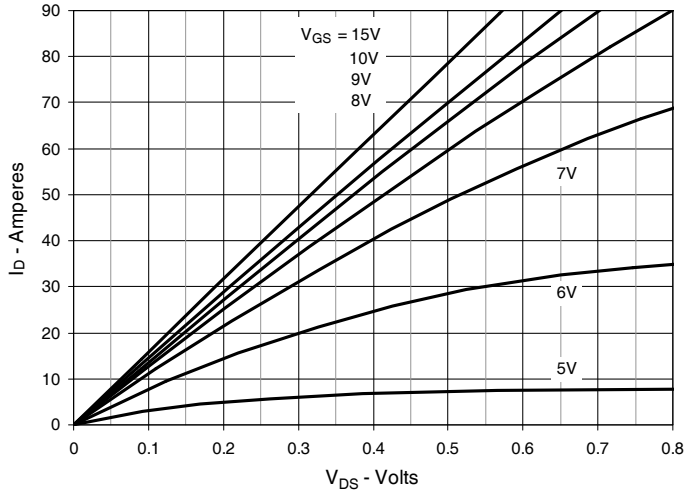


Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$

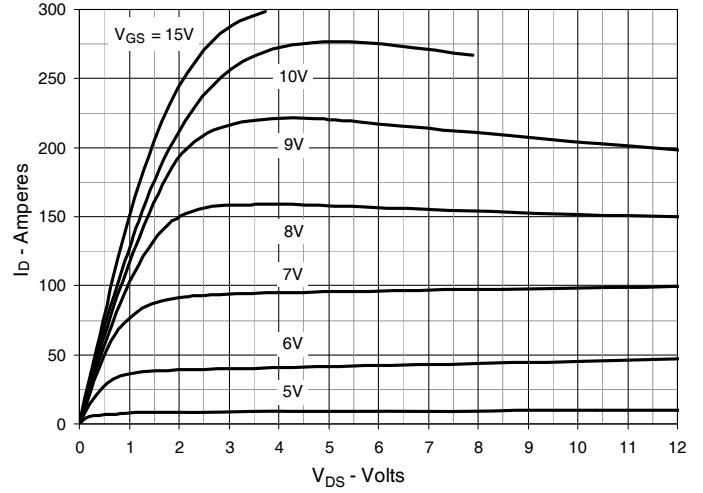


Fig. 3. Output Characteristics @  $T_J = 150^\circ\text{C}$

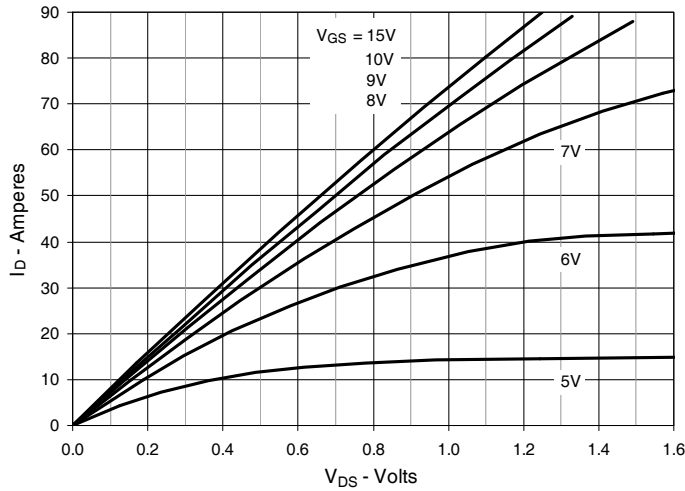


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 45\text{A}$  Value vs. Junction Temperature

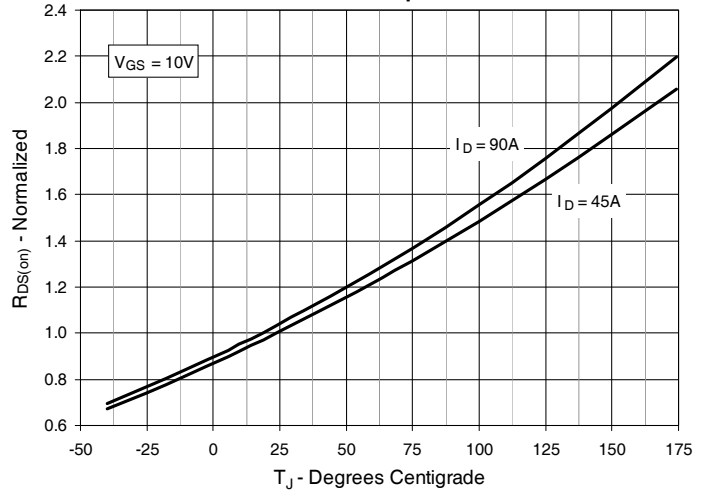


Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 45\text{A}$  Value vs. Drain Current

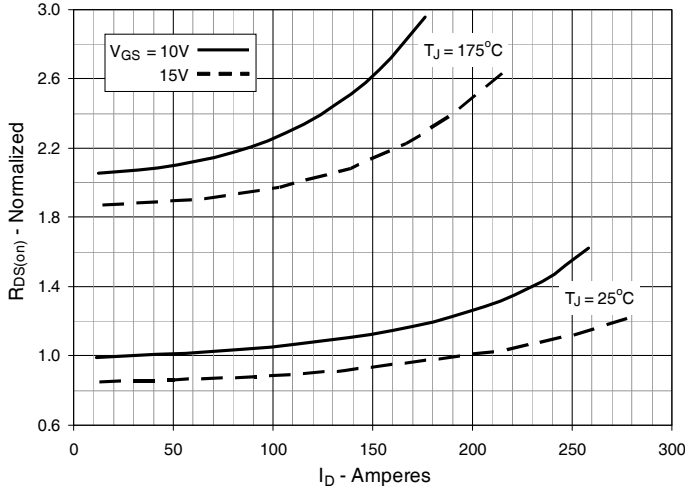


Fig. 6. Drain Current vs. Case Temperature

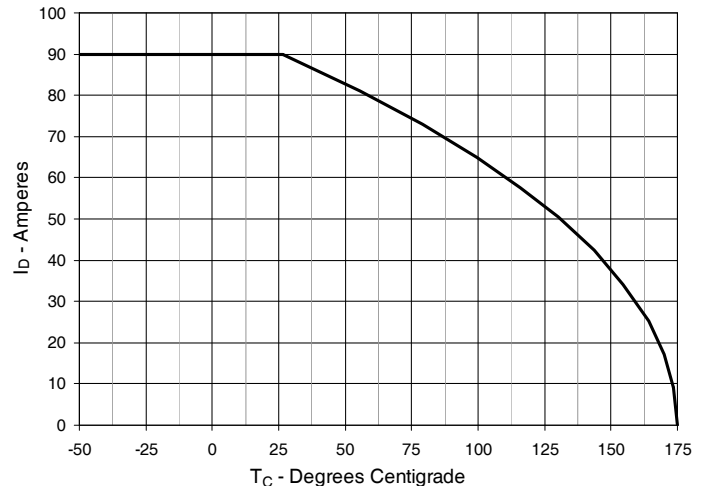


Fig. 7. Input Admittance

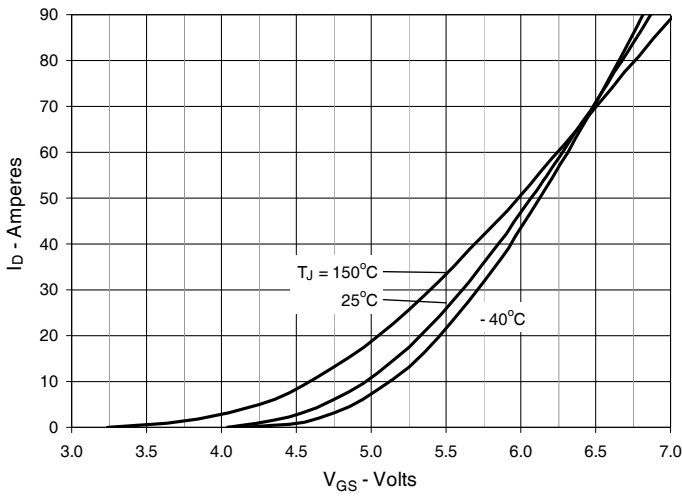


Fig. 8. Transconductance

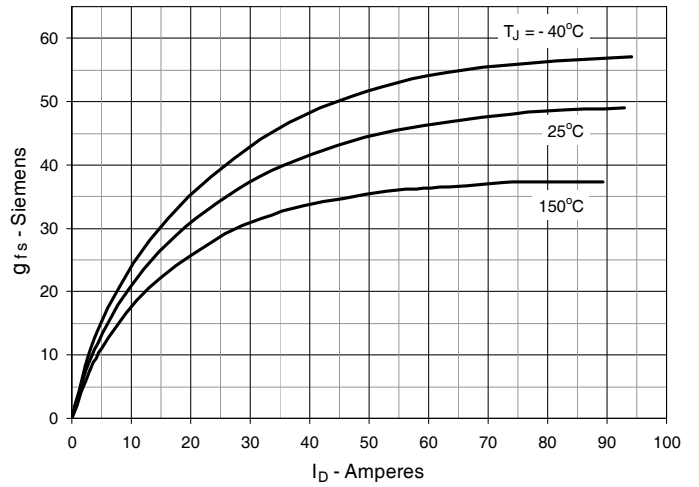


Fig. 9. Forward Voltage Drop of Intrinsic Diode

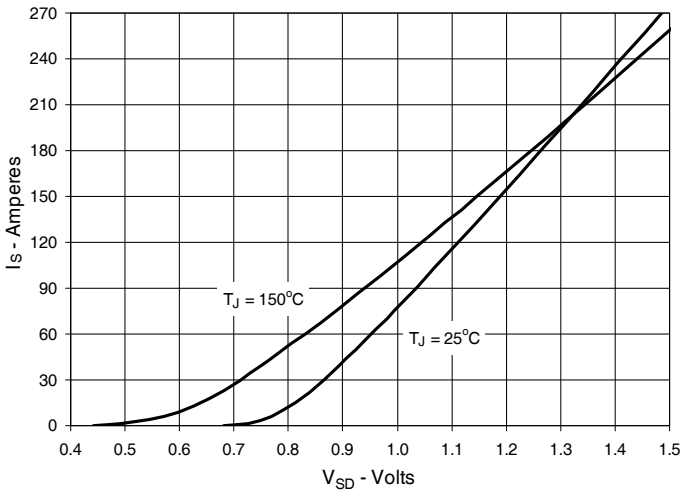


Fig. 10. Gate Charge

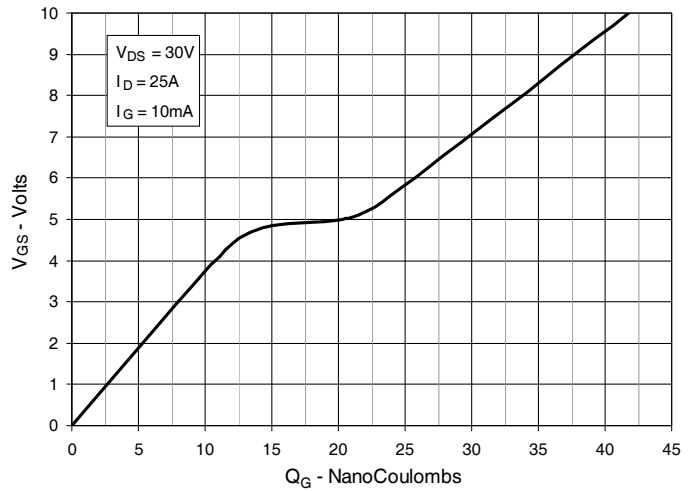


Fig. 11. Capacitance

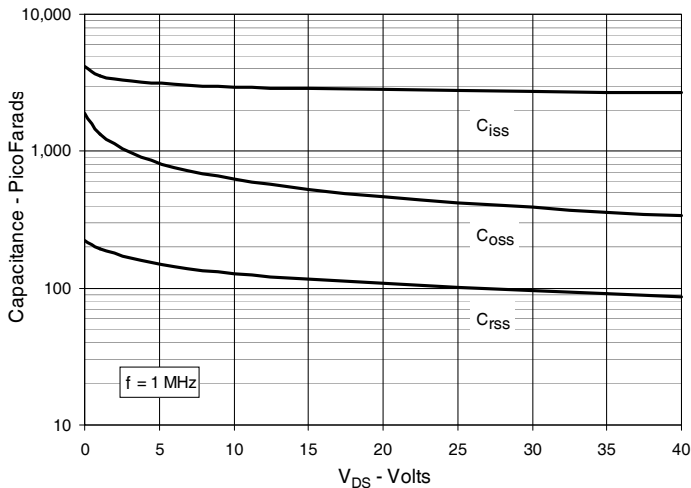
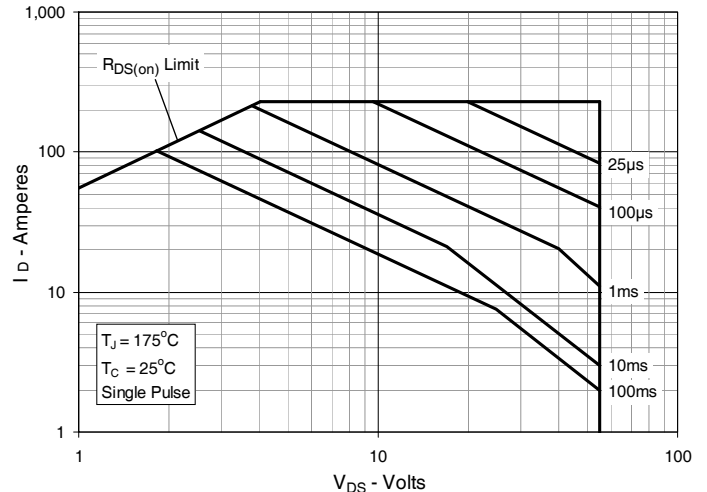
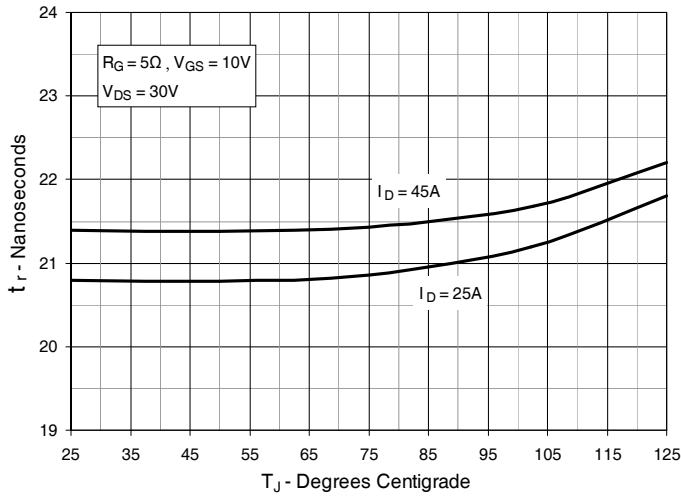


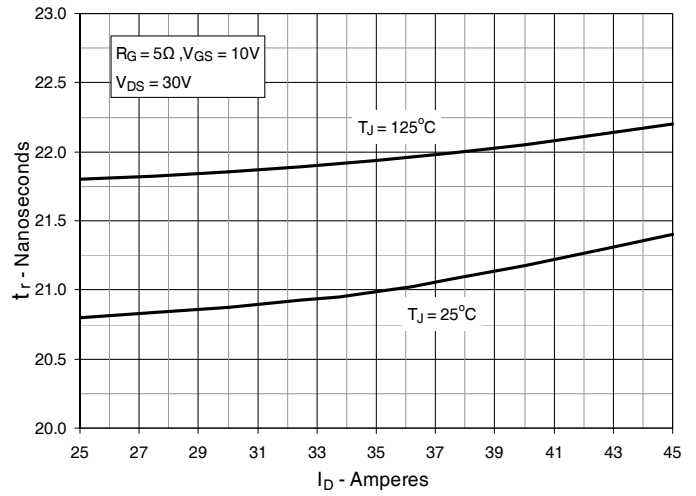
Fig. 12. Forward-Bias Safe Operating Area



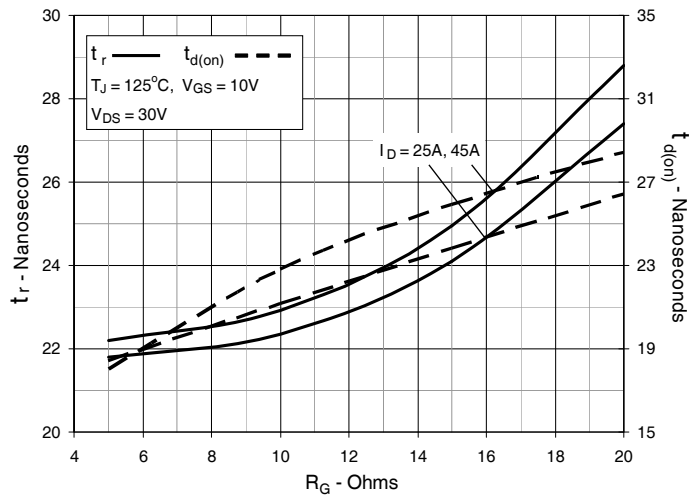
**Fig. 13. Resistive Turn-on Rise Time vs. Junction Temperature**



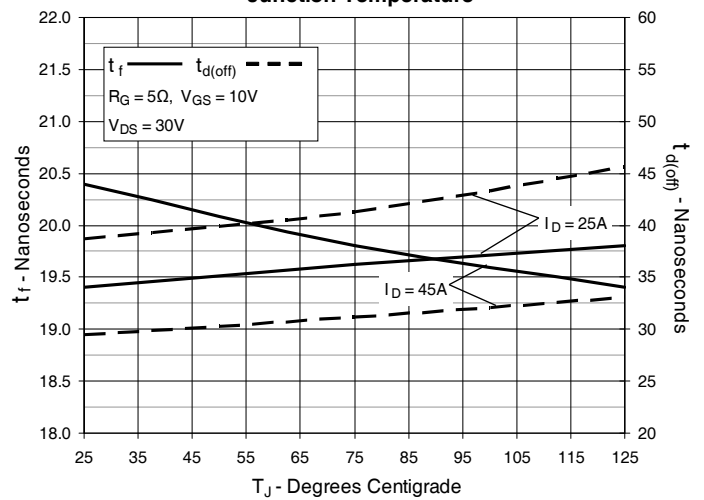
**Fig. 14. Resistive Turn-on Rise Time vs. Drain Current**



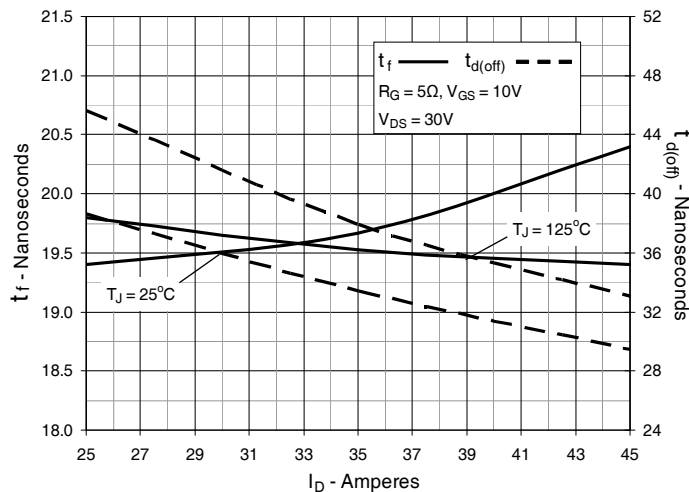
**Fig. 15. Resistive Turn-on Switching Times vs. Gate Resistance**



**Fig. 16. Resistive Turn-off Switching Times vs. Junction Temperature**



**Fig. 17. Resistive Turn-off Switching Times vs. Drain Current**



**Fig. 18. Resistive Turn-off Switching Times vs. Gate Resistance**

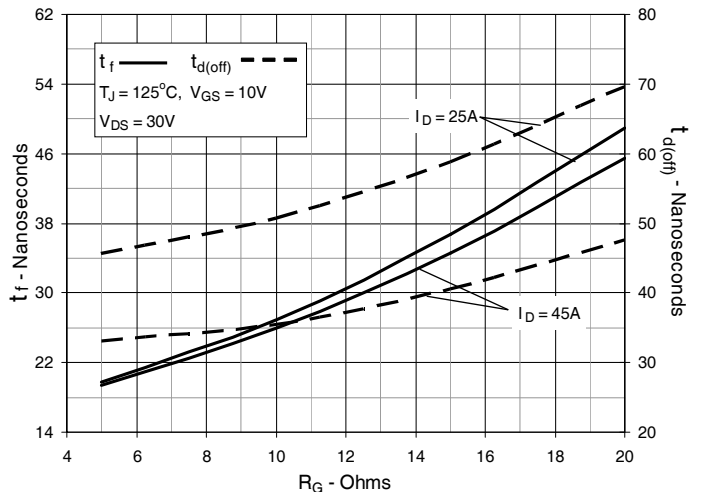
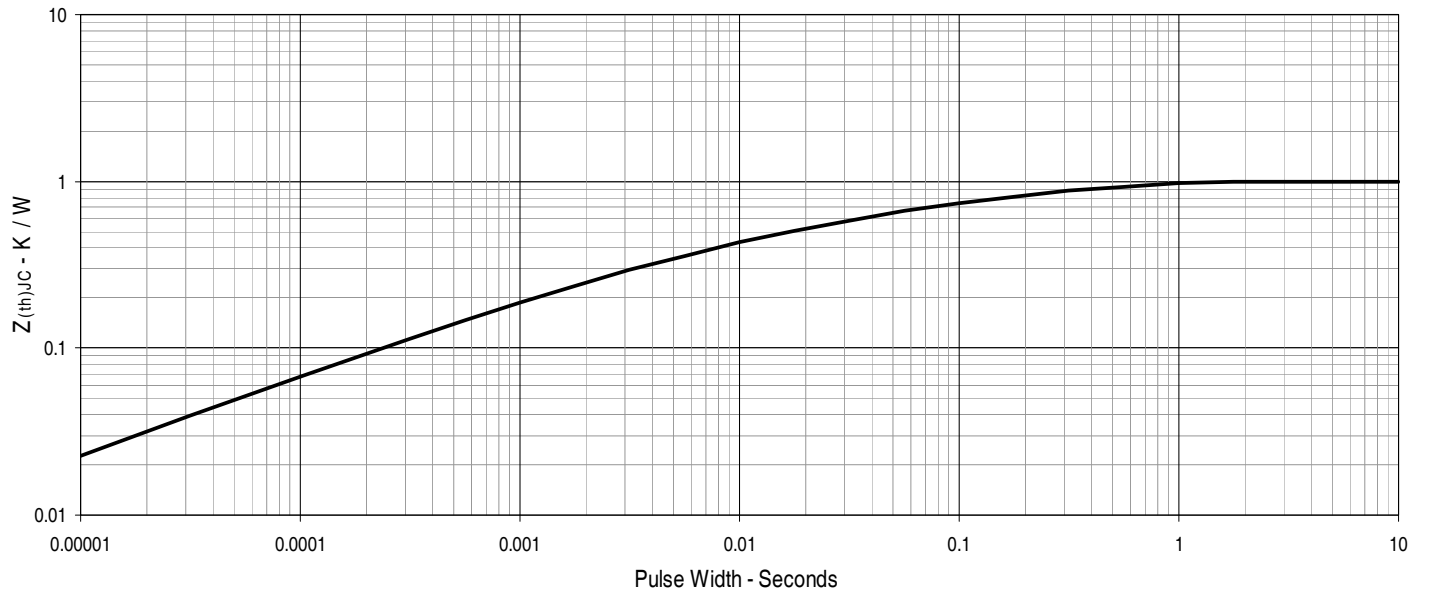
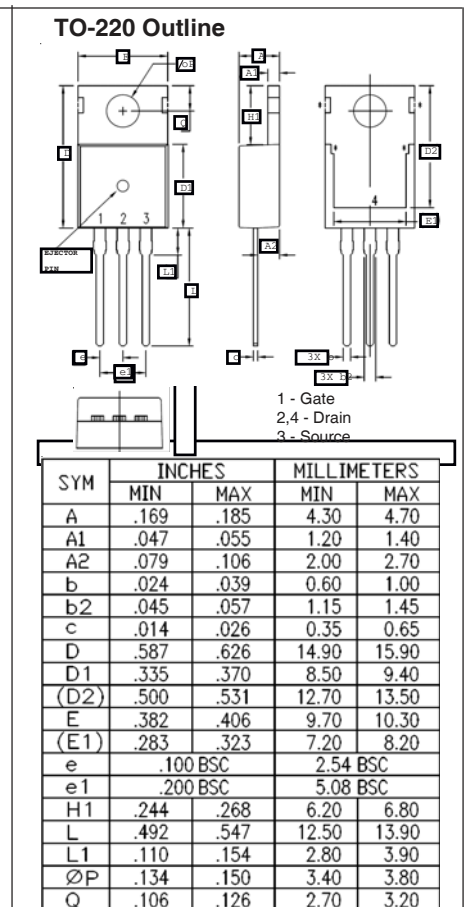
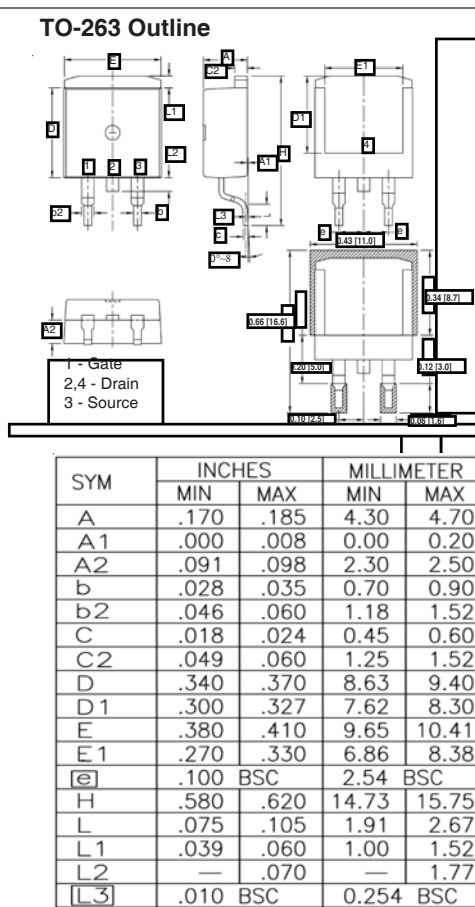
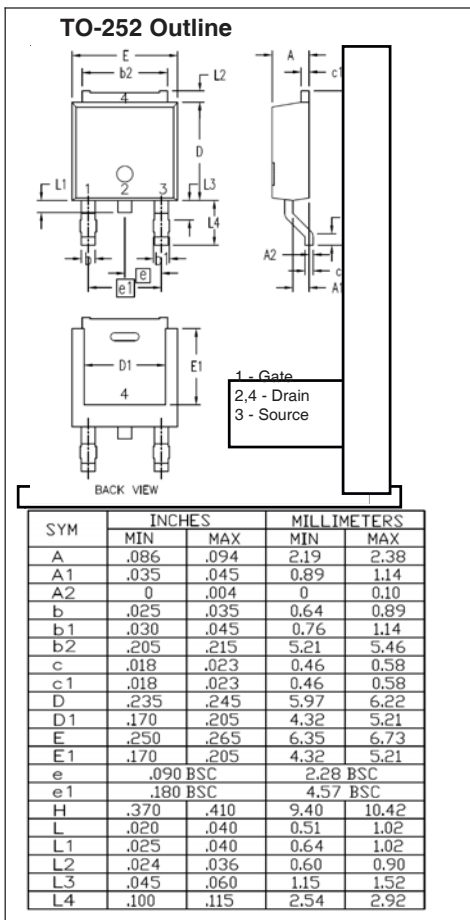
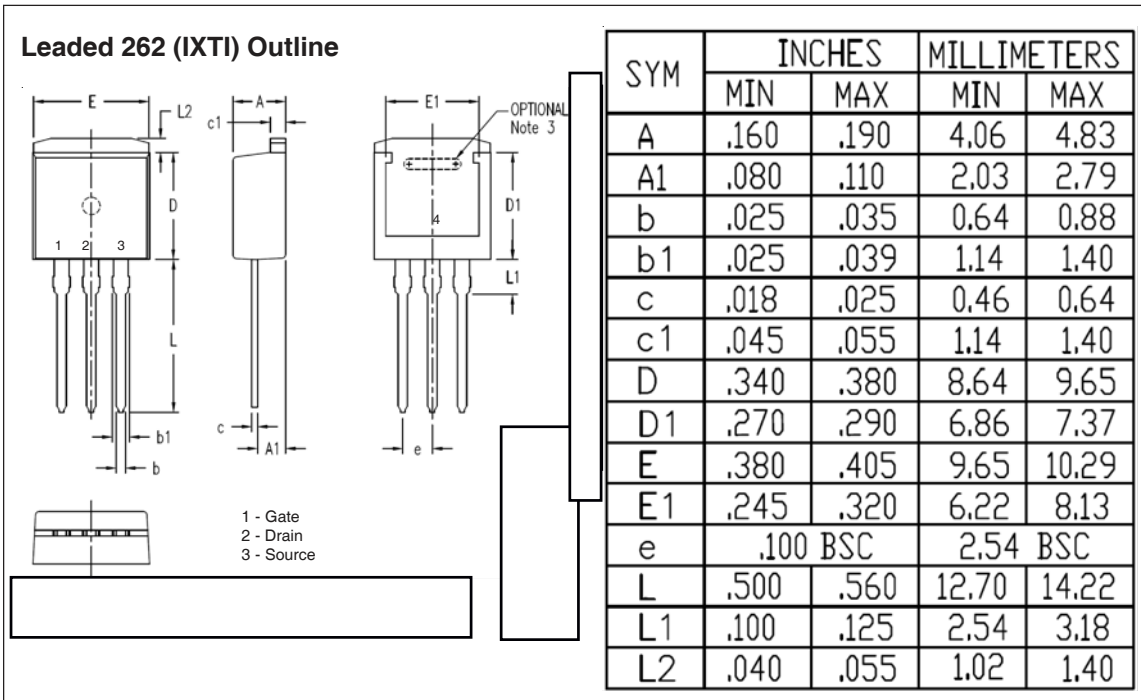


Fig. 19. Maximum Transient Thermal Impedance







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