



#### 60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
60V	65mΩ @ V <sub>GS</sub> = 10V	27A
60 V	79mΩ @ V <sub>GS</sub> = 4.5V	24A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

#### **Features and Benefits**

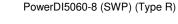
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low Input Capacitance
- Wettable Flank for Improved Optical Inspections
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
  - For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

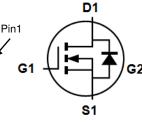
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
  - https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMNH6065SPDWQ</u>)

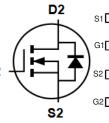
### **Mechanical Data**

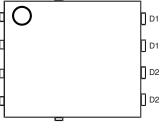
- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)











Top View

**Bottom View** 

Equivalent Circuit

Top View Pin Configuration

### Ordering Information (Note 4)

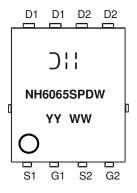
Part Number	Case	Packaging
DMNH6065SPDW-13	PowerDI5060-8 (SWP) (Type R)	2500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



# **Marking Information**



⊃¦¦= Manufacturer's Marking NH6065SPDW = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 20 = 2020) WW = Week (01 to 53)

# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	60	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current, $V_{GS} = 10V$ (Note 6) $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$		lo	27 19	А
Maximum Body Diode Forward Current (Note 6)	Is	27	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	108	Α	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	Ism	108	Α	
Avalanche Current, L = 1mH	las	13.3	Α	
Avalanche Energy, L = 1mH	Eas	89	mJ	

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	62	°C/W
Total Power Dissipation $T_A = +25^{\circ}C$		PD	2.4	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	2.2	°C/W
Total Power Dissipation	T <sub>C</sub> = +25°C	P <sub>D</sub>	68	W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Notes:

<sup>5.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate; measured with 1 channel active.

<sup>6.</sup> Thermal resistance from junction to solder point (on the exposed drain pin); measured with 1 channel active.



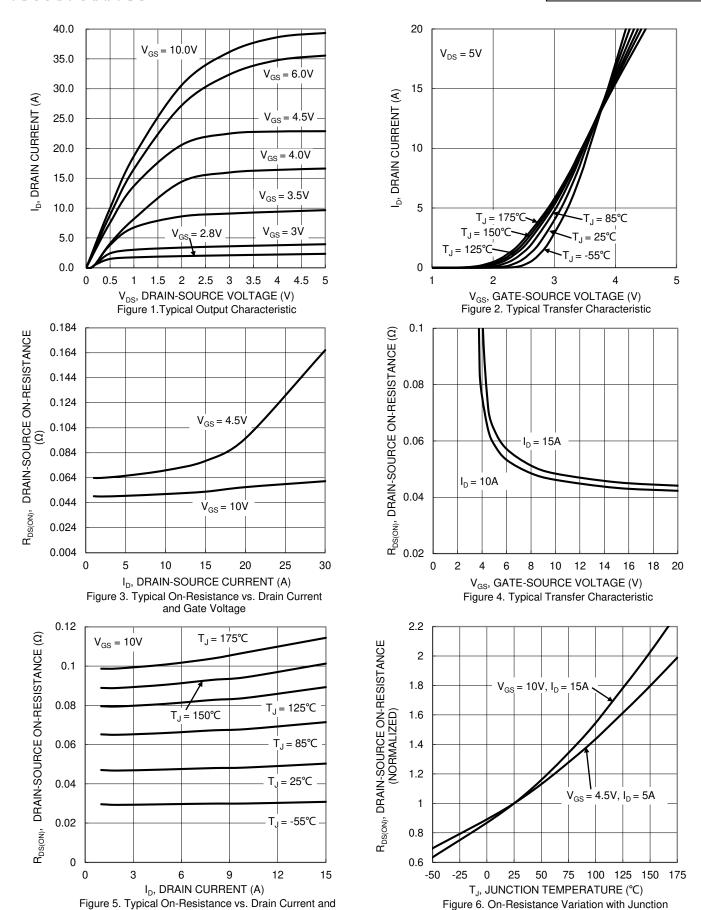
# Electrical Characteristics (@Tc = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	$V_{DS} = 60V$ , $V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	Vgs = ±20V, Vps = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	3	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Daggan	_	53	65	mΩ	VGS = 10V, ID = 15A	
Static Drain-Source On-Nesistance	RDS(ON)	_	68	79	11122	Vgs = 4.5V, ID = 7.5A	
Diode Forward Voltage	$V_{SD}$	_	0.8	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.6A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	466	_		V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	124	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	9.9	_			
Gate Resistance	Rg	_	3.3	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	
Total Gate Charge (VGS = 4.5V)	Qg	_	4.6	_			
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	9.5	_	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 20A	
Gate-Source Charge	$Q_{gs}$	_	1.3	_	110	VDS = 30 V, ID = 20A	
Gate-Drain Charge	$Q_{gd}$	_	2.9	_			
Turn-On Delay Time	td(on)	_	3.3	_		V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V,	
Turn-On Rise Time	t <sub>R</sub>	_	4.6	_	ns		
Turn-Off Delay Time	tD(OFF)	_	12.6	_	115	$R_G = 4.7\Omega$ , $I_D = 20A$	
Turn-Off Fall Time	tF	_	4.3	_			
Body Diode Reverse Recovery Time	trr	_	24	_	ns	IF = 20A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	20	_	nC	I <sub>F</sub> = 20A, di/dt = 100A/µs	

Notes:

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.

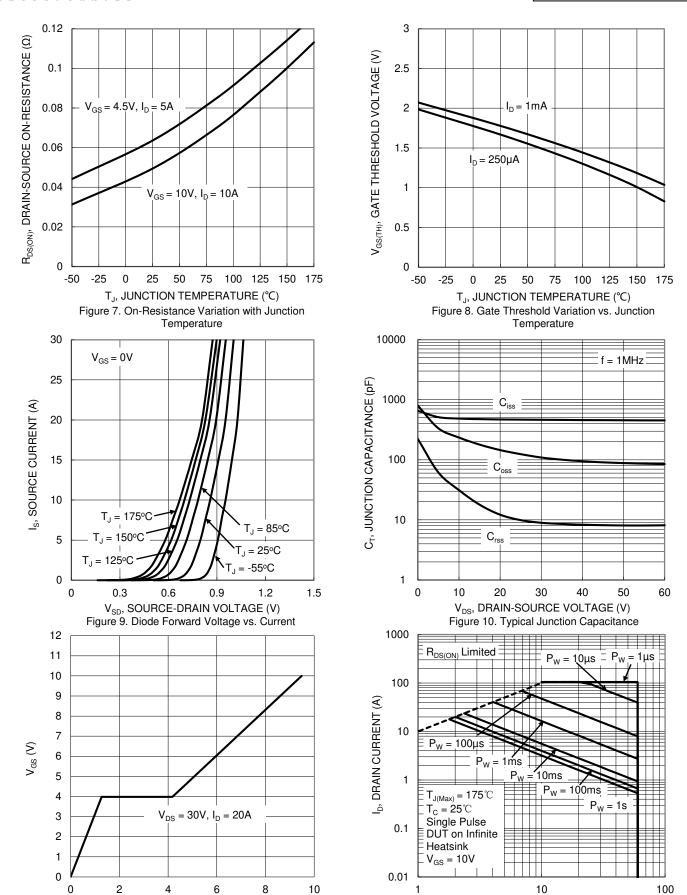




Junction Temperature

Temperature





 $Q_g$  (nC)

Figure 11. Gate Charge

V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area



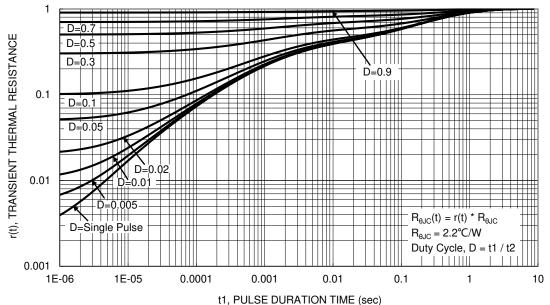
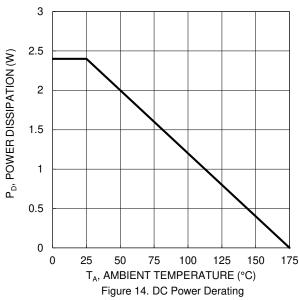


Figure 13. Transient Thermal Resistance



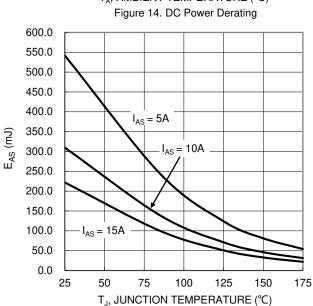
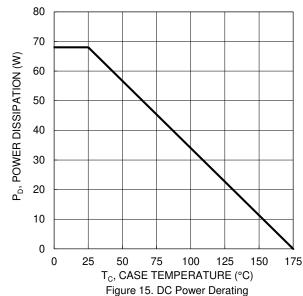


Figure 16. E<sub>AS</sub> vs T<sub>J</sub>

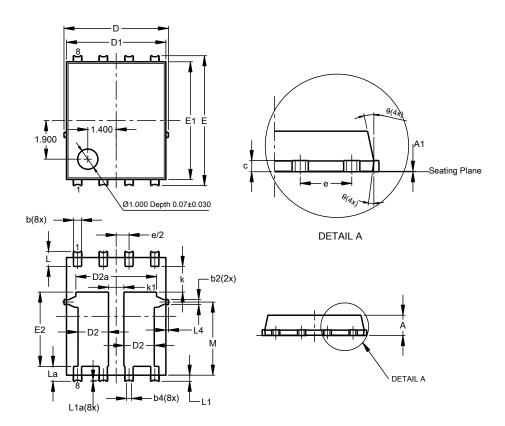




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8 (SWP) (Type R)

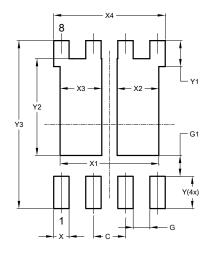


Dawer DIFOCO O (CWD)					
PowerDI5060-8 (SWP) (Type R)					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
<b>A</b> 1	0	0.05	-		
b	0.30	0.50	0.41		
b2	0.20	0.35	0.25		
b4	C	).25REF			
С	0.230	0.330	0.277		
D	5	.15 BS0	)		
D1	4.70	5.10	4.90		
D2	1.40	1.60	1.50		
D2a	3.78 4.18		3.98		
Е	6	.40 BS0	)		
E1	5.60	6.00	5.80		
E2			3.66		
е	1	.27BSC	)		
k	1.05				
k1	0.56				
L	0.635	0.835	0.735		
La	0.635	0.835	0.735		
L1	0.200	0.400	0.300		
L1a	0.050REF				
L4	0.025	0.225	0.125		
М	3.205	4.005	3.605		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8 (SWP) (Type R)



Dimensions	value		
Dillielisions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	3.910		
X2	1.650		
Х3	1.650		
X4	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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