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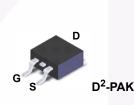
FQB8N90C N-Channel QFET[®] MOSFET 900 V, 6.3 A, 1.9 Ω

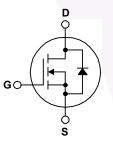
Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supplies.

Features

- 6.3 A, 900 V, $R_{DS(on)}$ = 1.9 Ω (Max.) @ V_{GS} = 10 V
- Low Gate Charge (Typ. 35 nC)
- Low C_{rss} (Typ. 12 pF)
- Fast Switching
- 100% Avalanche Tested
- Improved dv/dt Capability





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter	FQB8N90CTM	Unit
V _{DSS}	Drain-Source Voltage	900	V
I _D	Drain Current - Continuous (T _C = 25°C)	6.3	Α
	- Continuous (T _C = 100°C)	3.8	A
I _{DM}	Drain Current - Pulsed (Note 1)	25	Α
V _{GSS}	Gate-Source Voltage	± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	850	mJ
I _{AR}	Avalanche Current (Note 1)	6.3	A
E _{AR}	Repetitive Avalanche Energy (Note 1)	17.1	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.0	V/ns
PD	Power Dissipation ($T_C = 25^{\circ}C$)	171	W
- Derate Above 25°C		1.37	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds.	300	°C

Thermal Characteristics

Symbol	Parameter	FQB8N90CTM	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.73	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	C/VV

December 2013

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Part Nu	Imber	Top Mark	Package	Packing Method	Reel Size	Taj	oe Width	n Qu	uantity
FQB8N90CTM		FQB8N90C	D ² -PAK	Tape and Reel	330 mm	24 mm		800 untis	
lootri		oractorictica							
Symbol	Cal Characteristics T _C = 25°C unless of Parameter		otherwise noted. Test Conditions		Min.	Тур.	Max.	Unit	
Off Cha	aracteris	stics							
BV _{DSS}	1		ade	V _{GS} = 0 V, I _D = 250 μ	A	900			V
ΔBV _{DSS} / ΔT _J	Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient		-	$I_D = 250 \ \mu\text{A}, \text{ Referenced to } 25^{\circ}\text{C}$			0.95		V/°C
I _{DSS}				V _{DS} = 900 V, V _{GS} = 0	V			10	μA
	Zero Ga	te Voltage Drain Curre	ent	$V_{\rm DS} = 720 \text{ V}, \text{ T}_{\rm C} = 12$				100	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward		orward	$V_{\rm DS} = 720$ V, $V_{\rm DS} = 0$ V				100	nA
I _{GSSR}	Gate-Body Leakage Current, Polward			$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$				-100	nA
	aracteristics				0.0		50	V	
V _{GS(th)}		reshold Voltage	_	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance		V _{GS} = 10 V, I _D = 3.15 A			1.6	1.9	Ω	
9 _{FS}	Forward Transconductance			V _{DS} = 50 V, I _D = 3.15	A		5.5		S
Dynami	ie Cher	atoriation							
C _{iss}	ic Characteristics		N 05 X X 0 X			1600	2080	pF	
C _{oss}				_ V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz			130	170	pF
C _{rss}	Output Capacitance Reverse Transfer Capacitance					12	15	pF	
-155	1000000		·					10	P
Switchi	ing Cha	racteristics						1	
t _{d(on)}	Turn-On Delay Time			V _{DD} = 450 V, I _D = 8 A,			40	90	ns
t _r		Rise Time		V_{GS} = 10 V, R_G = 25 Ω			110	230	ns
t _{d(off)}	Turn-Off	Delay Time					70	150	ns
t _f	Turn-Off	Fall Time			(Note 4)	-	70	150	ns
Qg	Total Ga	al Gate Charge e-Source Charge		$V_{DS} = 720 \text{ V}, \text{ I}_{D} = 8 \text{ A},$ $V_{GS} = 10 \text{ V}$		35	45	nC	
Q _{gs}	Gate-So					10		nC	
Q _{gd}	Gate-Drain Charge		(Note 4)			14		nC	
Drain C		Jinda Characteri	tion and Ma	vinum Batinga					
		Diode Characteris						6.2	۸
I _S		n Pulsed Drain-Source						6.3	A
I _{SM}				$V_{GS} = 0 V, I_S = 6.3 A$				25	A V
V _{SD}		Durce Diode Forward V	ollage	$V_{GS} = 0 V, I_S = 0.3 A$ $V_{GS} = 0 V, I_S = 8 A,$				1.4	-
t _{rr}	Reverse Recovery Time		$V_{GS} = 0 V, I_S = 8 A,$			530		ns	

Q_{rr} Notes:

1. Repetitive rating : pulse-width limited by maximum junction temperature.

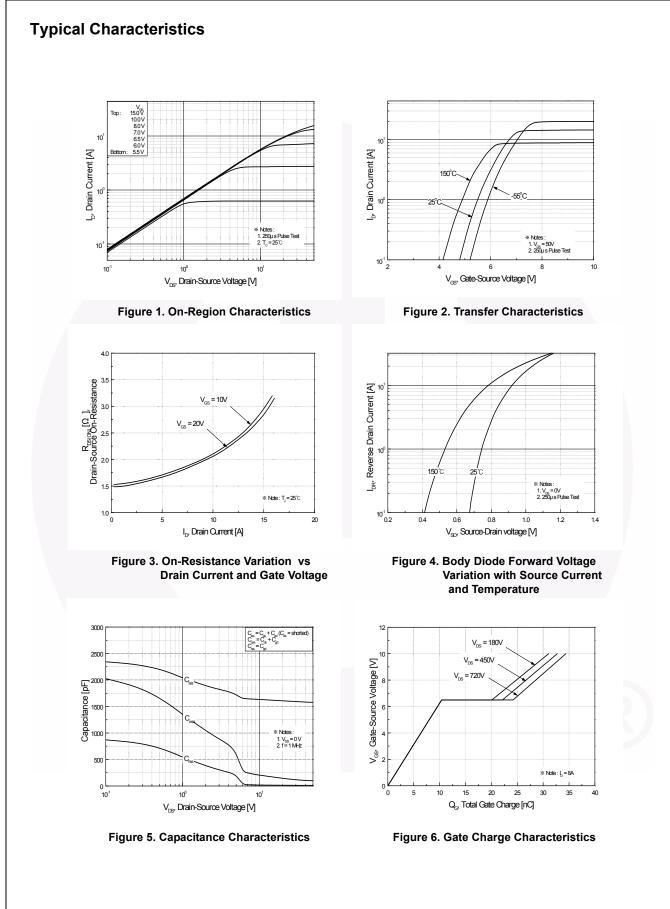
2. L = 40 mH, I_{AS} = 6.3 A, V_{DD} = 50 V, R_G = 25 Ω , starting T_J = 25°C. 3. I_{SD} ≤ 8 A, di/dt ≤ 200 A/µs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C. 4. Essentially independent of operating temperature.

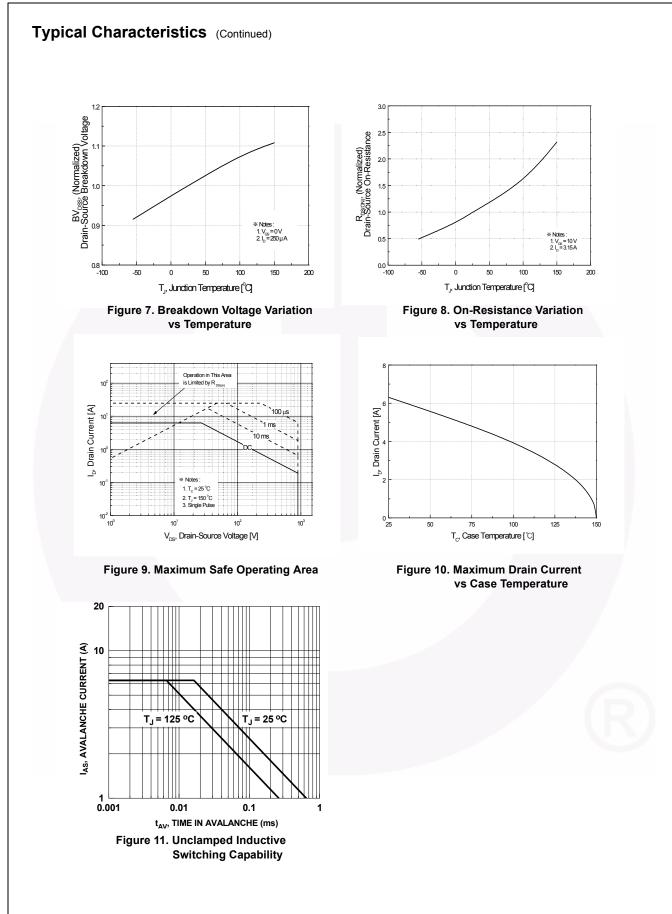
Reverse Recovery Charge

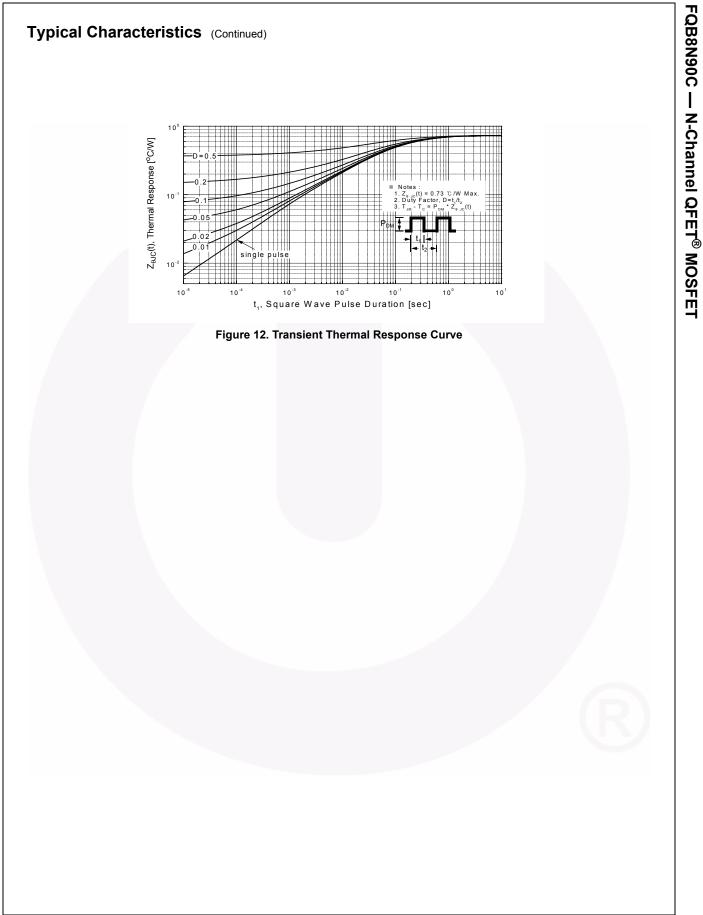
 $dI_F / dt = 100 A/\mu s$

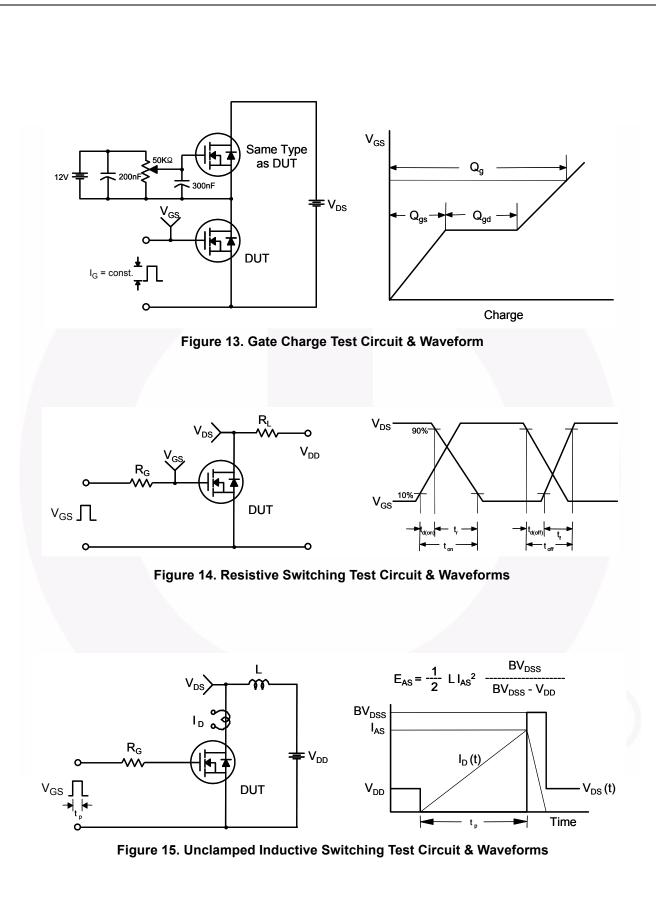
5.8

μC



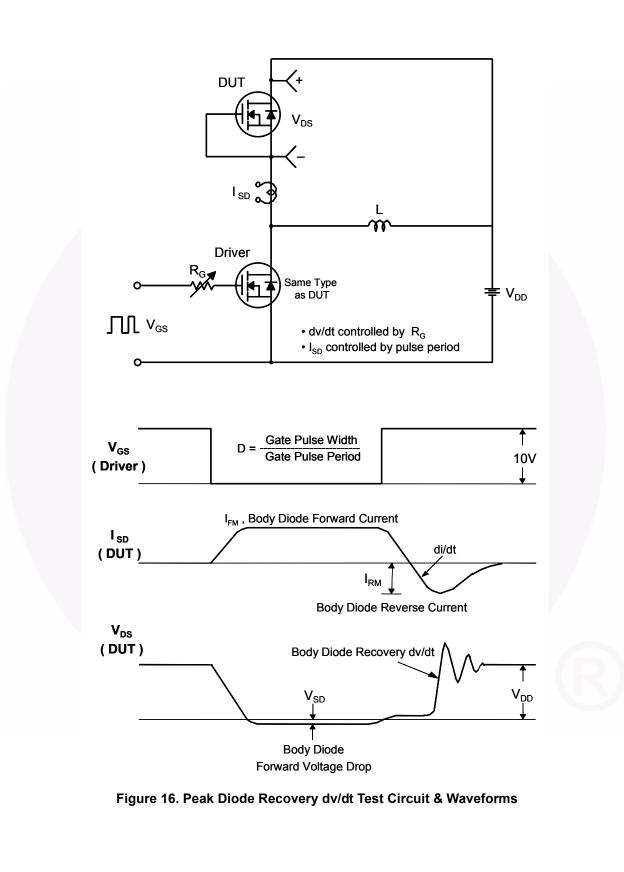






FQB8N90C — N-Channel QFET[®] MOSFET

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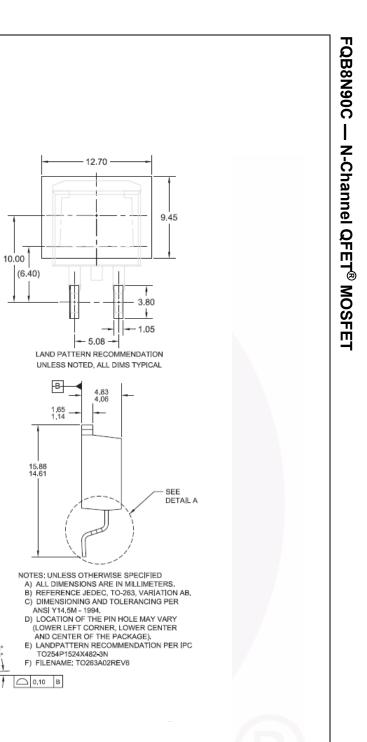


Figure 17. TO263 (D²PAK), Molded, 2-Lead, Surface Mount

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http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TT263-002

Mechanical Dimensions

10.67 9.65

1 2

5,08

6.22 MIN

4

2

3

0.25

SEATING PLANE

0.25 MAX

1

(2.12)

1

3

9.65 8.38

⊕ 0.25 M B AM

6.86 MIN

GAGE PLANE

0.74 0.33

2.79 1,78

(5, 38)

DETAIL A, ROTATED 90°

1.78 MAX

1.78 1.14

0,99

4



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