

FEATURES

- **HIGH ISOLATION VOLTAGE**
BV: 3.75 k Vr.m.s.
- **SOP (SMALL OUT-LINE PACKAGE)**
- **ISOLATED CHANNELS PER EACH PACKAGE**
- **HIGH SPEED SWITCHING**
 $t_r = 3 \mu\text{s TYP}$, $t_f = 5 \mu\text{s TYP}$
- **LOW COLLECTOR DARK CURRENT**
 $I_{CEO} = 5 \text{ nA TYP @ } T_A = 25 \text{ }^\circ\text{C}$, $V_{CE} = 40 \text{ V}$
- **TAPE AND REEL AVAILABLE**

DESCRIPTION

NEC's PS2701-1 is an optically coupled isolator containing a GaAs light emitting diode and a NPN silicon phototransistor. This device is mounted in a plastic SOP (Small Outline Package) for high density applications and has a shield effect to cut off ambient light.

APPLICATIONS

Interface circuit for various instrumentations and control equipment.

- **AC LINE/DIGITAL LOGIC**
- **DIGITAL LOGIC INTERFACE**
- **TWISTED PAIR LINE RECEIVER**
- **TELEPHONE/TELEGRAPH LINE RECEIVER**
- **HIGH FREQUENCY POWER SUPPLY FEEDBACK CONTROL**
- **RELAY CONTACT MONITOR**
- **POWER SUPPLY MONITOR**

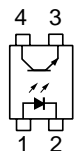
ELECTRICAL CHARACTERISTICS (TA = 25°C)

PART NUMBER			PS2701-1			
SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX	
Diode	V _F	Forward Voltage, I _F = 5 mA	V	1.1	1.4	
	I _R	Reverse Current, V _R = 5 V	μA		5	
	C _t	Terminal Capacitance, V = 0, f = 1.0 MHz	pF		30	
Transistor	I _{CEO}	Collector to Emitter Dark Current, V _{CE} = 40 V, I _F = 0	nA		100	
Coupled	CTR	Current Transfer Ratio ¹ , I _F = 5 mA, V _{CE} = 5 V	%	50	100	300
	V _{CE (sat)}	Collector Saturation Voltage, I _F = 10 mA, I _C = 2 mA	V			0.3
	R _{I-O}	Isolation Resistance, V _{IN-OUT} = 1.0 k Vdc	Ω	10 ¹¹		
	C _{I-O}	Isolation Capacitance, V = 0, f = 1.0 MHz	pF		0.4	
	t _r	Rise Time ² , V _{CC} = 5 V, I _C = 2 mA, R _L = 100 Ω	μs		3	
t _f	Fall Time ² , V _{CC} = 5 V, I _C = 2 mA, R _L = 100 Ω	μs		5		

Notes:

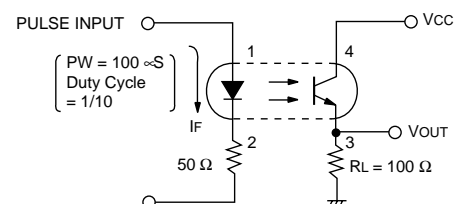
1. CTR rank

L: 100 to 300 %
M: 50 to 150 %
P: 150 to 300%



PS2701-1

2. Test Circuit for Switching Time



ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

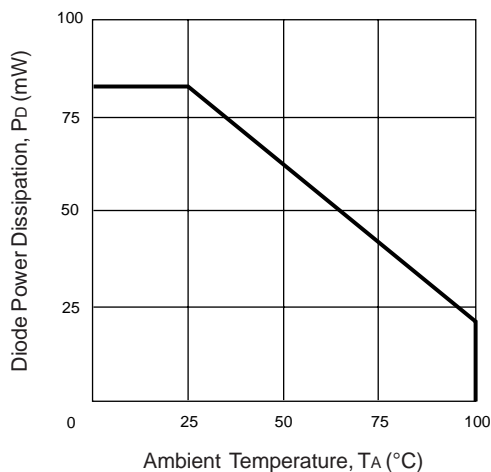
SYMBOLS	PARAMETERS	UNITS	RATINGS
			PS2701-1
Diode			
I _F	Forward Current (DC)	mA	50
V _R	Reverse Voltage	V	6
P _D	Power Dissipation	mW/Ch	80
I _F (PEAK)	Peak Forward Current (PW = 100 μs, Duty Cycle 1%)	A	1
Transistor			
V _{CEO}	Collector to Emitter Voltage (I _C = 1mA, I _B = 0)	V	40
V _{ECO}	Emitter to Collector Voltage (I _E = 100μA, I _B = 0)	V	6
I _C	Collector Current	mA/Ch	80
P _C	Power Dissipation	mW/Ch	150
Coupled			
BV	Isolation Voltage ²	V _{r.m.s.}	3750
T _{STG}	Storage Temperature	°C	-55 to +150
T _A	Operating Ambient Temperature	°C	-55 to +100

Notes:

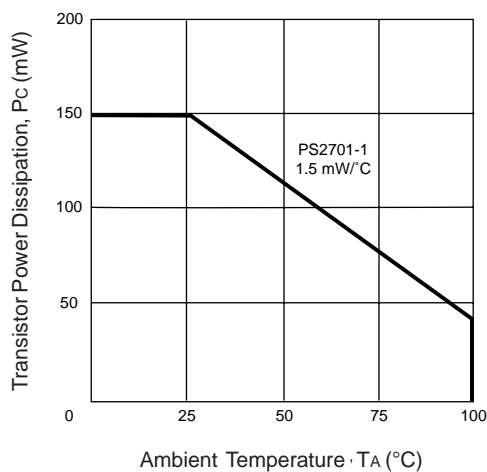
1. Operation in excess of any one of these parameters may result in permanent damage.
2. AC voltage for 1 minute at T_A = 25 °C, RH = 60 % between input and output.

TYPICAL PERFORMANCE CURVES (T_A = 25 °C)

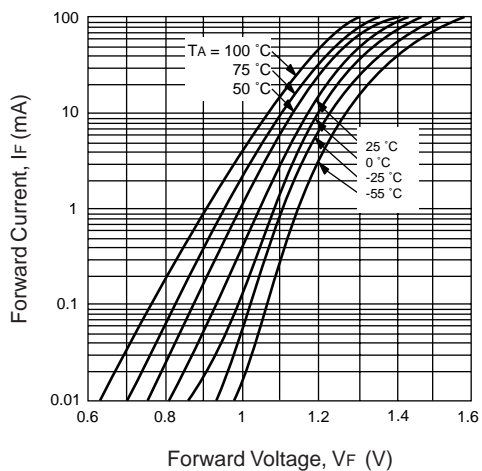
DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



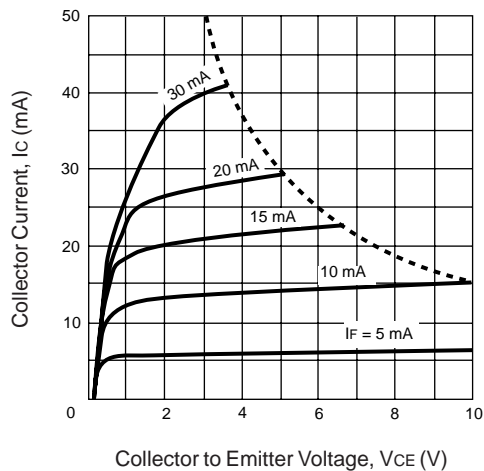
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



FORWARD CURRENT vs. FORWARD VOLTAGE

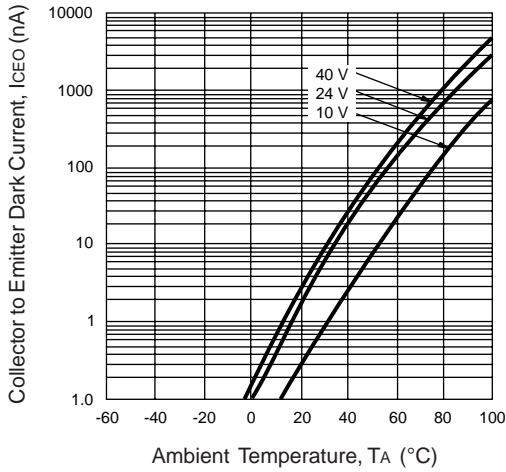


COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

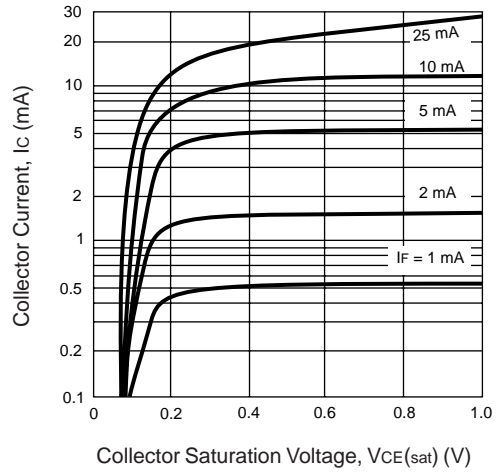


TYPICAL PERFORMANCE CURVES (TA = 25 °C)

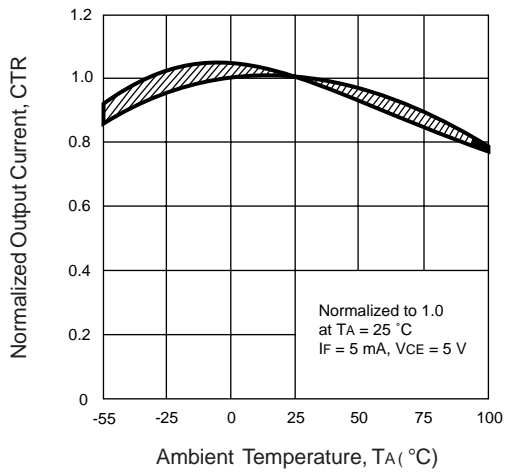
COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



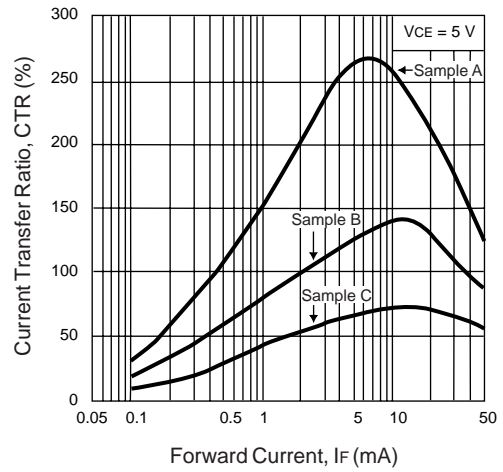
COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



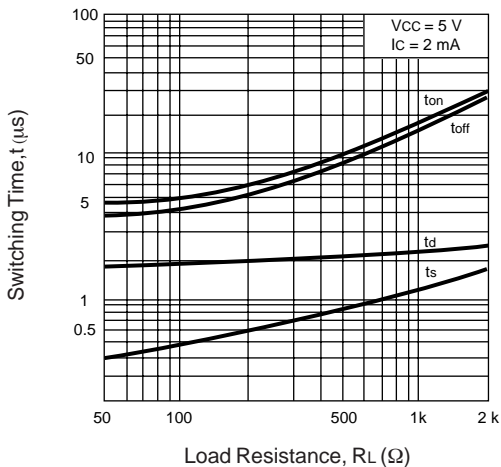
NORMALIZED OUTPUT CURRENT vs. AMBIENT TEMPERATURE



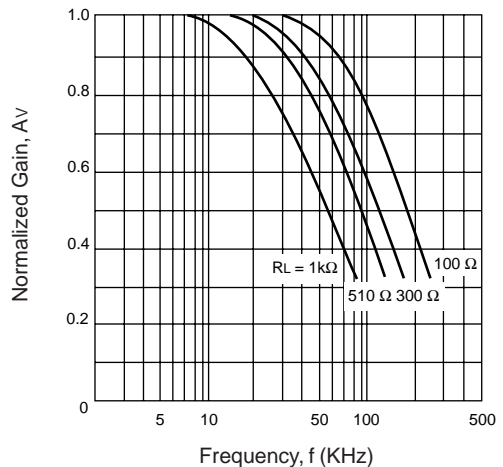
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



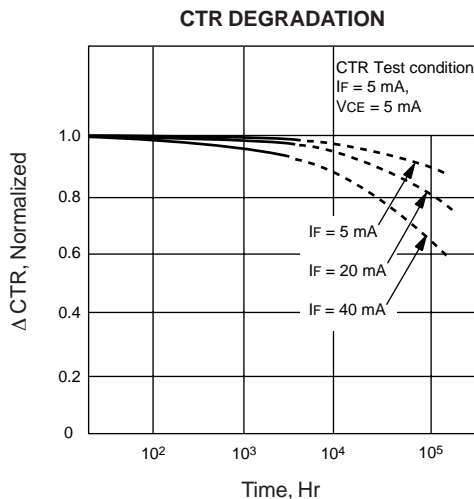
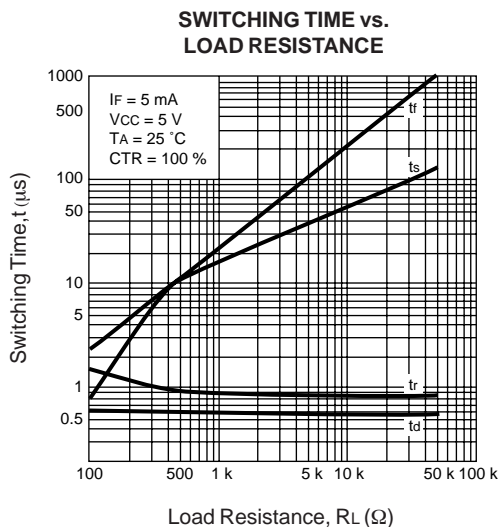
SWITCHING TIME vs. LOAD RESISTANCE



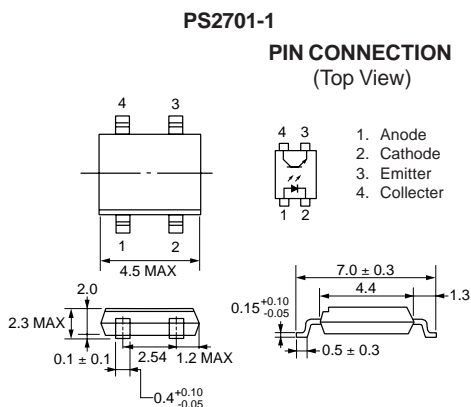
FREQUENCY RESPONSE



TYPICAL PERFORMANCE CURVES ($T_A = 25\text{ }^\circ\text{C}$)



OUTLINE DIMENSIONS (Units in mm)



Life Support Applications

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