"Full Bridge" IGBT MTP (TrenchStop IGBT), 57 A

MTP (Package example)

PRIMARY CHARACTERISTICS					
V _{CES}	1200 V				
I _C at T _C = 25 °C	57 A				
V _{CE(on)}	1.84 V				
Speed	8 kHz to 30 kHz				
Package	MTP				
Circuit configuration	Full bridge				

FEATURES

- Trench and Field Stop IGBT technology
- Positive V_{CE(on)} temperature coefficient
- 10 µs short circuit capability
- HEXFRED® antiparallel diodes with ultrasoft reverse recovery
- Low diode V_F
- Square RBSOA
- Aluminum nitride DBC
- Very low stray inductance design for high speed operation
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Optimized for welding, UPS and SMPS applications
- Rugged with ultrafast performance
- · Outstanding ZVS and hard switching operation
- · Low EMI, requires less snubbing
- Excellent current sharing in parallel operation
- · Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{CES}		1200	V	
Continuous collector current	L.	T _C = 25 °C	57		
Continuous collector current	I _C	T _C = 80 °C	42		
Pulsed collector current	I _{CM}	T _J = 150 °C, t _p = 6 ms, V _{GE} = 15 V	50	А	
Clamped inductive load current	I _{LM}		75	A	
Diode continuous forward current	I _F	T _C = 106 °C	25		
Diode maximum forward current	I _{FM}		100		
Gate to emitter voltage	V _{GE}		± 20	v	
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	v	
Maximum power dissipation (only IGBT)	Б	T _C = 25 °C	240	w	
	P _D	T _C = 80 °C	134	vv	

ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



COMPLIANT







www.vishay.com

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, \text{ I}_{C} = 850 \mu\text{A}$	1200	-	-	V
		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 20 \text{ A}$	-	1.84	2.16	
		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 40 \text{ A}$	-	2.60	-	
Collector to emitter saturation voltage	$V_{CE(on)}$ $V_{GE} = 15 \text{ V}, \text{ I}_{C} = 20 \text{ A}, \text{ T}_{J} = 125 \text{ °C}$ - 2.0	2.06	-	V		
		V_{GE} = 15 V, I _C = 40 A, T _J = 125 °C	-	3.19	-	
		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 20 \text{ A}, \text{ T}_{J} = 150 ^{\circ}\text{C}$	-	2.12	-	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_C = 850 \ \mu A$	4.7	5.8	6.8	
Temperature coefficient of threshold voltage	$V_{GE(th)}/\Delta T_J$	V_{CE} = V_{GE} , I_C = 0.85 mA (25 °C to 125 °C)	-	-12.1	-	mV/°C
Transconductance	9 _{fe}	$V_{CE} = 20 \text{ V}, \text{ I}_{C} = 20 \text{ A}$	-	13	-	S
Zero gate voltage collector current	I _{CES} ⁽¹⁾	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J} = 25 ^{\circ}\text{C}$	-	1.0	200	μA
		$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	0.52	-	
		$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$	-	2.1	-	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = \pm 20 \text{ V}$	-	-	± 250	nA

Note

 $^{(1)}~~I_{CES}$ includes also opposite leg overall leakage

SWITCHING CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I _C = 20 A	-	119	-	nC
Gate to emitter charge (turn-on)	Q _{ge}	V _{CC} = 960 V	-	20	-	
Gate to collector charge (turn-on)	Q _{gc}	V _{GE} = 15 V	-	57	-	
Turn-on switching loss	E _{on}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 20 \text{ A}, \text{ V}_{GE} = 15 \text{ V},$	-	0.75	-	- mJ
Turn-off switching loss	E _{off}	$R_g = 5 \Omega$, L = 1 mH, $T_J = 25 °C$, energy losses include tail and	-	0.66	-	
Total switching loss	E _{tot}	diode reverse recovery	-	1.41	-	
Turn-on switching loss	E _{on}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 20 \text{ A}, \text{ V}_{GE} = 15 \text{ V},$	-	1.08	-	
Turn-off switching loss	E _{off}	$R_g = 5 \Omega$, L = 1 mH, $T_J = 125 °C$, energy losses include tail and	-	1.18	-	
Total switching loss	E _{tot}	diode reverse recovery	-	2.26	-	
Input capacitance	Cies	V _{GE} = 0 V	-	1430	-	
Output capacitance	C _{oes}	$V_{CC} = 30 V$	-	115	-	pF
Reverse transfer capacitance	C _{res}	f = 1.0 MHz	-	75	-	
Reverse bias safe operating area	RBSOA	$ \begin{array}{l} T_J = 150 \ ^{\circ}C, \\ I_C = 75 \ A, \ V_{CC} = 900 \ V, \ V_p = 1200 \ V, \\ R_g = 4.7 \ \Omega, \ V_{GE} = + \ 15 \ V \ to \ 0 \ V, \\ L = 500 \ \mu H \end{array} $	Fullsquare			
Short circuit safe operating area	SCSOA	$ \begin{array}{l} T_J = 150 \ ^\circ C, \\ V_{CC} = 800 \ V, \ V_p = 1200 \ V, \\ R_g = 5 \ \Omega, \ V_{GE} = +15 \ V \ to \ 0 \ V \end{array} $	-	-	10	μs

DIODE SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Diode forward voltage drop		I _C = 20 A	-	2.48	2.94	
		I _C = 40 A	-	3.28	-	v
	V _{FM}	V_{FM} I _C = 20 A, T _J = 125 °C -	-	2.44	-	
		I _C = 40 A, T _J = 125 °C	-	3.45	-	
		I _C = 20 A, T _J = 150 °C	-	2.21	-	
Reverse recovery energy of the diode	E _{rec}	V_{GE} = 15 V, R_{g} = 5 Ω , L = 200 µH	-	420	-	μJ
Diode reverse recovery time	t _{rr}	$V_{CC} = 600 \text{ V}, I_{C} = 20 \text{ A}$	-	98	-	ns
Peak reverse recovery current	l _{rr}	T _J = 125 °C	-	33	-	А

Revision: 26-Jul-2021

2

Document Number: 96725

www.vishay.com

VS-20MT120PFP

Vishay Semiconductors

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Junction and storage tempe range	erature	T _J , T _{Stg}		-40	-	150	°C
Junction to case	IGBT	D		-	-	0.52	
JUNCTION TO Case	Diode	R _{thJC}		-	-	0.61	°C/W
Case to sink per module		R _{thCS}		-	0.06	-	
Clearance			External shortest distance in air between 2 terminals	5.5	-	-	
Creepage			Shortest distance along external surface of the insulating material between 2 terminals	8	-	-	mm
Mounting torque			A mounting compound is recommended and the torque should be checked after 3 hours to allow for the spread of the compound. Lubricated threads.	r 3 ± 10 %		Nm	
Weight					66		g

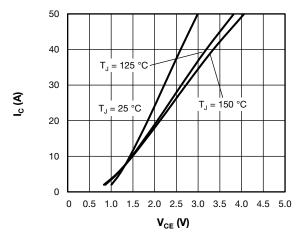


Fig. 1 - Typical Trench IGBT Output Characteristics, V_{GE} = 15 V

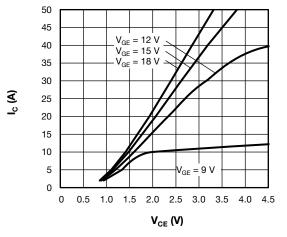


Fig. 2 - Typical Trench IGBT Output Characteristics, T_J = 125 °C

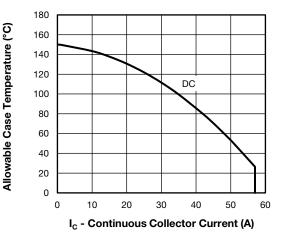
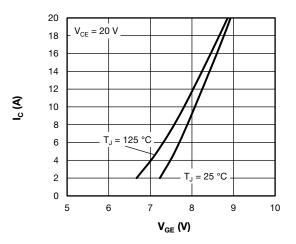


Fig. 3 - Maximum Trench IGBT Continuous Collector Current vs. Case Temperature

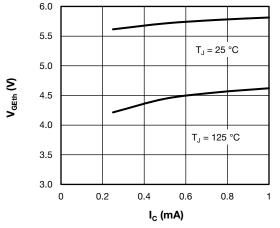




Revision: 26-Jul-2021

3

Document Number: 96725



www.vishay.com

Fig. 5 - Typical Trench IGBT Gate Threshold Voltage

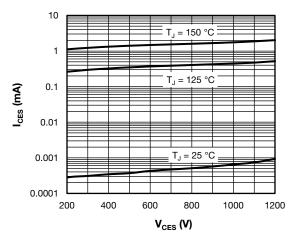
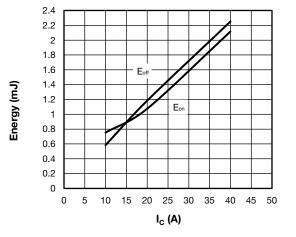
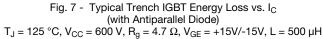


Fig. 6 - Typical Trench IGBT Zero Gate Voltage Collector Current





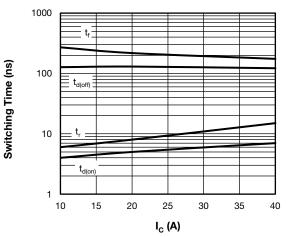


Fig. 8 - Typical Trench IGBT Switching Time vs. I_C (with Antiparallel Diode) T_J = 125 °C, V_{CC} = 600 V, R_g = 4.7 Ω , V_{GE} = +15V/-15V, L = 500 μ H

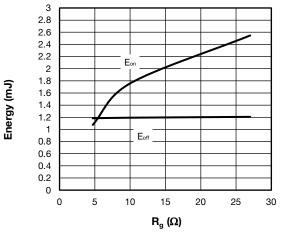


Fig. 9 - Typical Trench IGBT Energy Loss vs. R_g (with Antiparallel Diode) T_J = 125 °C, V_{CC} = 600 V, I_C = 20 A, V_{GE} = +15V/-15V, L = 500 µH

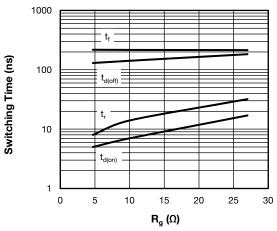


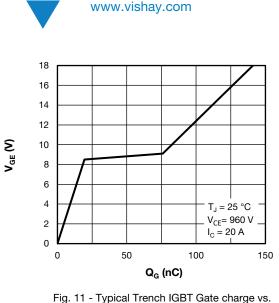
Fig. 10 - Typical Trench IGBT Switching Time vs. R_g (with Antiparallel Diode) T_J = 125 °C, V_{CC} = 600 V, I_C = 20 A, V_{GE} = +15V/-15V, L = 500 μH

Revision: 26-Jul-2021

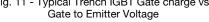
4

Document Number: 96725





SHAY



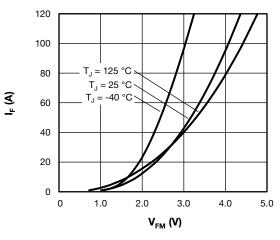
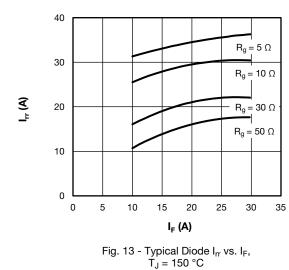


Fig. 12 - Typical Diode Forward Characteristics $t_p = 80 \ \mu s$



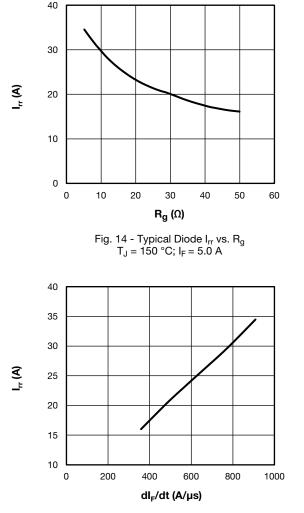


Fig. 15 - Typical Diode I $_{rr}$ vs. dI $_{F}/dt$ V_{CC} = 400 V; V_{GE} = 15 V; I $_{CE}$ = 5.0 A; T_J = 150 $^{\circ}C$

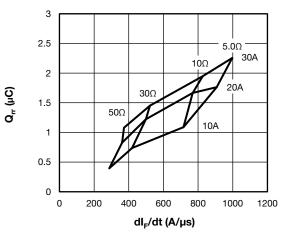
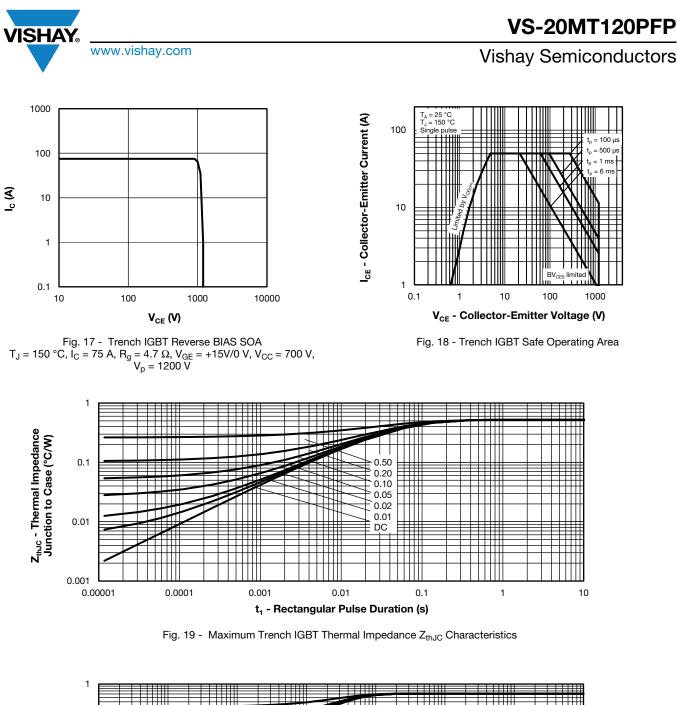


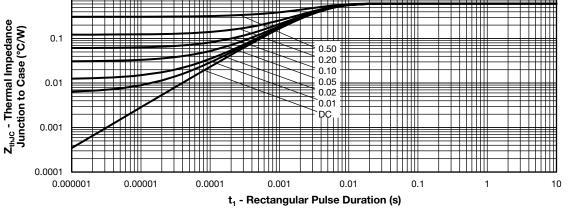
Fig. 16 - Typical Diode Q_{rr} vs. dI_F/dt V_{CC} = 400 V; V_{GE} = 15 V; T_J = 150 $^\circ C$

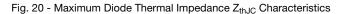
Revision: 26-Jul-2021

5

Document Number: 96725







Revision: 26-Jul-2021
6
Document Number: 96725

For technical questions within your region: DiodesAsia@vishay.com, DiodesAsia@vishay.com, DiodesAmericas@vishay.com, <a href="DiodesAmericas@vishay.com"/DiodesAmericas@vish





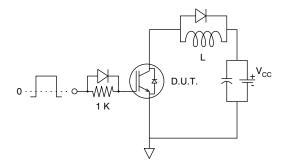


Fig. 21 - Gate Charge Circuit (Turn-Off)

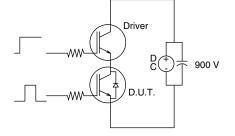


Fig. 23 - S.C. SOA Circuit

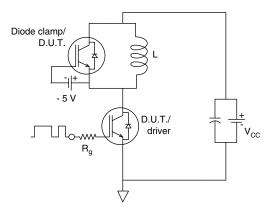


Fig. 24 - Switching Loss Circuit

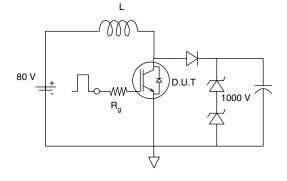
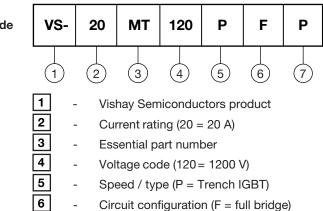


Fig. 22 - RBSOA Circuit

ORDERING INFORMATION TABLE

Device code



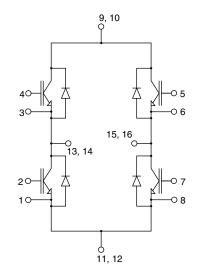
P = lead (Pb)-free

7





CIRCUIT CONFIGURATION

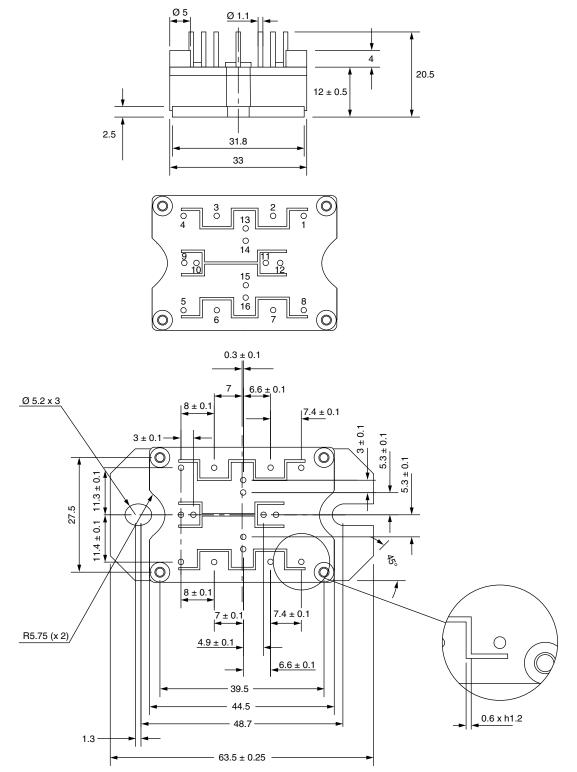


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95245			

MTP MOSFET/IGBT Full-Bridge

DIMENSIONS in millimeters

VISHAY





Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.