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Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HAT2143H

Silicon N Channel Power MOS FET
Power Switching

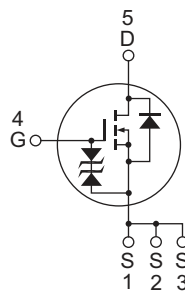
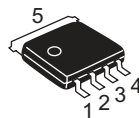
REJ03G1195-0300
(Previous: ADE-208-1584A)
Rev.3.00
Sep 07, 2005

Features

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
 $R_{DS(on)} = 4.9 \text{ m}\Omega$ typ. (at $V_{GS} = 10 \text{ V}$)

Outline

RENESAS Package code: PTZZ0005DA-A
(Package name: LFAK)



1, 2, 3 Source
4 Gate
5 Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V _{DS}	30	V
Gate to source voltage	V _{GS}	±20	V
Drain current	I _D	40	A
Drain peak current	I _{D (pulse)} ^{Note 1}	160	A
Body-drain diode reverse drain current	I _{DR}	40	A
Avalanche current	I _{AP} ^{Note 3}	16	A
Avalanche energy	E _{AR} ^{Note 3}	25	mJ
Channel dissipation	P _{ch} ^{Note 2}	20	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

- Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
 2. T_c = 25 °C
 3. Value at T_{ch} = 25°C, R_g ≥ 50 Ω

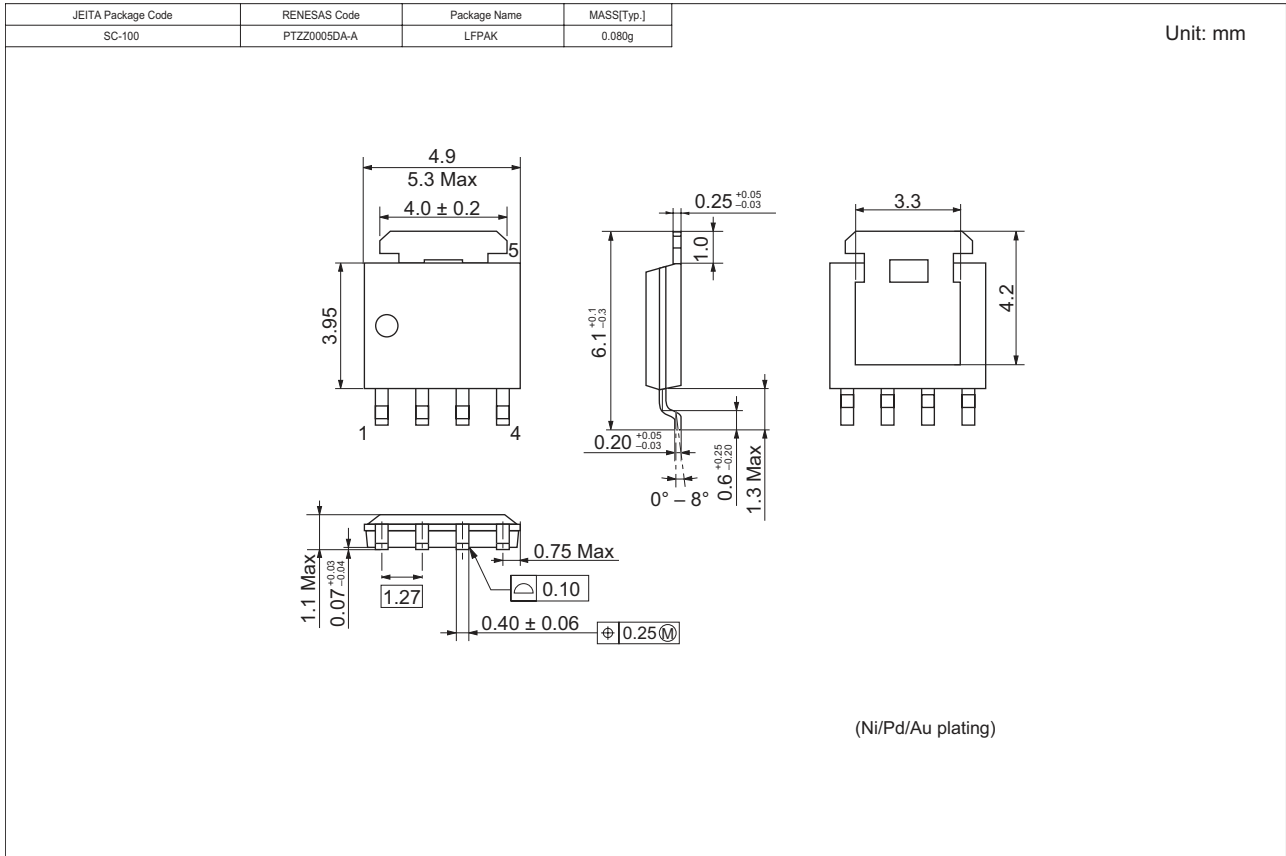
Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	30	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR) GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 30 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS (off)}	1.0	—	2.5	V	V _{DS} = 10 V, I _D = 1 mA
Static drain to source on state resistance	R _{DS (on)}	—	4.9	6.1	mΩ	I _D = 20 A, V _{GS} = 10 V ^{Note 4}
	R _{DS (on)}	—	7.9	11.5	mΩ	I _D = 20 A, V _{GS} = 4.5 V ^{Note 4}
Forward transfer admittance	y _{fs}	30	50	—	S	I _D = 20 A, V _{DS} = 10 V ^{Note 4}
Input capacitance	C _{iss}	—	2450	—	pF	V _{DS} = 10 V
Output capacitance	C _{oss}	—	540	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	280	—	pF	f = 1 MHz
Total gate charge	Q _g	—	40	—	nC	V _{DD} = 10 V
Gate to source charge	Q _{gs}	—	8	—	nC	V _{GS} = 10 V
Gate to drain charge	Q _{gd}	—	7	—	nC	I _D = 40 A
Turn-on delay time	t _{d (on)}	—	20	—	ns	V _{GS} = 10 V, I _D = 20 A
Rise time	t _r	—	56	—	ns	V _{DD} ≅ 10 V
Turn-off delay time	t _{d (off)}	—	76	—	ns	R _L = 0.5 Ω
Fall time	t _f	—	15	—	ns	R _g = 4.7 Ω
Body-drain diode forward voltage	V _{DF}	—	0.85	1.11	V	I _F = 40 A, V _{GS} = 0 ^{Note 4}
Body-drain diode reverse recovery time	t _{rr}	—	60	—	ns	I _F = 40 A, V _{GS} = 0 di _F /dt = 50 A/μs

Note: 4. Pulse test

Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2143H-EL-E	2500 pcs	Taping

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Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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