Low-power 2-input NAND Schmitt trigger Rev. 5 — 29 June 2012

Product data sheet

General description 1.

The 74AUP1G132 provides the single 2-input NAND Schmitt trigger function which accept standard input signals. They are capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

The inputs switch at different points for positive and negative-going signals. The difference between the positive voltage V_{T+} and the negative voltage V_{T-} is defined as the input hysteresis voltage V_H.

Features and benefits 2.

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

Applications 3.

- Wave and pulse shaper
- Astable multivibrator
- Monostable multivibrator.



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Ordering information 4.

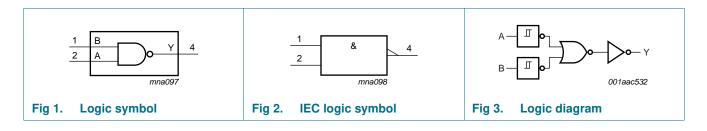
Table 1. Ordering	g information			
Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G132GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G132GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886
74AUP1G132GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1 \times 0.5 mm	SOT891
74AUP1G132GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115
74AUP1G132GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202
74AUP1G132GX	–40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226

5. Marking

Table 2. Marking	
Type number	Marking code ^[1]
74AUP1G132GW	aE
74AUP1G132GM	aE
74AUP1G132GF	aE
74AUP1G132GN	aE
74AUP1G132GS	aE
74AUP1G132GX	aE

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

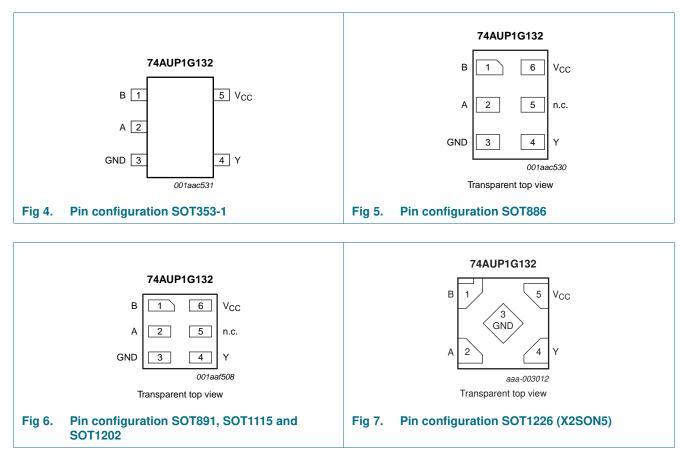
Functional diagram 6.



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7. Pinning information

7.1 Pinning



7.2 Pin description

Table 3.	Pin descr	iption		
Symbol	Pi	'n		Description
	т	SSOP5 and X2SON5	XSON6	
В	1		1	data input
Α	2		2	data input
GND	3		3	ground (0 V)
Y	4		4	data output
n.c.	-		5	not connected
V _{CC}	5		6	supply voltage

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8. Functional description

	Function table ^[1]		
Input			Output
Α		В	Y
L		L	Н
L		Н	Н
Н		L	Н
Н		Н	L

[1] H = HIGH voltage level; L = LOW voltage level.

9. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage		<u>1</u> –0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode	<u>1</u> –0.5	+4.6	V
lo	output current	$V_{O} = 0 V$ to V_{CC}	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$	[2] -	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For TSSOP5 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.
 For XSON6 and X2SON5 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

10. Recommended operating conditions

Table 6.	Recommended operating co	nditions			
Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V_{CC}	V
		Power-down mode; $V_{CC} = 0 V$	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C

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11. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Uni
T _{amb} = 2	5 °C					
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_O = $-20~\mu\text{A};~V_{CC}$ = 0.8 V to 3.6 V	$V_{CC}-0.1$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75 imes V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	۷
/ _{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		$I_{O} = 20 \ \mu A; V_{CC} = 0.8 \ V \text{ to } 3.6 \ V$	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		$I_0 = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.31	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.31	V
		$I_{\rm O} = 3.1 \text{ mA}; V_{\rm CC} = 2.3 \text{ V}$	-	-	0.44	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V
1	input leakage current	$V_{I} = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.1	μA
OFF	power-off leakage current	$V_{\rm I}$ or $V_{\rm O} = 0$ V to 3.6 V; $V_{\rm CC} = 0$ V	-	-	±0.2	μ Α
∆I _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μΑ
СС	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
7l ^{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	<u>[1]</u> -	-	40	μA
Ci	input capacitance	$V_{I} = GND \text{ or } V_{CC}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	1.1	-	pF
C _O	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.7	-	pF
	40 °C to +85 °C					•
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
011		$I_{\rm O} = -20 \ \mu \text{A}; \ V_{\rm CC} = 0.8 \ \text{V} \text{ to } 3.6 \ \text{V}$	V _{CC} – 0.1	-	-	V
		$I_{\rm O} = -1.1 \text{ mA}; V_{\rm CC} = 1.1 \text{ V}$	$0.7 \times V_{CC}$	-	-	V
		$I_{\rm O} = -1.7 \text{ mA}; V_{\rm CC} = 1.4 \text{ V}$	1.03	-	-	V
		$I_{\rm O} = -1.9$ mA; $V_{\rm CC} = 1.65$ V	1.30	-	-	V
		$I_{\rm O} = -2.3 \text{ mA}; V_{\rm CC} = 2.3 \text{ V}$	1.97	-	-	v
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	v
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	_	-	v
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	_	-	v
			2.00			v

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Symbol	Parameter	Conditions	Min	Тур	Max	Uni
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_O = 20 $\mu A;V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		$I_{O} = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3 \times V_{\text{CC}}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	٧
I	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V; } V_{CC} = 0 \text{ V}$	-	-	±0.5	μA
ΔI_{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μA
I _{CC}	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC}; \ I_{O} = 0 \ A; \\ V_{CC} = 0.8 \ V \text{ to } 3.6 \ V \end{array}$	-	-	0.9	μA
∆l _{CC}	additional supply current		[1] -	-	50	μA
T _{amb} = –	40 °C to +125 °C					
V _{ОН}	HIGH-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_{O} = –20 $\mu\text{A};V_{CC}$ = 0.8 V to 3.6 V	$V_{CC} - 0.11$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.6\times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.17	-	-	٧
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	٧
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	٧
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = 20 μ A; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	٧
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.33 \times V_{CC}$	٧
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	٧
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.50	V
lı	input leakage current	$V_{\rm I} = {\rm GND} \text{ to } 3.6 \text{ V}; V_{\rm CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μA
I _{OFF}	power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.75	μΑ

Table 7. Static characteristics ... continued

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At recom	mended operating condition	s; voltages are referenced to GND	pltages are referenced to GND (ground = $0 V$).					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
ΔI_{OFF}	additional power-off leakage current	$V_1 \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA		
I _{CC}	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC}; \ I_{O} = 0 \ \text{A}; \\ V_{CC} = 0.8 \ \text{V to } 3.6 \ \text{V} \end{array}$	-	-	1.4	μ A		
ΔI_{CC}	additional supply current		<u>[1]</u> -	-	75	μA		

Table 7. Static characteristics ...continued

[1] One input at V_{CC} – 0.6 V, other input at V_{CC} or GND.

12. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V; for test circuit see Figure 9.

Symbol	Parameter	Conditions			25 °C		-40	°C to +1	25 °C	Unit
				Min	Typ <mark>[1]</mark>	Max	Min	Max (85 °C)	Max (125 °C)	
C _L = 5 pl	F									
t _{pd}	propagation delay	A or B to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 V$		-	22.5	-	-	-	-	ns
		$V_{CC} = 1.1 V \text{ to } 1.3 V$		2.6	6.3	13.4	2.4	15.1	16.6	ns
		$V_{CC} = 1.4 \text{ V}$ to 1.6 V		2.2	4.6	8.2	1.9	9.7	10.7	ns
		$V_{CC} = 1.65 \text{ V}$ to 1.95 V		1.9	3.9	6.6	1.7	7.9	8.7	ns
		$V_{CC} = 2.3 \text{ V} \text{ to } 2.7 \text{ V}$		1.7	3.2	5.3	1.5	6.2	6.8	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		1.6	2.9	4.7	1.4	5.6	6.2	ns
C _L = 10	pF									
pd	propagation delay	A or B to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 V$		-	26.1	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.0	7.2	15.4	2.7	17.3	19.0	ns
		$V_{CC} = 1.4 \text{ V}$ to 1.6 V		2.5	5.2	9.3	2.2	11.0	12.1	ns
		V _{CC} = 1.65 V to 1.95 V		2.3	4.5	7.5	2.0	9.0	9.9	ns
		$V_{CC} = 2.3 \text{ V} \text{ to } 2.7 \text{ V}$		2.1	3.8	6.1	1.8	7.2	7.9	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		2.0	3.5	5.5	1.8	6.5	7.2	ns
C _L = 15 p	pF									
^l pd	propagation delay	A or B to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 V$		-	29.6	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.3	8.0	17.2	3.0	19.4	21.3	ns
		$V_{CC} = 1.4 V \text{ to } 1.6 V$		2.8	5.8	10.4	2.5	12.3	13.5	ns
		V _{CC} = 1.65 V to 1.95 V		2.6	5.0	8.3	2.3	10.0	11.0	ns
		V_{CC} = 2.3 V to 2.7 V		2.3	4.2	6.7	2.1	7.9	8.7	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		2.2	3.9	6.1	2.0	7.3	8.0	ns

$C_L = 30 \text{ pF}$

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Symbol	Parameter	Conditions			25 °C		-40) °C to +1	25 °C	Unit
			-	Min	Typ[1]	Мах	Min	Max (85 °C)	Max (125 °C)	
t _{pd}	propagation delay	A or B to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 V$		-	39.9	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V		4.3	10.2	22.6	3.8	25.4	27.9	ns
		V _{CC} = 1.4 V to 1.6 V		3.6	7.3	13.3	3.2	15.8	17.4	ns
		V _{CC} = 1.65 V to 1.95 V		3.2	6.3	10.6	2.9	12.8	14.1	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		3.0	5.3	8.5	2.7	10.1	11.1	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		2.8	5.0	7.8	2.7	9.2	10.1	ns
C _L = 5 p	F, 10 pF, 15 pF and 3	30 pF								
C _{PD}	power dissipation capacitance	$f_i = 1 \text{ MHz};$ V _I = GND to V _{CC}	<u>[3]</u>							
		$V_{CC} = 0.8 V$		-	2.6	-	-	-	-	pF
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		-	2.9	-	-	-	-	pF
		$V_{CC} = 1.4 \text{ V}$ to 1.6 V		-	3.0	-	-	-	-	pF
		$V_{CC} = 1.65 \text{ V}$ to 1.95 V		-	3.2	-	-	-	-	pF
		V_{CC} = 2.3 V to 2.7 V		-	3.8	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V		-	4.4	-	-	-	-	pF

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V; for test circuit see Figure 9.

[1] All typical values are measured at nominal V_{CC} .

$\label{eq:tpd} [2] \quad t_{pd} \text{ is the same as } t_{PLH} \text{ and } t_{PHL}.$

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} \times N + \Sigma(C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$

 f_i = input frequency in MHz;

 $f_o = output frequency in MHz;$

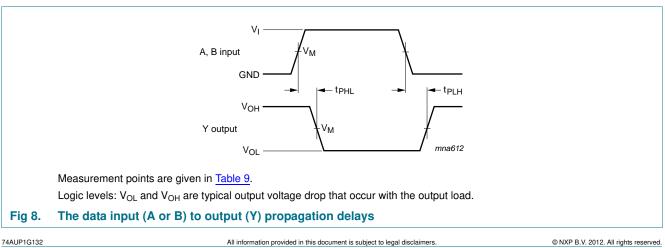
 C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}{}^2 \times f_{o})$ = sum of the outputs.

13. Waveforms



NXP Semiconductors

74AUP1G132

Low-power 2-input NAND Schmitt trigger

Supply voltage	Output	Input		
V _{cc}	V _M	V _M	VI	t _r = t _f
0.8 V to 3.6 V	$0.5 imes V_{CC}$	$0.5 imes V_{CC}$	V _{CC}	≤ 3.0 ns

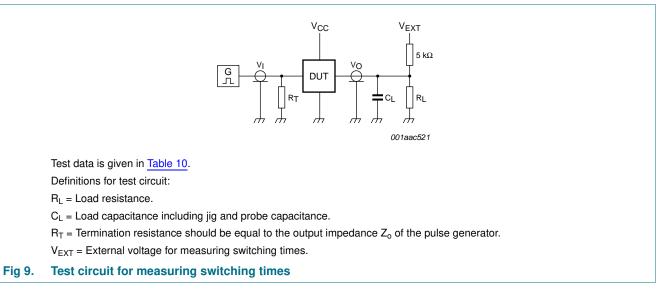


Table 10. Test data

Supply voltage	Load	V _{EXT}			
V _{CC}	CL	R _L []]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 M Ω	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times $R_L = 5 k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

Low-power 2-input NAND Schmitt trigger

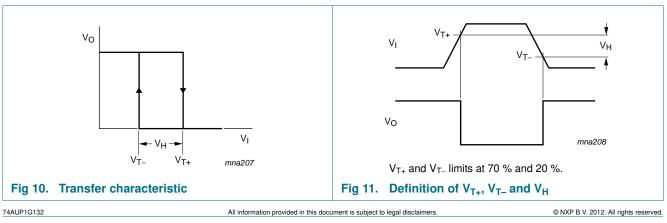
14. Transfer characteristics

Table 11. Transfer characteristics

Voltages are referenced to GND (ground = 0 V; for test circuit see Figure 9.

Symbol	Parameter	Conditions		25 °C			–40 °C to +125 °C		
				Тур	Мах	Min	Max (85 °C)	Max (125 °C)	
V _{T+} positive-going threshold voltage		see <u>Figure 10</u> and Figure 11							
		$V_{CC} = 0.8 V$	0.30	-	0.60	0.30	0.60	0.62	V
		$V_{CC} = 1.1 V$	0.53	-	0.90	0.53	0.90	0.92	V
		$V_{CC} = 1.4 V$	0.74	-	1.11	0.74	1.11	1.13	V
		$V_{CC} = 1.65 V$	0.91	-	1.29	0.91	1.29	1.31	V
		$V_{CC} = 2.3 V$	1.37	-	1.77	1.37	1.77	1.80	V
		$V_{CC} = 3.0 V$	1.88	-	2.29	1.88	2.29	2.32	V
V _{T-} negative-going threshold voltage	see <u>Figure 10</u> and <u>Figure 11</u>								
		$V_{CC} = 0.8 V$	0.10	-	0.60	0.10	0.60	0.60	V
		$V_{CC} = 1.1 V$	0.26	-	0.65	0.26	0.65	0.65	V
	$V_{CC} = 1.4 V$	0.39	-	0.75	0.39	0.75	0.75	V	
		V _{CC} = 1.65 V	0.47	-	0.84	0.47	0.84	0.84	V
		$V_{CC} = 2.3 V$	0.69	-	1.04	0.69	1.04	1.04	V
	$V_{CC} = 3.0 V$	0.88	-	1.24	0.88	1.24	1.24	V	
V _H hysteresis voltage		$(V_{T_{+}} - V_{T_{-}})$; see <u>Figure 10</u> , <u>Figure 11</u> , <u>Figure 12</u> and <u>Figure 13</u>							
		$V_{CC} = 0.8 V$	0.07	-	0.50	0.07	0.50	0.50	V
	V _{CC} = 1.1 V	0.08	-	0.46	0.08	0.46	0.46	V	
	$V_{CC} = 1.4 V$	0.18	-	0.56	0.18	0.56	0.56	V	
		V _{CC} = 1.65 V	0.27	-	0.66	0.27	0.66	0.66	V
		V _{CC} = 2.3 V	0.53	-	0.92	0.53	0.92	0.92	V
		$V_{CC} = 3.0 V$	0.79	-	1.31	0.79	1.31	1.31	V

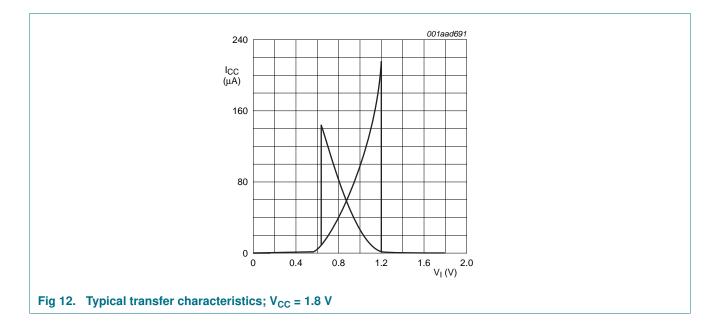
15. Waveforms transfer characteristics

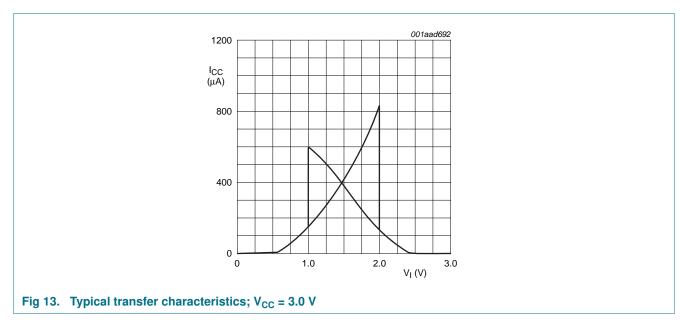


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16. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC} \text{ where:}$

 P_{add} = additional power dissipation (μ W);

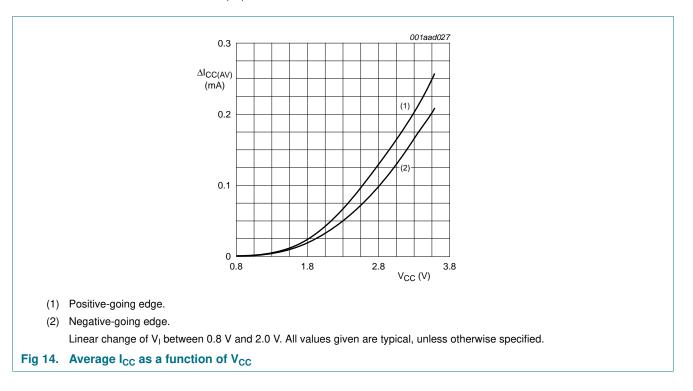
 $f_i = input frequency (MHz);$

 t_r = input rise time (ns); 10 % to 90 %;

 t_f = input fall time (ns); 90 % to 10 %;

 $\Delta I_{CC(AV)}$ = average additional supply current (µA).

Average $\Delta I_{CC(AV)}$ differs with positive or negative input transitions, as shown in Figure 14.



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17. Package outline

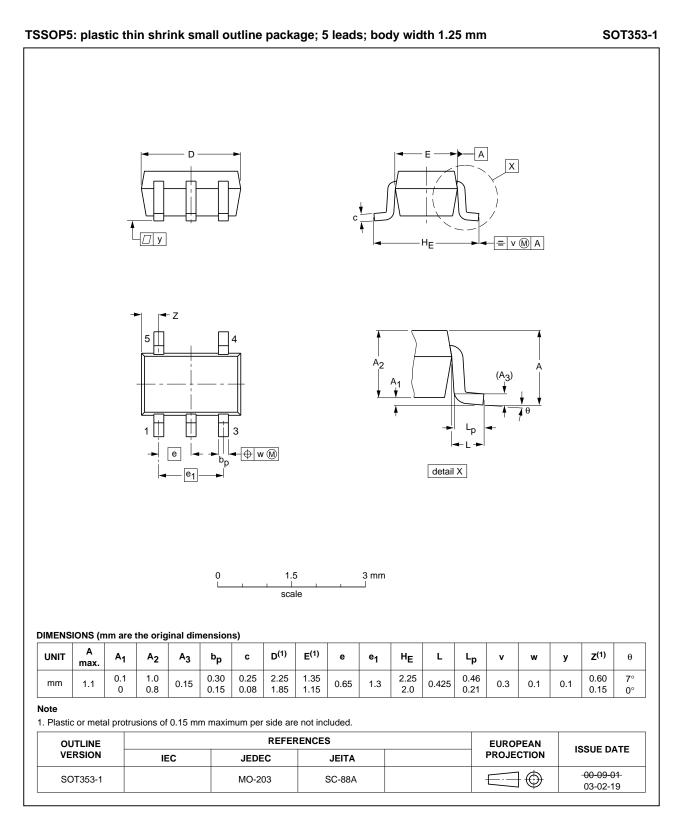


Fig 15. Package outline SOT353-1 (TSSOP5)

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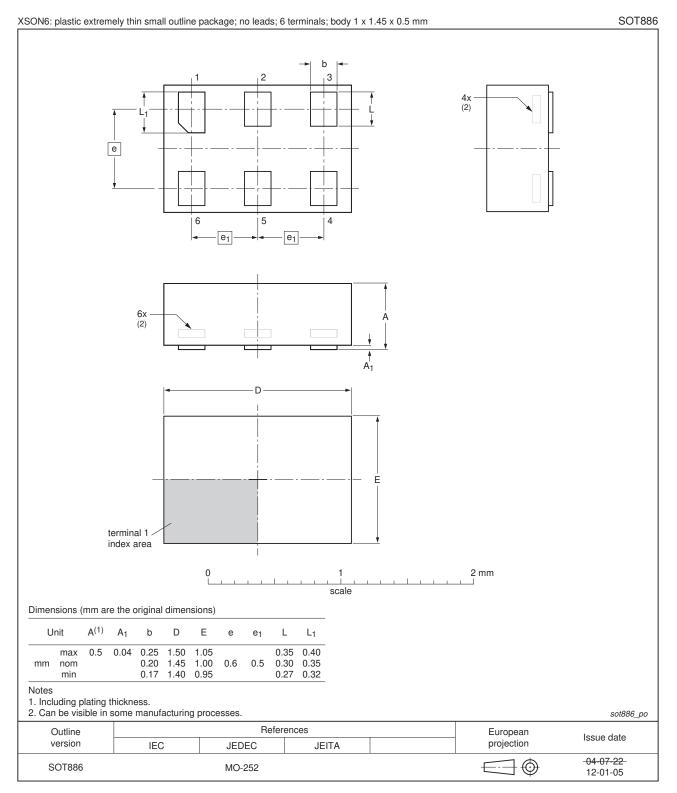


Fig 16. Package outline SOT886 (XSON6)

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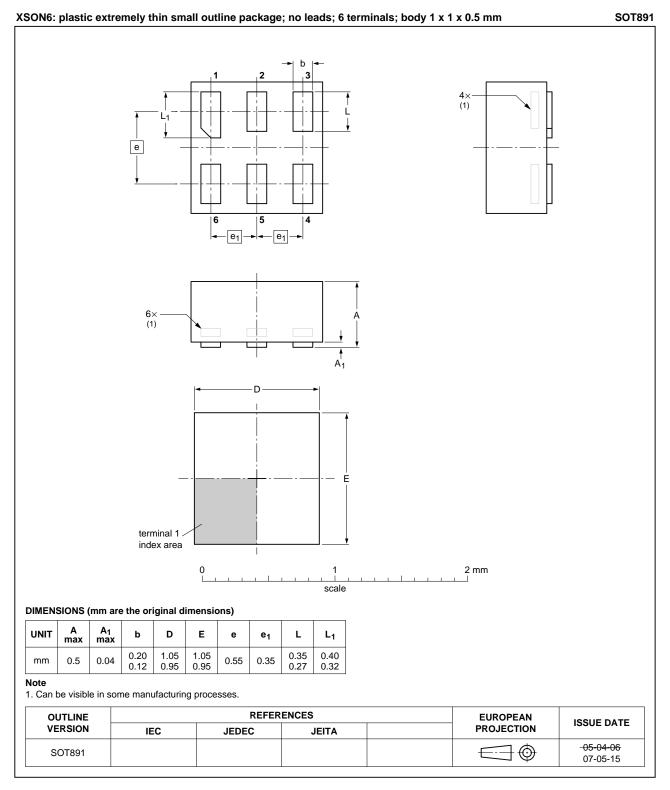
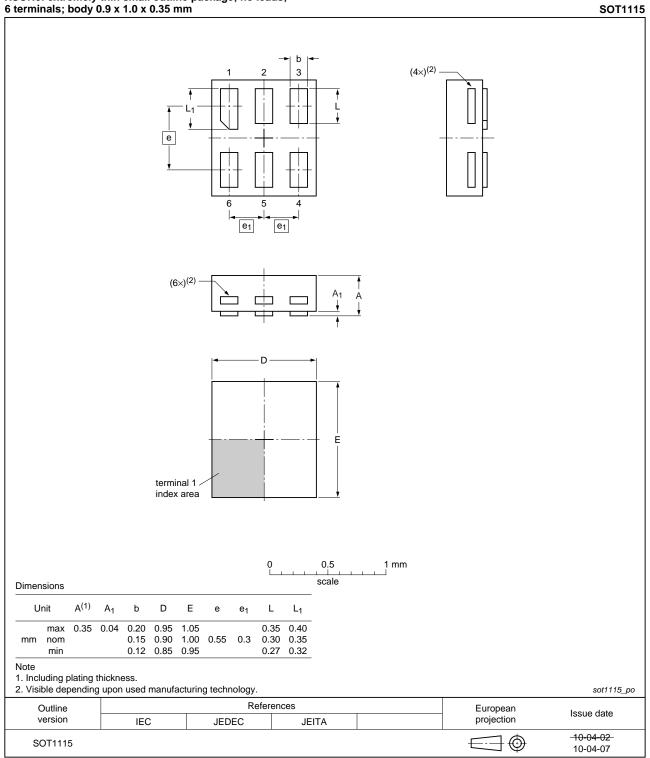


Fig 17. Package outline SOT891 (XSON6)

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XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

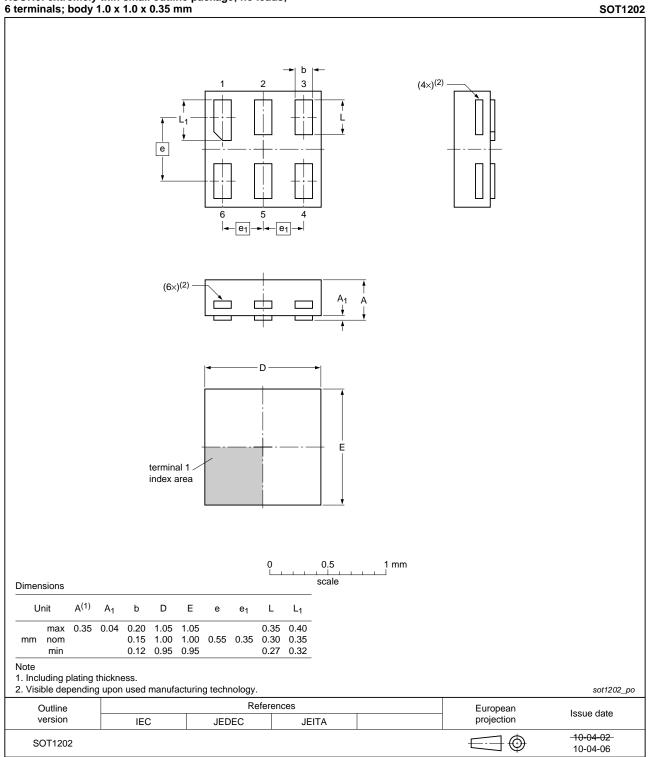
Fig 18. Package outline SOT1115 (XSON6)

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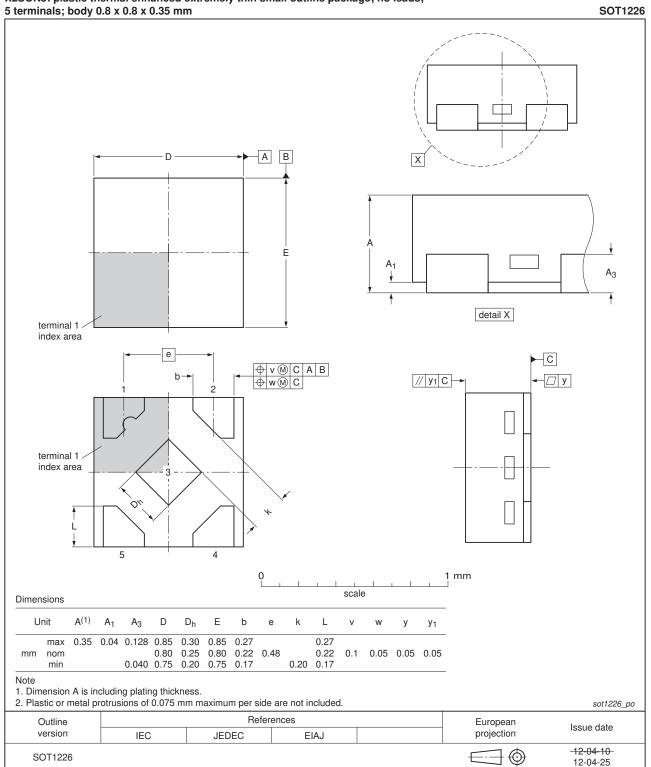


XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 19. Package outline SOT1202 (XSON6)

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X2SON5: plastic thermal enhanced extremely thin small outline package; no leads;

Fig 20. Package outline SOT1226 (X2SON5)

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18. Abbreviations

Table 12.	Abbreviations
Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

19. Revision history

Table 13. Revisio	n history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G132 v.5	20120629	Product data sheet	-	74AUP1G132 v.4
Modifications:	Added type	number 74AUP1G132GX (SC)T1226)	
	 Package out 	line drawing of SOT886 (<mark>Figu</mark>	re 16) modified.	
74AUP1G132 v.4	20111124	Product data sheet	-	74AUP1G132 v.3
Modifications:	 Legal pages 	updated.		
74AUP1G132 v.3	20101029	Product data sheet	-	74AUP1G132 v.2
74AUP1G132 v.2	20090615	Product data sheet	-	74AUP1G132 v.1
74AUP1G132 v.1	20061020	Product data sheet	-	-

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Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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