

54F/74F32 Quad 2-Input OR Gate

General Description

Features

This device contains four independent gates, each of which performs the logic OR function.

■ Guaranteed 4000V minimum ESD protection

Commercial	Military	Package Number	Package Description
74F32PC		N14A	14-Lead (0.300" Wide) Molded Dual-In-Line
	54F32DM (Note 2)	J14A	14-Lead Ceramic Dual-In-Line
74F32SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC
74F32SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ
	54F32FM (Note 2)	W14B	14-Lead Cerpack
	54F32LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C

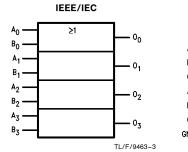
Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

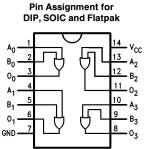
Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

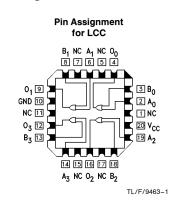
Logic Symbol

Connection Diagrams

TL/F/9463-2







Unit Loading/Fan Out

		54	F/74F
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}
A _n , B _n O _n	Inputs Outputs	1.0/1.0 50/33.3	20 µA/−0.6 mA −1 mA/20 mA

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RRD-B30M75/Printed in U. S. A.

54F/74F32 Quad 2-Input OR Gate

December 1994

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature	-65°C to +150°C
Ambient Temperature under Bias	-55°C to +125°C
Junction Temperature under Bias	-55°C to +175°C
Plastic	-55°C to +150°C
V _{CC} Pin Potential to	
Ground Pin	-0.5V to +7.0V
Input Voltage (Note 2)	-0.5V to $+7.0V$
Input Current (Note 2)	-30 mA to $+5.0$ mA
Voltage Applied to Output	
in HIGH State (with $V_{CC} = 0V$)	
Standard Output	- 0.5V to V _{CC}
TRI-STATE [®] Output	-0.5V to +5.5V
Current Applied to Output	
in LOW State (Max)	twice the rated I _{OL} (mA)
ESD Last Passing Voltage (Min)	4000V
Note 1: Absolute maximum ratings are value be damaged or have its useful life impair these conditions is not implied.	

Recommended Operating Conditions

Free Air Ambient Temperature

Military	-55°C to +125°C
Commercial	0°C to +70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

DC Electrical Characteristics

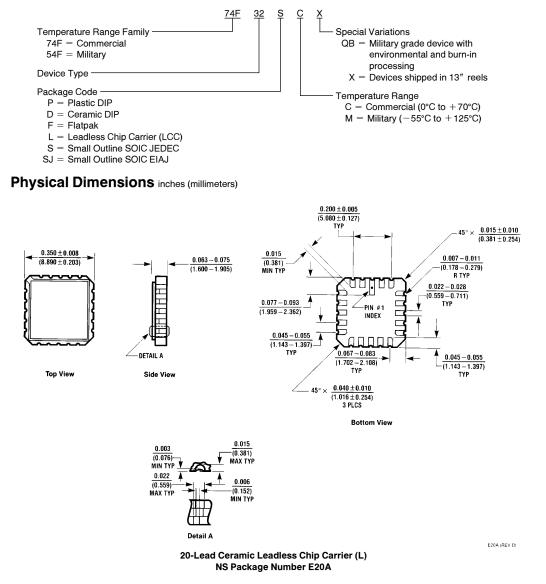
Note 2: Either voltage limit or current limit is sufficient to protect inputs.

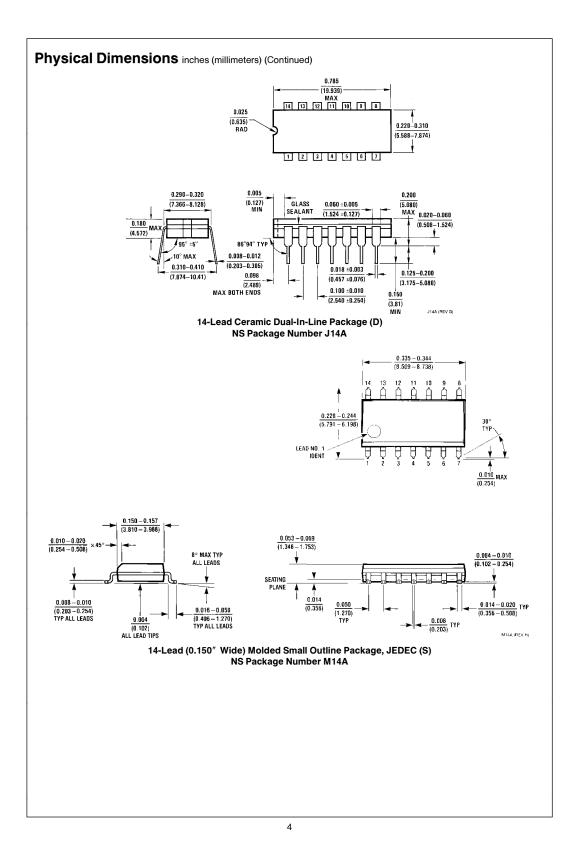
Symbol	Parameter		54F/74F			Units	Vcc	Conditions	
Symbol	Faiane		Min	Тур	Max	Units	•00	Conditions	
V _{IH}	Input HIGH Voltage		2.0			V		Recognized as a HIGH Sign	
V _{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signa	
V _{CD}	Input Clamp Diode Vo	oltage			-1.2	V	Min	$I_{IN} = -18 \text{ mA}$	
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	V	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
IIH	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	$V_{IN} = 2.7V$	
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V _{IN} = 7.0V	
ICEX	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$	
V _{ID}	Input Leakage Test	74F	4.75			V	0.0	$I_{ID} = 1.9 \ \mu A$ All Other Pins Grounded	
I _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All Other Pins Grounded	
IIL	Input LOW Current				-0.6	mA	Max	$V_{IN} = 0.5V$	
I _{OS}	Output Short-Circuit (Current	-60		-150	mA	Max	$V_{OUT} = 0V$	
ICCH	Power Supply Curren	t		6.1	9.2	mA	Max	V _O = HIGH	
I _{CCL}	Power Supply Curren	t		10.3	15.5	mA	Max	$V_{O} = LOW$	

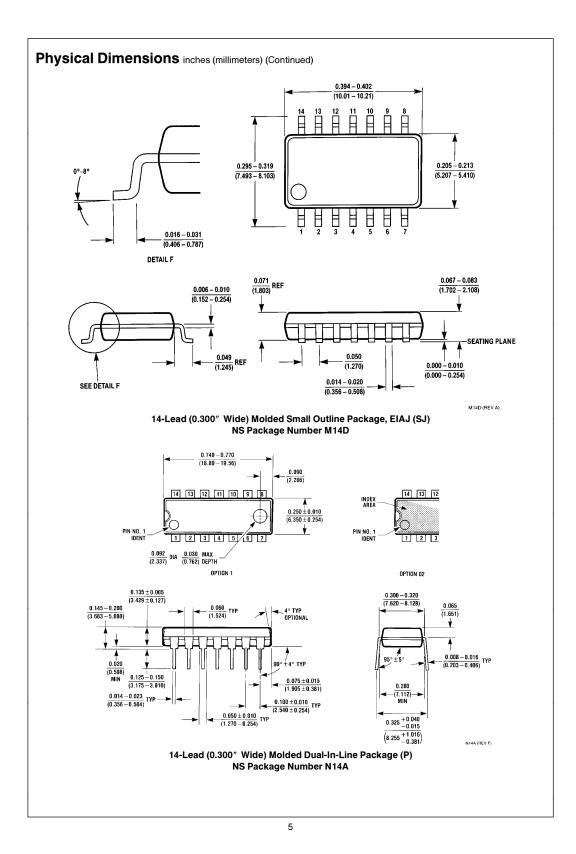
AC Ele	ectrical Charac	teristic	S						
		$74F \\ T_{A} = +25^{\circ}C \\ V_{CC} = +5.0V \\ C_{L} = 50 \text{ pF} $			$54F$ $T_{A}, V_{CC} = Mil$ $C_{L} = 50 \text{ pF}$		74F T _A , V _{CC} = Com C _L = 50 pF		Units
Symbol	Parameter								
		Min	Тур	Мах	Min	Max	Min	Мах	
t _{PLH}	Propagation Delay	3.0	4.2	5.6	3.0	7.5	3.0	6.6	
t _{PHL}	A _n , B _n to O _n	3.0	4.0	5.3	2.5	7.5	3.0	6.3	ns

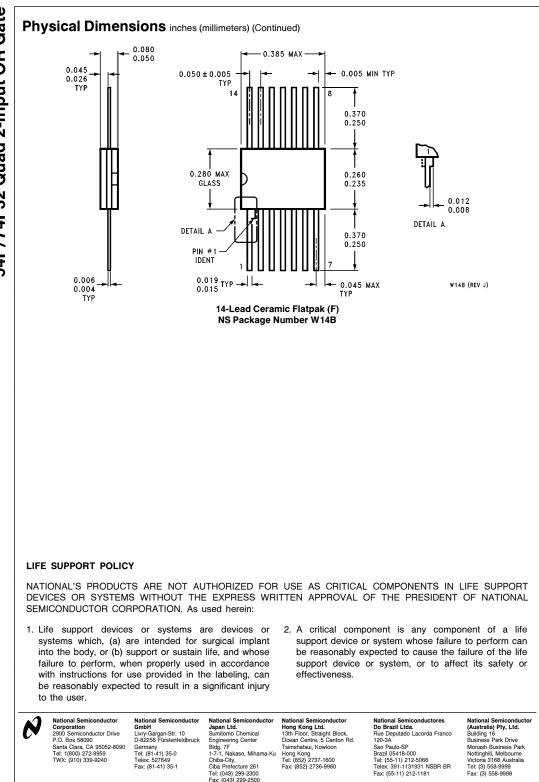
Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:









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54F/74F32 Quad 2-Input OR Gate

×	<u>Design</u>	<u>Purchasing</u>	<u>Quality</u>	<u>Company</u>	<u>Jobs</u>
Products > Military/Aero	space > Log	<u>ic > FAST > 5</u>	4F32		
	X				
54F32 Quad 2-Input OR C	Jate				
Contents					
<u>General Description</u>					

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General Description

This device contains four independent gates, each of which performs the logic OR function.

Features

• Guaranteed 4000V minimum ESD protection

Datasheet

Title	Size (in Kbytes)	Date	View Online	X Download	Receive via Email
154F37 Quad 7-Input OR Gate		9-Dec- 97	<u>View</u> Online	Download	Receive via Email
54F32 Mil-Aero Datasheet MN54F32-X	16 Kbytes		<u>View</u> <u>Online</u>	Download	Receive via Email

Please use Adobe Acrobat to view PDF file(s).

If you have trouble printing, see Printing Problems.

Package Availability, Models, Samples & Pricing

	Packa	ige		Mod	els	Samples	Budgeta	ry Pricing	Std
Part Number	Туре	# pins	Status	SPICE	IBIS	& Electronic Orders	Quantity	\$US each	Pacl Size
54F32LMQB	LCC	20	Full production	N/A	N/A		50+	\$3.3500	tube of 50
54F32DMQB	Cerdip	14	Full production	N/A	N/A		50+	\$1.6000	tube of 25
54F32DM	Cerdip	14	Full production	N/A	N/A		50+	\$7.5000	tube of 25
54F32FMQB	Cerpack	14	Full production	N/A	N/A		50+	\$2.6000	tube of 19
54F32DC	Cerdip	14	Full production	N/A	N/A				tube of N/A
JM38510/33501B2	LCC	20	Full production	N/A	N/A		50+	\$7.3000	tube of 50
JM38510/33501BC	Cerdip	14	Full production	N/A	N/A		50+	\$7.0000	tube of 25
JM38510/33501BD	Cerpack	14	Full production	N/A	N/A		50+	\$7.9000	tube of 19
JM38510/33501SC	Cerdip	14	Full production	N/A	N/A		50+	\$145.0000	tube of 25
JM38510/33501SD	Cerpack	14	Full production	N/A	N/A		50+	\$145.0000	tube of 19
54F32 MW8	wafe	r	Full production	N/A	N/A				N/A

[Information as of 28-Jul-2000]

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